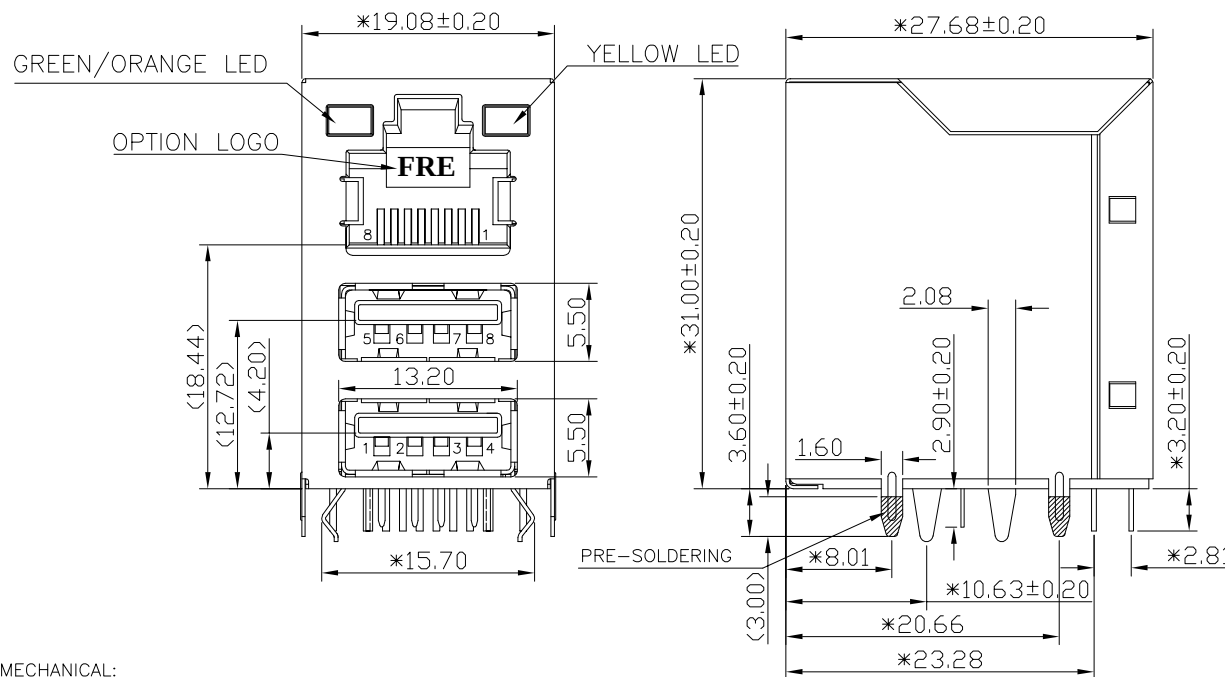


REVISION RECORD				
REV	ECD	DESCRIPTION	DRFT	CHKD



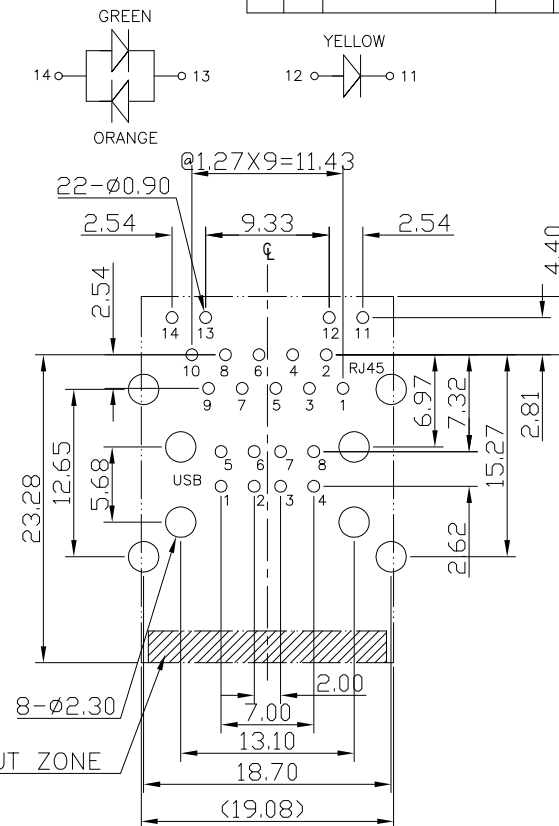
MECHANICAL:

- SHIELD MATERIAL : CARTRIDGE t=0.25mm.
PLATED Nickel 50u" & OPTION PLATED Sn/Pb 100u".
THICK NICKEL.
Tinned grounding tab, H=2.00mm(Min), W=2.10mm(Max).
- HOUSING MATERIAL: PBT+30%GF. THERMOPLASTIC UL94V-0;
- INSERT MATERIAL: PBT+30%GF. THERMOPLASTIC UL94V-0;
- CONTACT MATERIAL: PHOSPHORUS BRONZE t=0.35mm. (C5210EH)
- PLATING : 1. MATING AREA GOLD OVER BRIGHT NICKEL.
2. SOLDER AREA TIN LEAD OVER NICKEL.
3. LED PINS AREA TIN LEAD OVER NICKEL.

RJ45 SPECIFICATIONS:

- ELECTRICAL CHARACTERISTICS:
 - CURRENT RATING: 1.5 AMPERES MAXIMUM.
 - VOLTAGE RATING: 125VAC MAXIMUM.
 - INSULATION RESISTANCE: 500 MEGAOHMS MINIMUM @ 500VDC.
 - DIELECTRIC STRENGTH: 500VAC RMS 60Hz, 1MIN.
- MECHANICAL CHARACTERISTICS:
 - MATING/UNMATING FORCE: 5 LBS MAXIMUM.
 - DURABILITY (MATING/UNMATING CYCLES): 1000 CYCLES.
 - OPERATING TEMPERATURE: 0°C TO +70°C.
STORAGE TEMPERATURE: -40°C TO +85°C.
 - CONNECTOR TO PCB INSERTION FORCE < 8LBS.
 - PLUG TO JACK RETENTION: 10LBS MIN.

PART NUMBER: E828C-22X5VX-B

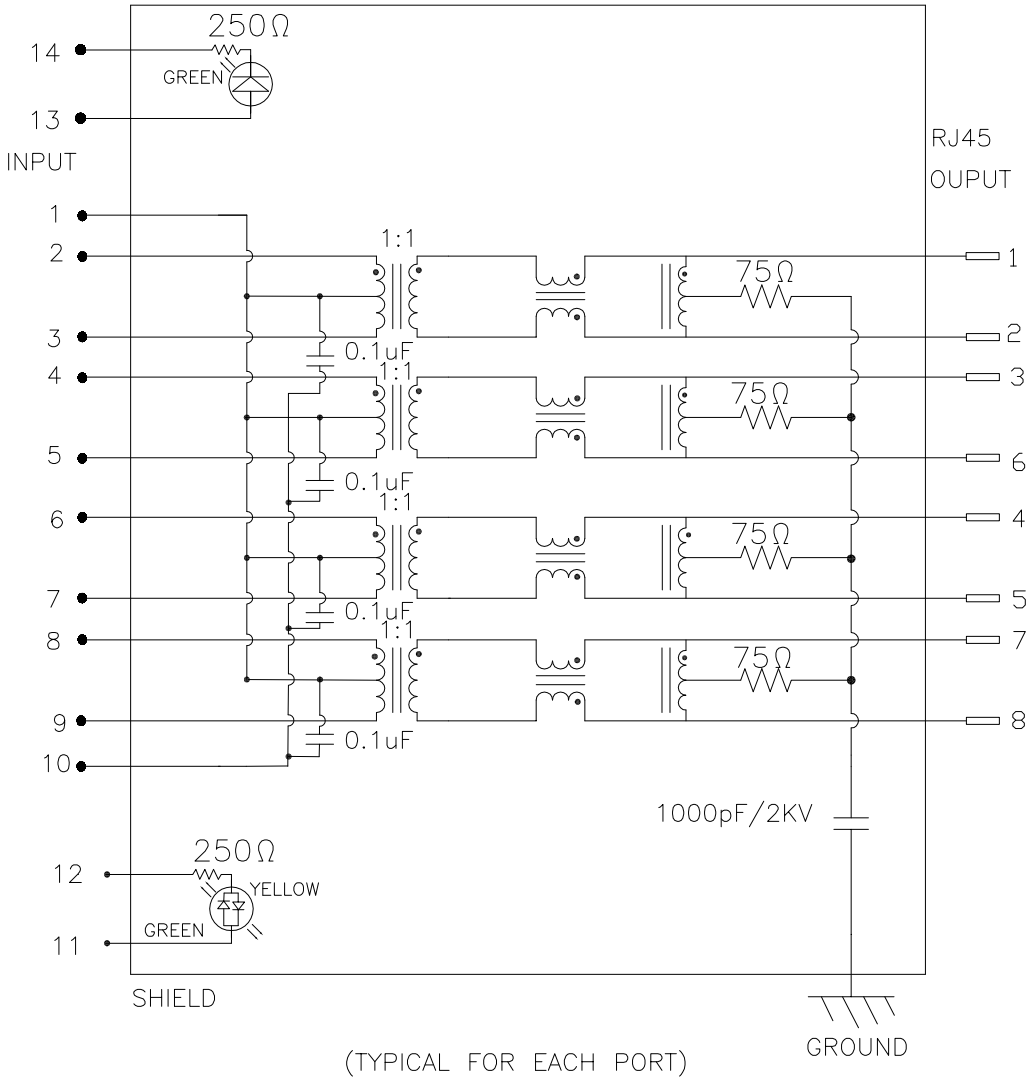
PLATING: 1-3u" 2-6u"
3-15u" 4-30u" 5-50u"B- W/O FRE LOGO
C- W/ FRE LOGOPC Board Layout
Component Side Shown

USB SPECIFICATIONS:

- ELECTRICAL CHARACTERISTICS:
 - CONTACT CURRENT RATING: 1 AMPERES.
 - CONTACT RESISTANCE : 30 mOHMS MAX.
 - DIELECTRIC WITHSTANDING VOLTAGE: 750 VAC RMS 60HZ, 1 MIN.
 - INSULATION RESISTANCE: 1000 MOHMS MIN @500 VDC.
- MECHANICAL CHARACTERISTICS:
 - CONTACT MATING FORCE: 34.3N(3.5kg) MAX.
 - CONNECTOR UNMATING FORCE : 9.8N(1.0kg) MIN.
 - DURABILITY: 1500 CYCLES.
 - OPERATING TEMPERATURE: 0°C TO +70°C.

DETACHED LISTS	MM (INCH)		DFTO YJL	DATE 05/18/05	FULL RISE ELECTRONIC CO., LTD		
	TOLERANCES EXCEPT AS NOTED		CHKDLIUXIN	DATE 2005/5/24			
			MFO	DATE			
			APPVLLUSHENG	DATE 2005/5/26			
	MM .0 ± 0.20 .00 ± 0.15 .000 ± 0.075				TITLE		
			MATERIAL :		RJ45/LED/USB/TRANSFORMER GIGA JACK		
	ANGLES ±		QT'Y :				
			FINISH:		DRAWING NO. GE823081-B		
	THIRD ANGLE PROJECTION		SCALE : 20:1		/PART NO. SEE NOTE		
					DO NOT SCALE DRAWING		
				SHEET 1 OF 2			

REVISION RECORD				
REV	ECD	DESCRIPTION	DRFT	CHKD



- NOTE:
- Transmitter filter & Receiver filter
Type : Balance low pass 100 OHM impedance
Insertion loss : 1~100MHZ-1.0dB MAX
Return : 1~30MHZ-18dB MIN. load 100 OHM
30~60 MHZ-16dB MIN. load 100 OHM
60~80 MHZ-12dB MIN. load 100 OHM
80~100 MHZ-10dB MIN. load 100 OHM
 - Common Mode Rejection
@1~100 MHZ-30dB MIN.
 - Cross Talk
@1~100 MHZ-25dB MIN.
 - Inductance @ 100KHz, 0.1V, 8mA DC BIAS
Input(2-1), Input(4-3): 350 uH MIN.
Input(6-5), Input(8-7): 350 uH MIN.
 - Hipot TEST
Input(1-2) to Output(1-2) : 1500VAC, 60 sec
Input(3-4) to Output(3-6) : 1500VAC, 60 sec
Input(5-6) to Output(4-5) : 1500VAC, 60 sec
Input(7-8) to Output(7-8) : 1500VAC, 60 sec

DETACHED LISTS	MM (INCH)		DFTD YJL	DATE 05/18/05	FULL RISE ELECTRONIC CO., LTD					
	TOLERANCES EXCEPT AS NOTED		CHKDLIUXIN	DATE 2005/5/24						
	MM		MFO	DATE	TITLE RJ45/LED/USB/TRANSFORMER GIGA JACK					
	.0 ± 0.20		APPVLLUSHENG	DATE 2005/5/26						
	.00 ± 0.15		MATERIAL :		DRAWING NO. GE823081-B /PART NO. SEE NOTE					
	.000 ± 0.075		FINISH:					SIZE A3	REV 0 05/18/05	
	ANGLES ±		QT'Y :		DO NOT SCALE DRAWING					
			SCALE : 20:7					SHEET 2 OF 2		
	THIRD ANGLE PROJECTION									