

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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# HVL396C

## Variable Capacitance Diode for VCO

REJ03G0024-0300

Rev.3.00

Mar 21, 2006

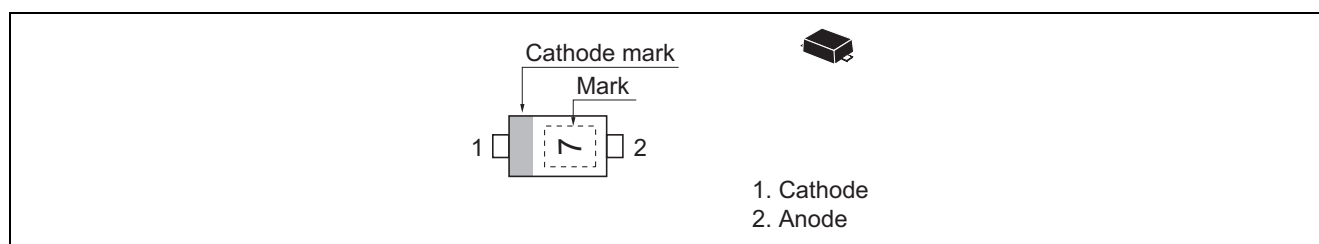
### Features

- High capacitance ratio. ( $n = 2.62$  min)
- Low series resistance. ( $r_s = 0.40 \Omega$  max)
- Extremely small Flat Lead Package (EFP) is suitable for surface mount design.

### Ordering Information

Type No.	Laser Mark	Package Name	Package Code
HVL396C	7	EFP	PXSF0002ZA-A

### Pin Arrangement



## Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	$V_R$	10	V
Junction temperature	$T_j$	125	°C
Storage temperature	$T_{stg}$	-55 to +125	°C

## Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	$I_{R1}$	—	—	10	nA	$V_R = 10\text{ V}$
	$I_{R2}$	—	—	50		$V_R = 10\text{ V}, T_a = 60^\circ\text{C}$
Capacitance	$C_1$	14.6	—	15.8	pF	$V_R = 1\text{ V}, f = 1\text{ MHz}$
	$C_4$	5.20	—	5.80		$V_R = 4\text{ V}, f = 1\text{ MHz}$
Capacitance ratio	n	2.62	—	—	—	$C_1 / C_4$
Series resistance	$r_s$	—	—	0.40	$\Omega$	$V_R = 1\text{ V}, f = 470\text{ MHz}$

Note: For EFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

Main Characteristic

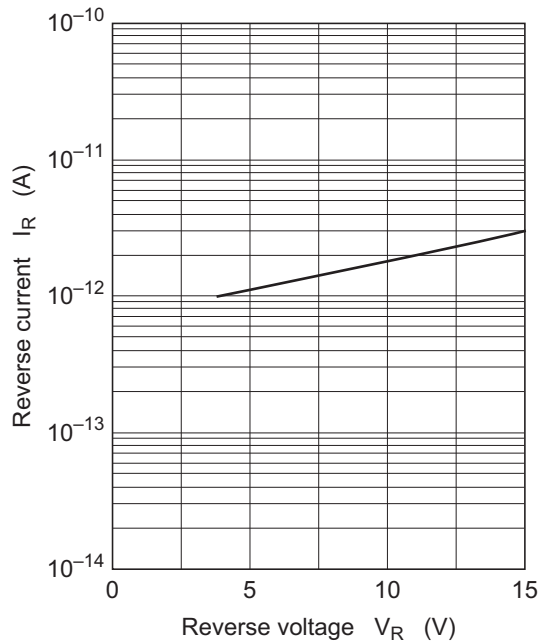


Fig.1 Reverse current vs. Reverse voltage

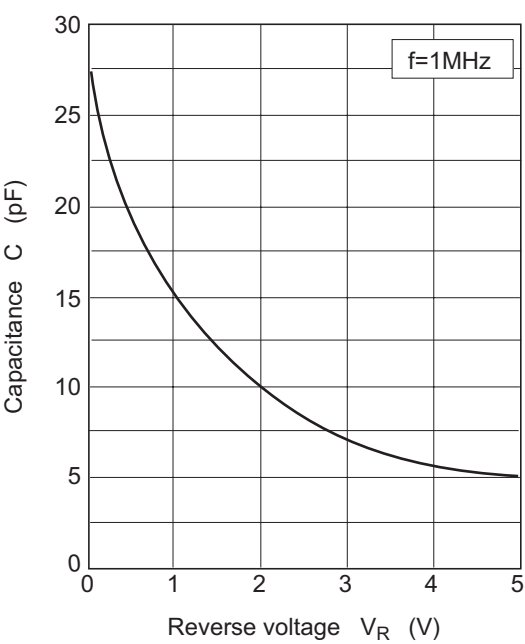


Fig.2 Capacitance vs. Reverse voltage

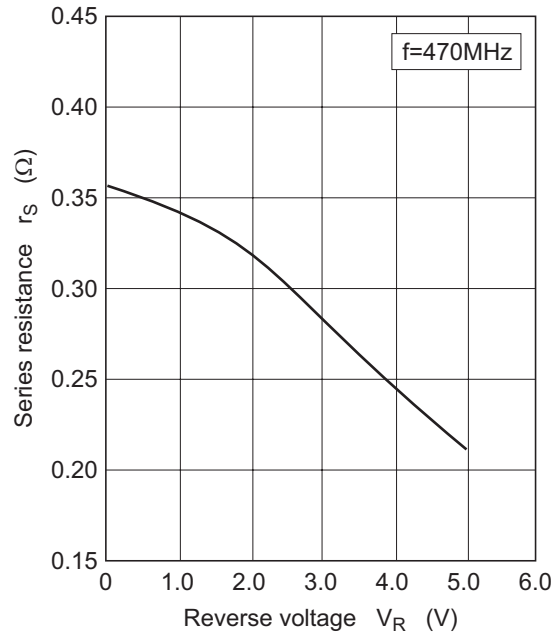
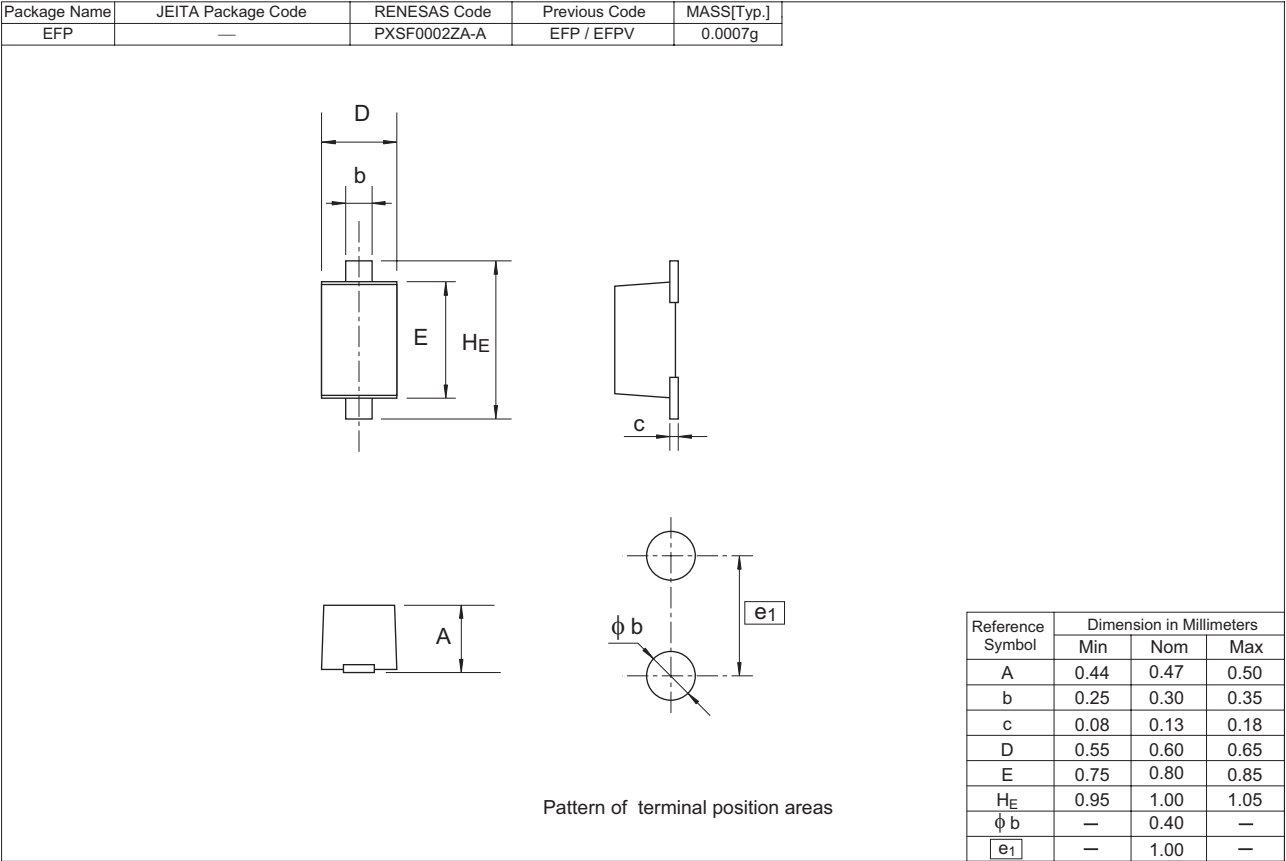


Fig.3 Series resistance vs. Reverse voltage

Package Dimensions



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