

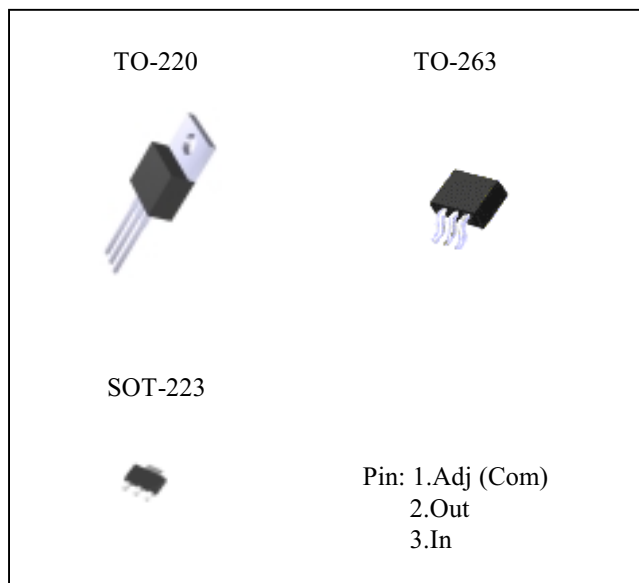
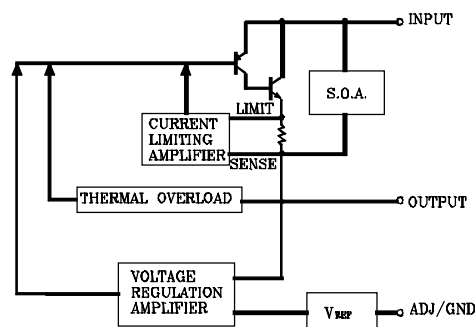
1 Amp Low Dropout Positive Voltage Regulator

The PJ1117 Series are high performance positive voltage regulators designed for use in applications requiring low dropout performance at full rated current. Additionally, the PJ1117 Series provides excellent regulation over variations due to changes in line, load and temperature. Outstanding features include low dropout performance at rated current, fast transient response. The PJ1117 Series are three terminal regulators with fixed and adjustable voltage options available in popular packages.

FEATURES

- Low dropout performance 1.3 V max.
- Full current rating over line and temperature
- Fast transient response
- $\pm 2\%$ Total output regulation over line, load and temperature
- Adjust pin current max 90 μ A over temperature
- Line regulation typical 0.015%.
- Load regulation typical 0.05%.
- Fixed/adjustable output voltage
- TO-220, TO-263 & SOT-223 package

BLOCK DIAGRAM



ORDERING INFORMATION

Device	Operating Temperature	Package
PJ1117CZ PJ1117CZ-1.8V PJ1117CZ-2.5V PJ1117CZ-2.85V PJ1117CZ-3.3V	0 to +70°C	TO-220
PJ1117CM PJ1117CM-1.8V PJ1117CM-2.5V PJ1117CM-2.85V PJ1117CM-3.3V	0 to +70°C	TO-263
PJ1117CW PJ1117CW-1.8V PJ1117CW-2.5V PJ1117CW-2.85V PJ1117CW-3.3V	0 to +70°C	SOT-223

NOTE: Contact factory for additional voltage option.

ABSOLUTE MAXIMUM RATING

Parameter	Symbol	Maximum	Units
Input Voltage	V_{IN}	12	V
Power Dissipation	P_D	Internally Limited	W
Thermal Resistance Junction to Case	θ_{JC}	2.5	°C/W
Thermal Resistance Junction to Ambient	θ_{JA}	50	
Operating Junction Temperature Range	T_J	0 to 125	
Storage Temperature Range	T_{STG}	-65 to 150	°C
Lead Temperature (Soldering) 10 Sec.	T_{LEAD}	260	

1.0 Amp Low Dropout Positive Voltage Regulators

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, Adjust $V_{IN} = 2.75V$ to $12V$ and Adjust $I_O = 10mA$ to $1.0A$

Fixed $V_{IN} = 4.75V$ to $12V$ and Fixed $I_O = 10mA$ to $1.0A$

(For Fixed 5.0V Output Version, $V_{IN} = 7.5V$ to $12V$)

Parameter	Symbol	Test Conditions			Test Limits			Units
		$V_{IN} - V_{OUT}$	I_O	$T_J^{(4)}$	Min	Typ	Max	
Output Voltage ⁽¹⁾	V_O	5V	10mA	25	0.99 V _O	V_O	1.01 V _O	V
Fixed Voltage				Over Temp.	0.98 V _O		1.02 V _O	
Reference Voltage ⁽¹⁾	V_{REF}	5V	10mA	25	1.238	1.250	1.262	V
Adj Voltage				Over Temp.	1.225		1.275	
Line Regulation ⁽¹⁾ ($V_{in} - V_{out} = 3V$)	$REG_{(LINE)}$		10mA	25		0.015	0.2	%
				Over Temp.		0.035		
Load Regulation ⁽¹⁾ ($V_{in} - V_{out} = 3V$)	$REG_{(LOAD)}$			25		0.1	0.3	
				Over Temp.		0.2	0.4	
Dropout Voltage $\Delta V_{REF} = 1\%$	V_D			25		1		V
						1.1	1.3	
Current Limit ($V_{in} - V_{out} = 5V$)	I_{CL}			Over Temp.	1.0	1.1		A
Quiescent Current Fixed Model	I_Q	5V				12	14	mA
Temperature Coefficient	T_c					0.005		%/°C
Adjust Pin Current	I_{ADJ}			25		55		μA
							120	
Adjust Pin Current Change	ΔI_{ADJ}					0.2	5	
Temperature Stability	T_s	5V	500mA	Over Temp.		0.5		%
Minimum Load Current Adjust Model	I_O	5V				5	10	mA
RMS Output Noise ⁽²⁾	V_N			25		0.003		%V _O
Ripple Rejection Ratio ⁽³⁾	R_A	5V	1.0A	Over Temp.	60	72		dB

(1)Low duty cycle pulse testing with Kelvin connections required.

(2)Bandwidth of 10Hz to 10KHz.

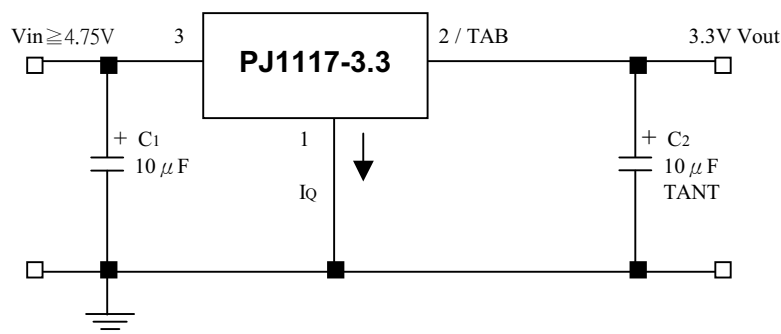
(3)120Hz input ripple (C_{ADJ} for ADJ) = 25 μF .

(4)Over Temp.-over specified operating junction temperature range.

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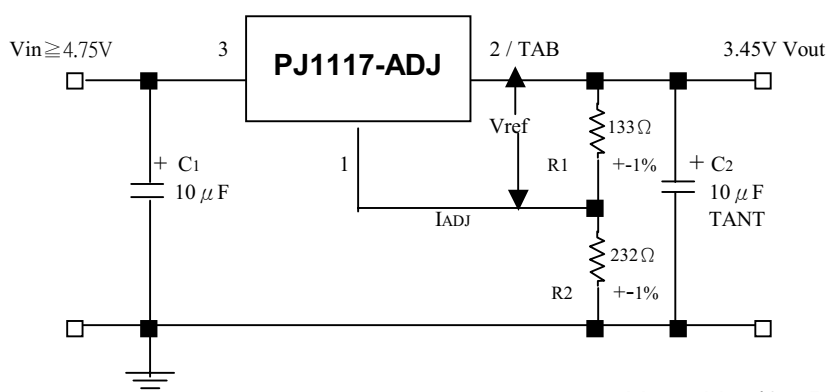
ELECTRICAL CHARACTERISTICS

FIXED VOLTAGE REGULATOR (1)(2)



- (1) C₁ NEEDED IF DEVICE IS FAR FROM FILTER CAPACITORS
(2) C₂ REQUIRED FOR STABILITY

ADJUSTABLE VOLTAGE REGULATOR (1)(2)



$$V_{out} = V_{ref} (1 + R_2/R_1) + I_{adj}R_2$$

- (1) C₁ NEEDED IF DEVICE IS FAR FROM FILTER CAPACITORS
(2) C₂ REQUIRED FOR STABILITY

RECOMMENDED CIRCUIT FOR ACTIVE SCSI BUS TERMINATOR

