

Silicon PNP Power Transistors

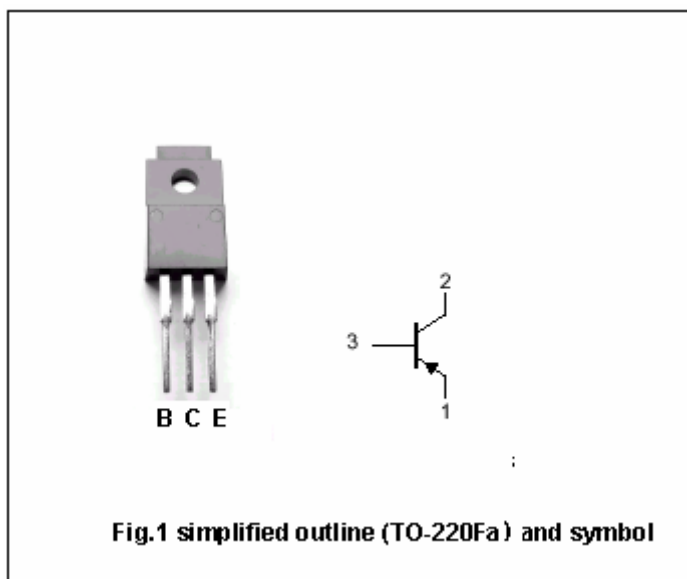
2SB1370

DESCRIPTION

- With TO-220Fa package
- $P_C=2W(T_a=25^\circ) / 30W(T_C=25^\circ)$
- Low collector saturation voltage
- Wide area of safe operation

PINNING

PIN	DESCRIPTION
1	Emitter
2	Collector
3	Base



Absolute maximum ratings($T_a=25^\circ$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	-60	V
V_{CEO}	Collector -emitter voltage	Open base	-60	V
V_{EBO}	Emitter-base voltage	Open collector	-5	V
I_C	Collector current		-3	A
I_{CM}	Collector current-peak		-6	A
P_C	Collector power dissipation	$T_a=25^\circ$	2.0	W
		$T_C=25^\circ$	30	
T_j	Junction temperature		150	$^\circ$
T_{stg}	Storage temperature		-55~150	$^\circ$

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CHARACTERISTICS

T_j=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-1mA; I _B =0	-60			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =-50μA; I _E =0	-60			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =-50μA; I _C =0	-5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-2A; I _B =-0.2A			-1.5	V
V _{BEsat}	Base-emitter saturation voltage	I _C =-2A; I _B =-0.2A			-1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =-60V; I _E =0			-10	μA
I _{EBO}	Emitter cut-off current	V _{EB} =-4V; I _C =0			-10	μA
h _{FE}	DC current gain	I _C =-0.5A; V _{CE} =-5V	100		320	
f _T	Transition frequency	I _C =-0.5A; V _{CE} =-5V		15		MHz
C _{OB}	Collector output capacitance	f=1MHz; V _{CB} =10V		80		pF

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PACKAGE OUTLINE



Fig.2 Outline dimensions (unindicated tolerance: ± 0.15 mm)