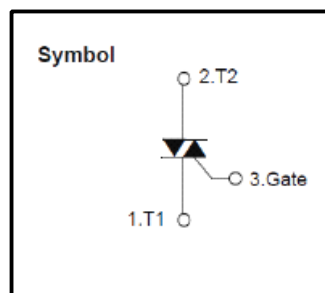


Bi-Directional Triode Thyristor

Features

- Repetitive Peak off -State Voltage:600V
- R.M.S On-State Current(IT(RMS))=8A)
- High Commutation dv/dt
- Isolation Voltage (V_{ISO}=1500V AC)



General Description

This device fully isolated package suitable for AC switching application, phase control application such as fan speed and temperature modulation control, lighting control and static switching relay. This device is approved to comply with applicable requirements by Underwriters Laboratories Inc.



Absolute Maximum Ratings (T_J=25°C unless otherwise specified)

symbol	Parameter	condition	Ratings	Units
V _{DRM}	Repetitive Peak Off-State Voltage		600	V
I _{T(RMS)}	R.M.S On-State Current	T _c =89°C	8.0	A
I _{TSM}	Surge On-State Current	One Cycle, 50Hz/60Hz, Peak, Non-Repetitive	80/88	A
I ² t	I ² t		32	A ² s
P _{GM}	Peak Gate Power Dissipation		5.0	W
P _{G(AV)}	Average Gate Power dissipation		0.5	W
I _{GM}	Peak Gate Current		2.0	A
V _{GM}	Peak Gate Voltage		10	V
V _{ISO}	Isolation Breakdown voltage(R.M.S.)	A.C 1 minute	1500	V
T _J	Operating Junction Temperature		-40~125	°C
T _{STG}	Storage Temperature		-40~150	°C

Thermal Characteristics

Symbol	Parameter	Value	Units
R _{θjc}	Thermal Resistance Junction to Case	3.7	°C/W

Electrical Characteristics($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Items		conditions	Ratin			Unit
				Min	Typ	Max	
I_{DRM}	Repetitive Peak Off-State Current		$V_D=V_{\text{DRM}}$, Single Phase, Half Wave $T_J=125^\circ\text{C}$	-	-	2.0	mA
V_{TM}	Peak On-State Voltage		$I_T=12\text{A}$, Inst. Measurement	-	-	1.4	V
I_{GT1}^+	I	Gate Trigger Current	$V_D=6\text{V}, R_L=10\Omega$	-	-	30	mA
I_{GT1}^-	II			-	-	30	
I_{GT1}^-	III			-	-	30	
V_{GT1}^+	I	Gate Trigger Voltage	$V_D=6\text{V}, R_L=10\Omega$	-	-	1.5	V
V_{GT1}^-	II			-	-	1.5	
V_{GT3}^-	III			-	-	1.5	
V_{GD}	Non-Trigger Gate Voltage		$T_J=125^\circ\text{C}, V_D=1/2V_{\text{DRM}}$	0.2	-	-	V
(dv/dt)c	Critical Rate of Rise Off-State Voltage at Commutation		$T_J=125^\circ\text{C}, [di/dt]_c=-4.0\text{A/ms}, V_D=2/3V_{\text{DRM}}$	10	-	-	V/ μs
I_{H}	Holding Current			-	15	-	mA

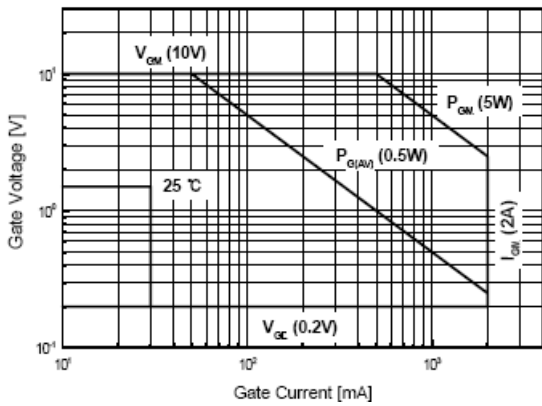


Fig1. Gate Characteristics

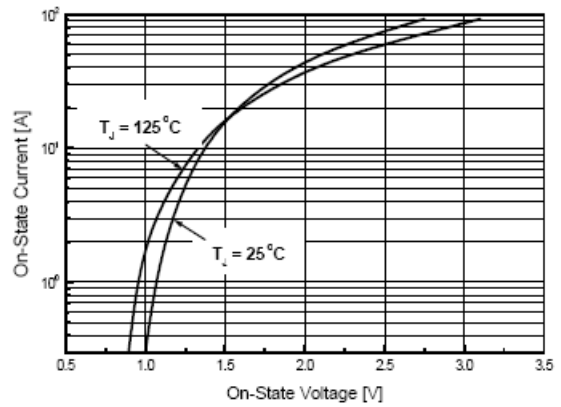


Fig.2 On-State Voltage

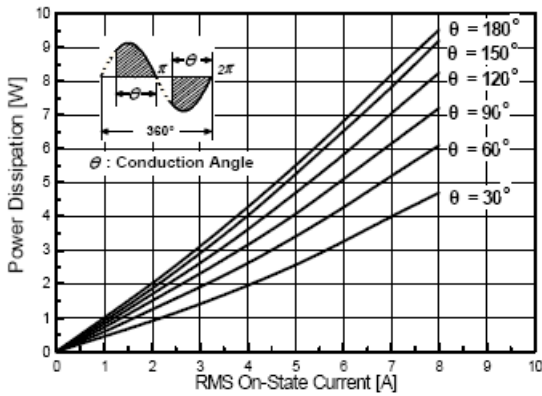


Fig.3 On State Current vs. Maximum Power Dissipation

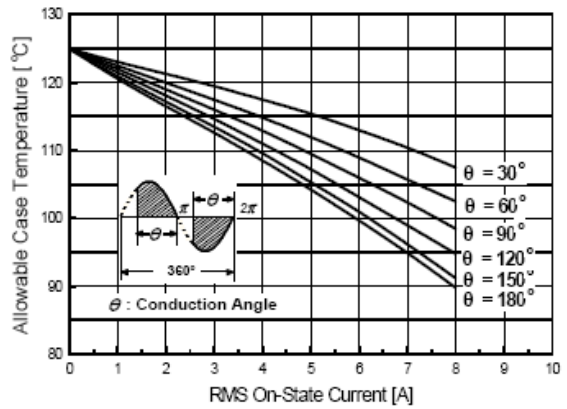


Fig.4 On State Current vs. Allowable Case Temperature

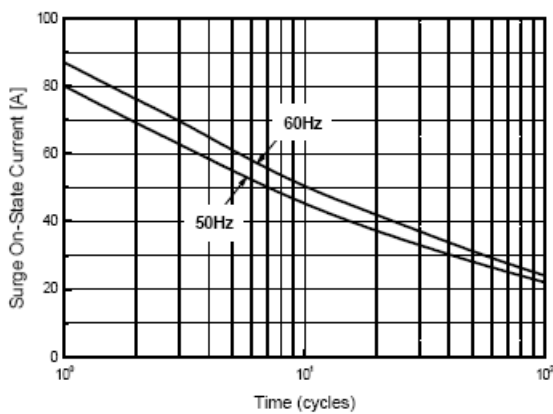


Fig.5 Surge On-State Current Rating (Non-Repetitive)

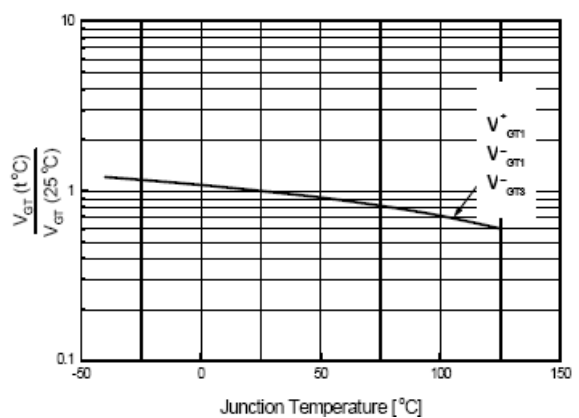


Fig.6 Gate Trigger Voltage vs. Junction Temperature

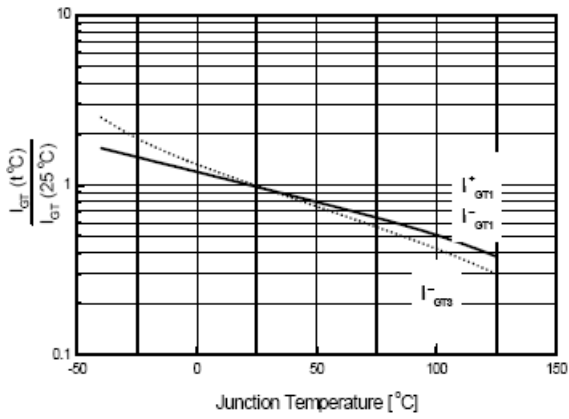


Fig.7 Gate Trigger Current vs. Junction Temperature

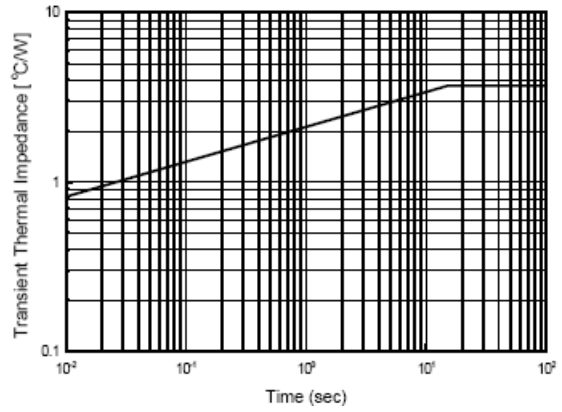


Fig.8 Transient Thermal Impedance

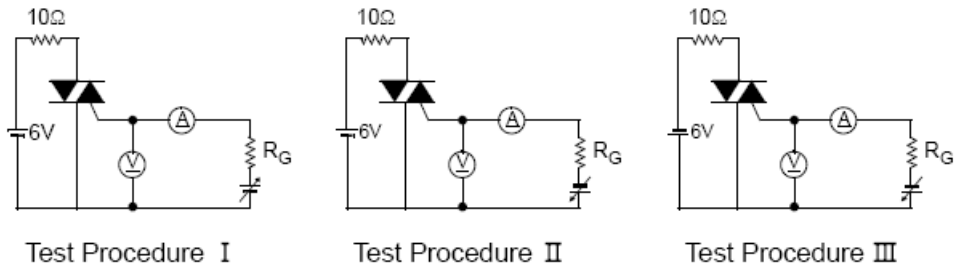


Fig.9 Gate Trigger Characteristics Test Circuit

DPAK Package Dimension

Unit:mm

