

2PC4081

NPN general-purpose transistor Rev. 05 — 25 November 2004

Product data sheet



1.1 General description

NPN transistor in a SOT323 (SC-70) plastic package. The PNP complement is 2PA1576.

1.2 Features

- Low current (max. 150 mA)
- Low voltage (max. 50 V).

1.3 Applications

- General-purpose switching
- Small signal amplification.

Pinning information 2.

Table 1: **Pinning**

Pin	Description	Simplified outline	Symbol
1	base		
2	emitter	3	3
3	collector	1 2	1 —
			sym021

Ordering information 3.

Table 2: **Ordering information**

Type number	Package				
	Name	Description	Version		
2PC4081Q	SC-70	plastic surface mounted package; 3 leads	SOT323		
2PC4081R					
2PC4081S					



NPN general-purpose transistor

4. Marking

Table 3: Marking codes

Type number	Marking code [1]
2PC4081Q	Z*Q
2PC4081R	Z*R
2PC4081S	Z*S

^[1] * = -: made in Hong Kong.

5. Limiting values

Table 4: Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter	-	60	V
V_{CEO}	collector-emitter voltage	open base	-	50	V
V_{EBO}	emitter-base voltage	open collector	-	7	V
I _C	collector current (DC)		-	150	mA
I _{CM}	peak collector current		-	200	mA
I _{BM}	peak base current		-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1] -	200	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-65	+150	°C

^[1] Transistor mounted on an FR4 printed-circuit board, single-sided copper, tin-plated and standard footprint.

6. Thermal characteristics

Table 5: Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		<u>[1]</u> -	-	625	K/W

^[1] Transistor mounted on an FR4 printed-circuit board, single-sided copper, tin-plated and standard footprint.

^{* =} t: made in Malaysia.



7. Characteristics

Table 6: Characteristics

 T_{amb} = 25 °C; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	$I_E = 0 A; V_{CB} = 30 V$	-	-	100	nA
		$I_E = 0 \text{ A}; V_{CB} = 30 \text{ V};$ $T_j = 150 \text{ °C}$	-	-	5	μΑ
I _{EBO}	emitter-base cut-off current	$I_C = 0 A; V_{EB} = 4 V$	-	-	100	nA
h _{FE}	DC current gain	$I_C = 1 \text{ mA}; V_{CE} = 6 \text{ V}$				
	2PC4081Q		120	-	270	
	2PC4081R		180	-	390	
	2PC4081S		270	-	560	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 50 \text{ mA}; I_B = 5 \text{ mA}$	[1] -	-	400	mV
C _c	collector capacitance	$I_E = i_e = 0 \text{ A};$ $V_{CB} = 12 \text{ V}; f = 1 \text{ MHz}$	-	2	3.5	pF
f _T	transition frequency	$I_C = 2 \text{ mA}; V_{CE} = 12 \text{ V};$ f = 100 MHz	100	-	-	MHz

^[1] Pulse test: $t_p \le 300 \ \mu s; \ \delta \le 0.02.$

2PC4081

8. Package outline

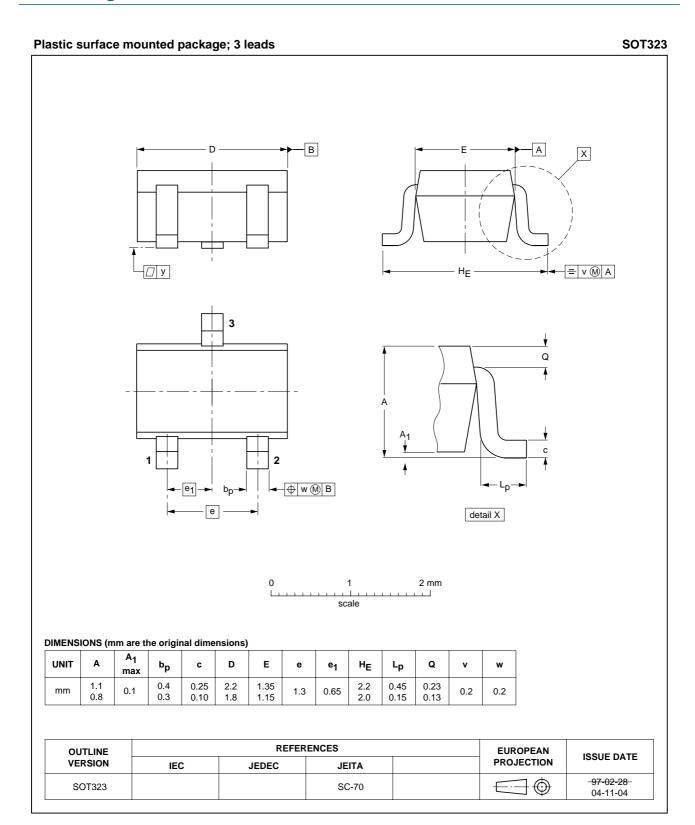


Fig 1. Package outline SOT323 (SC-70)

2PC4081 **Philips Semiconductors**



NPN general-purpose transistor

Revision history

Table 7: **Revision history**

Document ID	Release date	Data sheet status	Change notice	Doc. number	Supersedes
2PC4081_5	20041125	Product data sheet	-	9397 750 14084	2PC4081_4
Modifications:		t of this data sheet has be n standard of Philips Sem	•	comply with the new	v presentation and
	 Section 1.2 	2: maximum low current a	and maximum low v	oltage upgraded	
	• <u>Table 4</u> : V ₀	_{CBO} value changed to 60	V		
	• <u>Table 4</u> : V ₀	CEO value changed to 50	V		
	 <u>Table 4</u>: V_E 	_{EBO} value changed to 7 V	•		
	• <u>Table 4</u> : I _C	value changed to 150 m.	A.		
2PC4081_4	19990408	Product specification	-	9397 750 05524	2PC4081_3
2PC4081_3	19970704	Product specification	-	9397 750 02584	2PC4081_2
2PC4081_2	19931213	n.a.	-	n.a.	n.a.

NPN general-purpose transistor



Level	Data sheet status [1]	Product status [2] [3]	Definition
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
II	Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
III	Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Relevant changes will be communicated via a Customer Product/Process Change Notification (CPCN).

- [1] Please consult the most recently issued data sheet before initiating or completing a design.
- [2] The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.
- [3] For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

11. Definitions

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information — Applications that are described herein for any of these products are for illustrative purposes only. Philips Semiconductors make no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

12. Disclaimers

Life support — These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips Semiconductors customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips Semiconductors for any damages resulting from such application.

Right to make changes — Philips Semiconductors reserves the right to make changes in the products - including circuits, standard cells, and/or software - described or contained herein in order to improve design and/or performance. When the product is in full production (status 'Production'), relevant changes will be communicated via a Customer Product/Process Change Notification (CPCN). Philips Semiconductors assumes no responsibility or liability for the use of any of these products, conveys no license or title under any patent, copyright, or mask work right to these products, and makes no representations or warranties that these products are free from patent, copyright, or mask work right infringement, unless otherwise specified.

13. Contact information

For additional information, please visit: http://www.semiconductors.philips.com
For sales office addresses, send an email to: sales.addresses@www.semiconductors.philips.com

NPN general-purpose transistor

14. Contents

1	Product profile
1.1	General description
1.2	Features
1.3	Applications
2	Pinning information 1
3	Ordering information
4	Marking 2
5	Limiting values 2
6	Thermal characteristics 2
7	Characteristics 3
8	Package outline 4
9	Revision history 5
10	Data sheet status 6
11	Definitions 6
12	Disclaimers 6
13	Contact information 6



All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.

Date of release: 25 November 2004 Document number: 9397 750 14084

