# **HLMP-EGxx, HLMP-ELxx and HLMP-EHxx**

T-1¾ (5mm) Extra High Brightness AllnGaP LED Lamps



# **Data Sheet**

# Description

These Precision Optical Performance AllnGaP LEDs provide superior light output for excellent readability in sunlight and are extremely reliable. AllnGaP LED technology provides extremely stable light output over long periods of time. Precision Optical Performance lamps utilize the aluminum indium gallium phosphide (AllnGaP) technology.

These LED lamps are untinted, T-1¾ packages incorporating second generation optics producing well defined spatial radiation patterns at specific viewing cone angles.

### **Benefits**

- Superior performance for outdoor environments
- Suitable for auto-insertion onto PC board

### **Features**

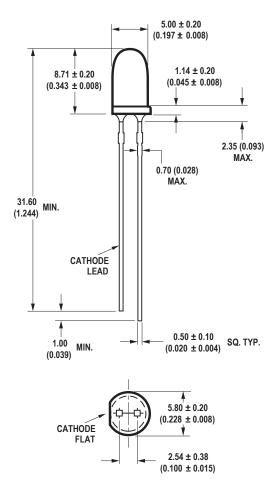
- Viewing angle: 15°, 23°, 30°
- High luminous output
- Colors:
  - 590nm Amber 615nm Red-Orange 626nm Red
- Package options:
   With or without lead standoff

## **Applications**

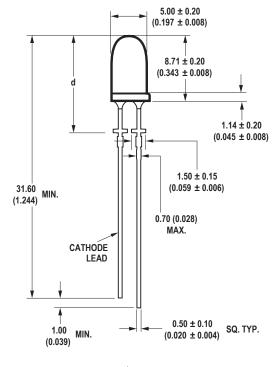
- Traffic management:
  - Traffic signals
  - Pedestrian signals
  - Work zone warning lights
  - Variable message signs
- Commercial outdoor advertising
  - Signs
  - Marquees

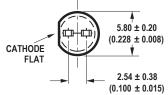
# **Package Dimension**

Α



В





d

12.42 ± 0.25

 $(0.489 \pm 0.010)$ 

11.59 ± 0.25

 $(0.446 \pm 0.010)$ 

11.96 ± 0.25 (0.471 ± 0.010)

PART NO.

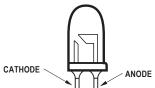
HLMP-XX13

HLMP-XX23

HLMP-XX37

#### NOTES:

- 1. ALL DIMENSIONS ARE IN MILLIMETERS (INCHES).
- 2. LEADS ARE MILD STEEL, SOLDER DIPPED.
- 3. TAPERS SHOWN AT TOP OF LEADS (BOTTOM OF LAMP PACKAGE) INDICATE AN EPOXY MENISCUS THAT MAY EXTEND ABOUT 1 mm (0.040 in.) DOWN THE LEADS.
- 4. RECOMMENDED PC BOARD HOLE DIAMETERS:
  - LAMP PACKAGE A WITHOUT STAND-OFFS: FLUSH MOUNTING AT BASE OF LAMP PACKAGE = 1.143/1.067 (0.044/0.042).
  - LAMP PACKAGE B WITH STAND-OFFS: MOUNTING AT LEAD STAND-OFFS = 0.965/0.889 (0.038/0.035).
- 5. FOR DOME HEIGHTS ABOVE LEAD STAND-OFF SEATING PLANE, d, LAMP PACKAGE B, SEE TABLE.
- 6. FOR IDENTIFICATION OF POLARITY AFTER THE LEADS ARE TRIMMED OFF, PLEASE REFER TO THE ILLUSTRATION BELOW:



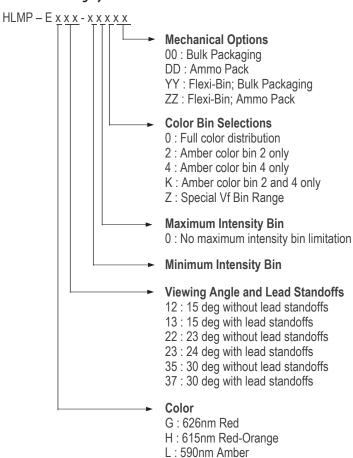
7. MAJOR HEAT PATH IS THROUGH THE ANODE LEAD

## **Device Selection Guide**

Typical Viewing	Color and Dominant Wavelength (nm), Typ. <sup>[3]</sup>	Lamps without Standoffs on Leads	Lamps with Standoffs on Leads	Luminous Intensity Iv (mcd) <sup>[1,2,5]</sup> @ 20mA	
Angle 2θ½ (Deg) [4]		(Outline Drawing A)	(Outline Drawing B)	Min.	Max.
15°	Amber 590	HLMP-EL12-VY0DD	HLMP-EL13-VY0DD	4200	12000
		HLMP-EL12-XYKDD		7200	12000
	Red-Orange 615	HLMP-EH12-VY0DD	HLMP-EH13-VY0DD	4200	12000
	Red 626	HLMP-EG12-VY0DD	HLMP-EG13-VY0DD	4200	12000
		HLMP-EG12-WX0DD		5500	9300
23°	Amber 590	HLMP-EL22-UXKDD	HLMP-EL23-UXKDD	3200	9300
		HLMP-EL22-UX0DD	HLMP-EL23-UX0DD	3200	9300
		HLMP-EL22-VWKDD		4200	7200
	Red-Orange 615	HLMP-EH22-TW0DD	HLMP-EH23-TW0DD	2500	7200
	Red 626	HLMP-EG22-UX0DD	HLMP-EG23-UX0DD	3200	9300
		HLMP-EG22-VW0DD		4200	7200
30°	Amber 590	HLMP-EL35-TW0DD	HLMP-EL37-TW0DD	2500	7200
		HLMP-EL35-TWKDD	HLMP-EL37-TWKDD	2500	7200
		HLMP-EL35-UVKDD		3200	5500
	Red-Orange 615	HLMP-EH35-SVODD	HLMP-EH37-SV0DD	1900	5500
	Red 626	HLMP-EG35-TW0DD	HLMP-EG37-TW0DD	2500	7200
		HLMP-EG35-UV0DD		3200	5500

- The luminous intensity is measured on the mechanical axis of the lamp package.
   The optical axis is closely aligned with the package mechanical axis.
   Dominant wavelength, λd, is derived from the CIE Chromaticity Diagram and represents the color of the lamp.
   θ½ is the off-axis angle where the luminous intensity is half the on-axis intensity.
- 5. Tolerance for each intensity bin limit is  $\pm$  15%.

# **Part Numbering System**



# Absolute Maximum Ratings at $T_A = 25$ °C

DC Forward Current [1]	50 mA	
Peak Pulsed Forward Current	100 mA	
Average Forward Current	30 mA	
Reverse Voltage (Ir = 100 μA)	5 V	
Operating Temperature	-40°C to +100°C	
Storage Temperature	−40°C to +120°C	
Wave Soldering Temperature <sup>[2]</sup>	250°C for 3 seconds	
Solder Dipping Temperature <sup>[2]</sup>	260°C for 5 seconds	

#### Notes:

- 1. Derate linearly as shown in Figure 4.
- 2. 1.59mm (0.060 in.) below body

# Electrical/ Optical Characteristics at $T_A = 25$ °C

Parameter	Symbol	Minimum	Average	Maximum	Units	Test Condition
Forward Voltage						$I_F = 20 \text{mA}$
Amber			2.20			
Red	$V_{F}$	1.80	2.10	2.40	V	
Red-Orange			2.00			
Peak Wavelength						$I_F = 20 \text{mA}$
Amber			590			
Red	$\lambda_{PEAK}$		626		nm	
Red-Orange			615			
Dominant Wavelength [1]						$I_F = 20 \text{mA}$
Amber		584.5		594.5		
Red	$\lambda_{d}$	620.0		630.0	nm	
Red-Orange		612.0		621.7		
Reverse Voltage	$V_{R}$	5			V	$I_R = 100 \mu A$
Spectral Halfwidth	$\Delta\lambda_{1/2}$		17		nm	$I_F = 20 \text{ mA}$
Capacitance	C		40		pF	$V_F = 0$ , $f = 1$ MHz
Thermal Resistance	Rθ <sub>J-PIN</sub>		240		°C/W	LED Junction-to-Anode Lead
Luminous Efficacy [2]						Emitted Luminous Flux/Emitted Radiant
Amber			480			Flux
Red	ηγ		150		lm/W	
Red-Orange			260			

### Notes:

- 1. The dominant wavelength, λd is derived from the CIE Chromaticity Diagram referenced to Illuminant E. Tolerance for each color of dominant wavelength is +/- 0.5nm.
- 2. The radiant intensity, le in watts per steradian, maybe found from the equation le =  $Iv / \eta V$  where Iv is the luminous intensity in candela and  $\eta V$  is the luminous efficacy in lumens/watt.

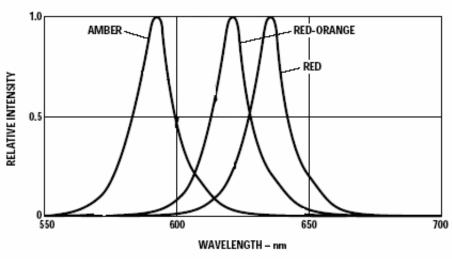


Figure 1. Relative intensity vs. peak wavelength

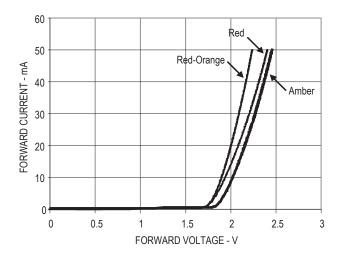


Figure 2. Forward current vs. forward voltage

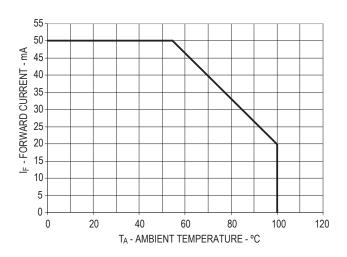


Figure 4. Maximum forward current vs. ambient temperature

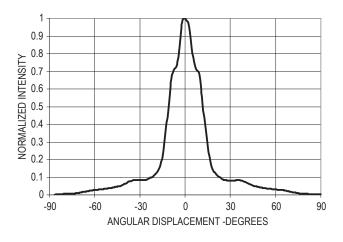


Figure 6. Representative spatial radiation pattern for 23  $^{\circ}$  viewing angle lamps

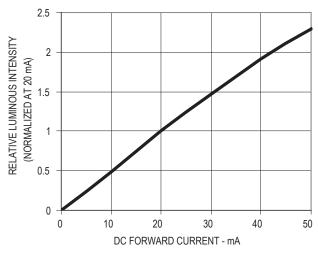


Figure 3. Relative luminous intensity vs. forward current

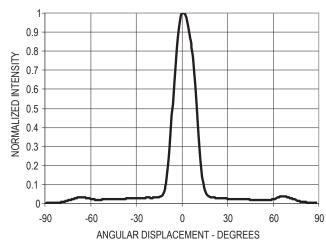


Figure 5. Representative spatial radiation pattern for 15° viewing angle lamps

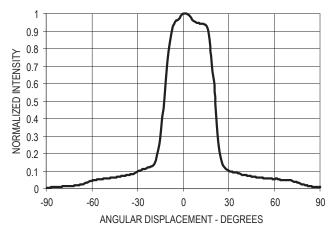


Figure 7. Representative spatial radiation pattern for 30° viewing angle lamps

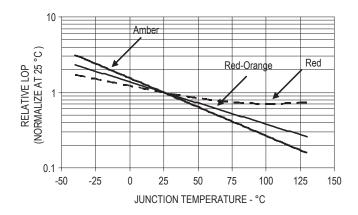


Figure 8. Relative light output vs. junction temperature

# Intensity Bin Limits (mcd at 20mA)

Bin Name	Minimum	Maximum
R	1500	1900
S	1900	2500
T	2500	3200
U	3200	4200
V	4200	5500
W	5500	7200
Х	7200	9300
Υ	9300	12000
Z	12000	16000
1	16000	21000
2	21000	27000

Tolerance for each bin limit is  $\pm$  15%

# Forward Voltage Bin Limits (V at 20mA)

Bin Name	Minimum	Maximum
VD	1.8	2.0
VA	2.0	2.2
VB	2.2	2.4

Tolerance for each bin limit is  $\pm 0.05V$ 

# Amber Color Bin Limits (nm at 20mA)

Bin Name	Minimum	Maximum
1	584.5	587.0
2	587.0	589.5
4	589.5	592.0
6	592.0	594.5

Tolerance for each bin limit is  $\pm 0.5$ nm

Note: Bin categories are established for classification of products. Products may not available in all bin categories.

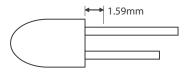
#### **Precautions:**

## **Lead Forming:**

- The leads of an LED lamp may be performed or cut to length prior to insertion and soldering on PC board.
- If lead forming is required before soldering, care must be taken to avoid any excessive mechanical stress that induced into the LED package. Otherwise, cut the leads to applicable length after soldering process at room temperature. The solder joint formed will absorb the mechanical stress, due to the lead cutting, from traveling to the LED chip die attach and wirebond.
- For better control, it is recommended to use proper tool to precisely form and cut the leads to applicable length rather than doing it manually.

#### **Soldering condition:**

- Care must be taken during PCB assembly and soldering process to prevent damage to the LED component.
- The closest manual soldering distance of the soldering heat source (soldering iron's tip) to the body is 1.59mm. Soldering the LED closer than 1.59mm might damage the LED.



• Recommended soldering condition:

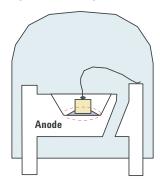
	Wave Soldering	Manual Solder Dipping
Pre-heat temperature	105 °C Max.	-
Preheat time	30 sec Max	-
Peak temperature	250 °C Max.	260 °C Max.
Dwell time	3 sec Max.	5 sec Max

 Wave soldering parameter must be set and maintain according to the recommended temperature and dwell time. Customer is advised to daily check on the soldering profile to ensure that the soldering profile is always conforming to recommended soldering condition.

#### Note:

- PCB with different size and design (component density) will have different heat mass (heat capacity). This might cause a change in temperature experienced by the board if same wave soldering setting is used. So, it is recommended to re-calibrate the soldering profile again before loading a new type of PCB.
- Avago Technologies' high brightness LED are using high efficiency LED die with single wire bond as shown below. Customer is advised to take extra precaution during wave soldering to ensure that the maximum wave temperature is not exceeding 250°C. Over-stressing the LED during soldering process might cause premature failure to the LED due to delamination.

## Avago Technologies LED configuration



**AllnGaP Device** 

Note: Electrical connection between bottom surface of LED die and the lead frame material through conductive paste of solder.

- If necessary, use fixture to hold the LED component in proper orientation with respect to the PCB during soldering process.
- At elevated temperature, the LED is more susceptible to mechanical stress. Therefore, PCB must allowed to cool down to room temperature prior to handling, which includes removal of jigs, fixtures or pallet.
- Special attention must be given to board fabrication, solder masking, surface platting and lead holes size and component orientation to assure the solderability.
- Recommended PC board plated through holes size for LED component leads.

LED component lead size	Diagonal	Plated through hole diameter
0.457 x 0.457 mm	0.646 mm	0.976 to 1.078 mm
(0.018 x 0.018 inch)	(0.025 inch)	(0.038 to 0.042 inch)
0.508 x 0.508 mm	0.718 mm	1.049 to 1.150 mm
(0.020 x 0.020 inch)	(0.028 inch)	(0.041 to 0.045 inch)

 Under sizing of plated through hole can lead to twisting or improper LED placement during auto insertion. Over sizing plated through hole can lead to mechanical stress on the epoxy lens during clinching.
 Note: Refer to application note AN1027 for more information on soldering LED components.

## **Recommended Wave Soldering Profile**

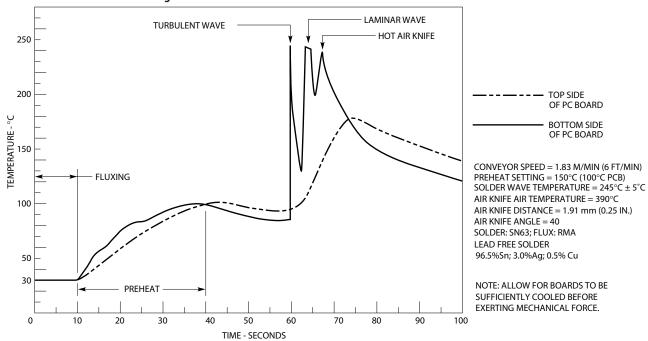
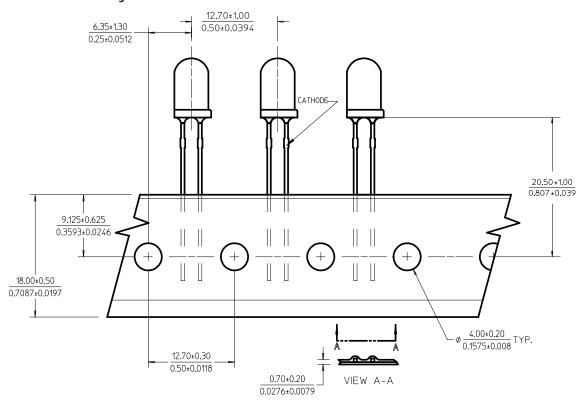


Figure 9. Recommended Wave Soldering Profile

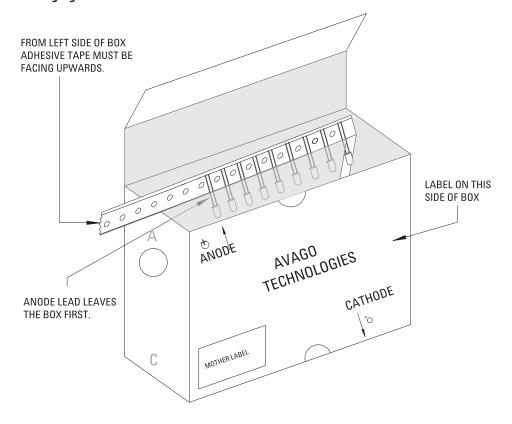
# **Ammo Packs Drawing**



 $Note: The\ ammo-packs\ drawing\ is\ applicable\ for\ packaging\ option\ -DD\ \&\ -ZZ\ and\ regardless\ standoff\ or\ non-standoff\ or\ no$ 

Figure 10. Dimension for ammo pack

# **Packaging Box for Ammo Packs**



Note: The dimension for ammo pack is applicable for the device with standoff and without standoff.

Figure 11. The arrangement of unit in ammo pack