XR16C850

UART WITH 128 BYTES OF FIFO AND INFRARED ENCODER/DECODER

DESCRIPTION

The XR16C850 is a universal asynchronous receiver and transmitter with 128 bytes of transmit and receive FIFO. It include transmit/receive FIFO counters to increase data loading and unloading from controling CPU. A programmable baud rate generator is provided to select transmit and receive clock rates from 50 Bps to 1.5 Mbps.

The XR16C850 is an improved version of the ST16C650 UART with deeper FIFO, user programmable transmit and receive trigger levels, FIFO count buffer, and support for RS485 half duplex operation. Included is complete MODEM control capability, and a processor interrupt system that may be software tailored to the user's requirements. The XR16C850 provides internal loop-back capability for on board diagnostic testing. The XR16C850 is fabricated in an advanced CMOS process to achieve low drain power and high speed requirements.

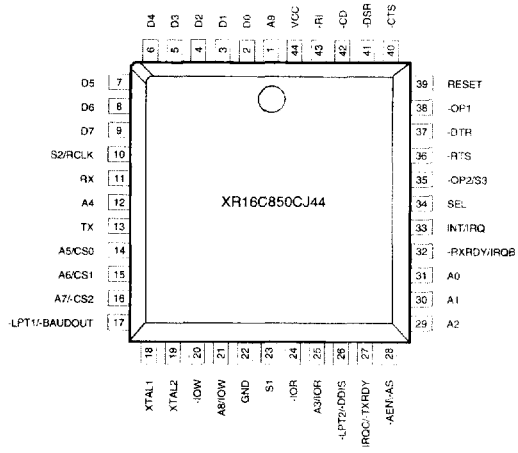
FEATURES

- Pin to pin and functional compatible to ST16C550, ST16C650
- Transmit/receive FIFO counters
- 128 bytes of Transmit/Receive FIFO
- Software/Hardware flow control
- Programmable Xon/Xoff characters
- Sleep mode (200µA stand-by)
- Programmable, selectable Transmit/Receive trigger levels
- RS485 half duplex direction support
- Infrared receive and transmit encoder/decoder
- Standard and PC mode interface with address decoder

ORDERING INFORMATION

Part number	Pin	Package	Operating temperature
XR16C850CJ44	44	PLCC	0° C to + 70° C
XR16C850CP40	40	PDIP	0° C to + 70° C
XR16C850CQ48	48	TQFP	0° C to + 70° C
XR16C850IJ44	44	PLCC	-40° C to + 85° C
XR16C850IP40	40	PDIP	-40° C to + 85° C
XR16C850IQ48	48	TQFP	-40° C to + 85° C

PLCC Package



Plastic-Dip Package