

## STTH2002CRC

## High efficiency ultrafast diode

### Main product characteristics

I <sub>F(AV)</sub>	2 x 10 A
V <sub>RRM</sub>	200 V
T <sub>j</sub> (max)	175° C
V <sub>F</sub> (typ)	0.76 V
t <sub>rr</sub> (typ)	20 ns

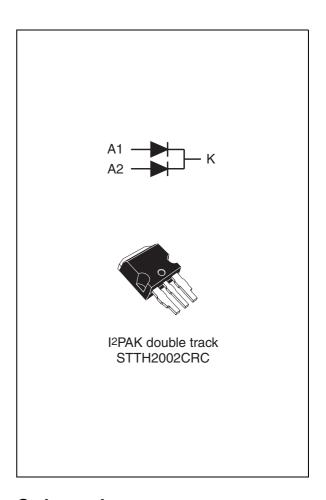
### Features and benefits

- Suited for SMPS
- Low losses
- Low forward and reverse recovery times
- High surge current capability
- High junction temperature

### **Description**

Dual center tab rectifier suited for switch mode power supplies and high frequency DC to DC converters.

Packaged in I<sup>2</sup>PAK double track, this device is intended for use in low voltage, high frequency inverters, free wheeling and polarity protection



### **Order codes**

Part Number	Marking
STTH2002CRC	STTH2002C
STTH2002CRC-TR	STTH2002C

**Characteristics** STTH2002CRC

#### **Characteristics** 1

Absolute ratings (limiting values at  $T_j$  = 25° C, unless otherwise specified) Table 1.

Symbol	Parameter			Unit
V <sub>RRM</sub>	Repetitive peak reverse voltage	Repetitive peak reverse voltage		V
I <sub>F(RMS)</sub>	RMS forward current Per diode		32	Α
,	Average few yeard average \$ 0.5	Per diode T <sub>c</sub> = 150° C	10	
$I_{F(AV)}$ Average forward current, $\delta = 0.5$	Per device T <sub>c</sub> = 145° C	20	Α	
I <sub>FSM</sub>	Surge non repetitive forward current t <sub>p</sub> = 10 ms Sinusoidal		100	Α
T <sub>stg</sub>	Storage temperature range			° C
T <sub>j</sub>	Maximum operating junction temperature			° C

Table 2. **Thermal parameters** 

Symbol	Parameter		Value	Unit
В	D. Junction to cook	Per diode	2.5	
R <sub>th(j-c)</sub> Junction to case	Total	1.4	° C/W	
R <sub>th(c)</sub>	Coupling		0.25	

When the two diodes 1 and 2 are used simultaneously:

 $\Delta Tj(\text{diode 1}) = P \text{ (diode 1) } X \text{ R}_{th(j\text{-}c)} \text{ (Per diode)} + P \text{ (diode 2) } x \text{ R}_{th(c)}$ 

Table 3. Static electrical characteristics

Symbol	Parameter	Test conditions		Min.	Тур	Max.	Unit
I <sub>P</sub> <sup>(1)</sup> Reverse leakage current	T <sub>j</sub> = 25° C	$V_R = V_{RRM}$			10		
	T <sub>j</sub> = 125° C			10	100	μA	
		T <sub>j</sub> = 150° C	I <sub>F</sub> = 10 A		0.76	0.85	
V <sub>F</sub> <sup>(2)</sup> Forward voltage drop	T <sub>j</sub> = 25° C	I <sub>F</sub> = 20 A			1.2	V	
	T <sub>j</sub> = 150° C			0.90	1.02		

<sup>1.</sup> Pulse test:  $t_p$  = 5 ms,  $\delta$  < 2 %

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To evaluate the conduction losses use the following equation: P = 0.68 x  $I_{F(AV)}$  + 0.017  $I_{F}^{2}_{(RMS)}$ 

$$P = 0.68 \times I_{F(AV)} + 0.017 I_{F^2(RMS)}$$

<sup>2.</sup> Pulse test:  $t_p$  = 380  $\mu$ s,  $\delta$  < 2 %

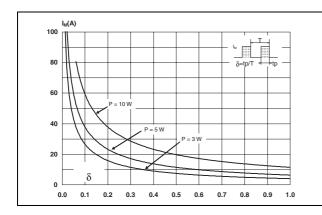
STTH2002CRC Characteristics

Table 4. Dynamic characteristics

Symbol	Parameter	Test conditions	Min.	Тур	Max.	Unit
+	Payarsa racayary tima	$I_F = 1 \text{ A, } dI_F/dt = -100 \text{ A/}\mu\text{s,}$ $V_R = 30 \text{ V, } T_j = 25 \text{ °C}$		20	25	ns
t <sub>rr</sub> Reverse recovery time	$I_F = 1 \text{ A, } dI_F/dt = -50 \text{ A/}\mu\text{s,}$ $V_R = 30 \text{ V, } T_j = 25 \text{ °C}$		28	35	115	
I <sub>RM</sub>	Reverse recovery current	$I_F = 10 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, \ V_R = 160 \text{ V}, T_j = 125 ^{\circ}\text{C}$		5.8	7.5	Α
t <sub>fr</sub>	Forward recovery time	$I_F = 10 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}$ $V_{FR} = 1.1 \text{ x } V_{Fmax}, T_j = 25 \text{ °C}$		180		ns
V <sub>FP</sub>	Forward recovery voltage	$I_F = 10 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A/}\mu\text{s},$ $V_{FR} = 1.1 \text{ x } V_{Fmax}, T_j = 25 \text{ °C}$		1.6		٧

Figure 1. Peak current versus duty cycle

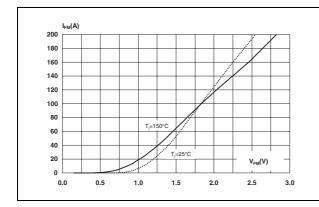
Figure 2. Forward voltage drop versus forward current (typical values)

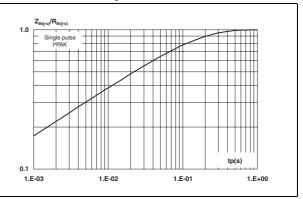


180
160
140
120
100
80
60
40
20
0
0.0
0.5
1.0
1.5
2.0
2.5
3.0

Figure 3. Forward voltage drop versus forward current (maximum values)

Figure 4. Relative variation of thermal impedance, junction to case, versus pulse duration

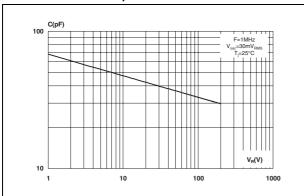




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Figure 5. Junction capacitance versus reverse applied voltage (typical values)

Figure 6. Reverse recovery charges versus dl<sub>F</sub>/dt (typical values)



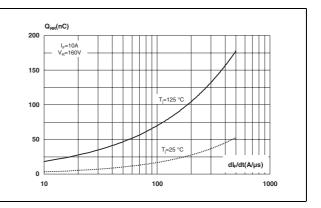
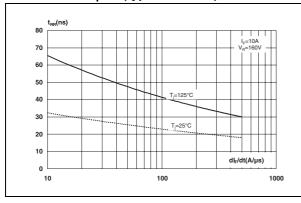


Figure 7. Reverse recovery time versus dl<sub>F</sub>/dt (typical values)

Figure 8. Peak reverse recovery current versus dl<sub>F</sub>/dt (typical values)



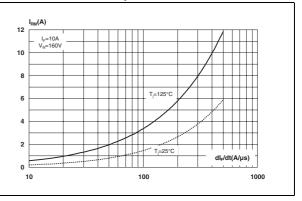
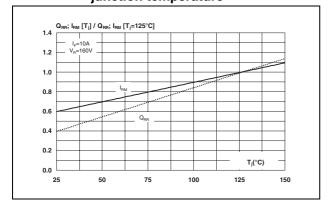
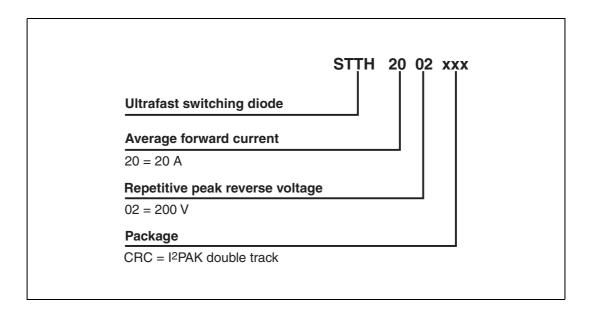


Figure 9. Dynamic parameters versus junction temperature



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# 2 Ordering information scheme



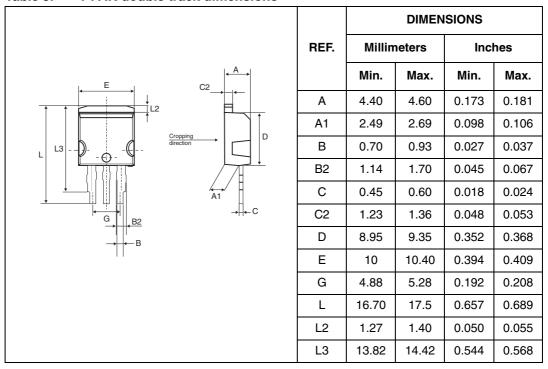
Package information STTH2002CRC

## 3 Package information

Epoxy meets UL94, V0

Cooling method: by conduction (C)

Table 5. I<sup>2</sup>PAK double track dimensions



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

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# 4 Ordering information

Part Number	Marking	Package	Weight	Base qty	Delivery mode
STTH2002CRC	STTH2002C	I <sup>2</sup> PAK DT	1.48 g	50	Tube
STTH2002CRC-TR	STTH2002C	I <sup>2</sup> PAK DT	1.48 g	1000	Tape and reel

# 5 Revision history

Date	Revision	Description of Changes
05-Apr-2006	1	First issue

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