

IC for CMOS Detector Monolithic IC KIC75** Series

This IC functions in a variety of CPU systems and other logic systems, to detect supply voltage and reset the system accurately when the power is turned on or interrupted.

To 2% of detection voltage accuracy of the conventional models, a maximum of 1% of super-high precision is realized, and it is more suitable for battery detection etc.

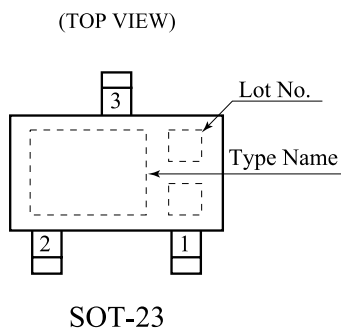
FEATURES

- High Accuracy : $\pm 1\%$
- Ultra-low current consumption: 0.25 μA (Typ)
- Operating temperature range : $-40 \sim +105^\circ\text{C}$
- Detecting voltage rank : 1.9 ~ 6.0V(0.1V step)
- Output configuration : Open drain output

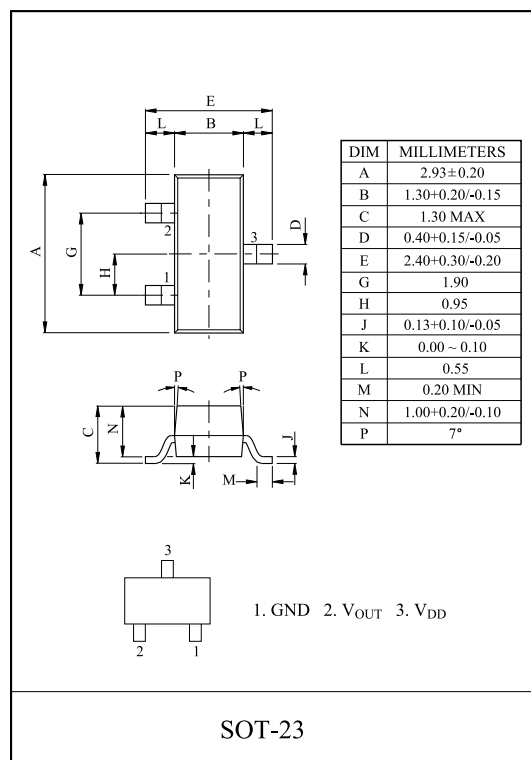
Applications

- Reset circuits for microcomputers, CPUs and MPUs
- Reset circuits for logic circuits
- Battery voltage check circuits
- Back-up power supply switching circuits
- Level detection circuits

Pin Configuration

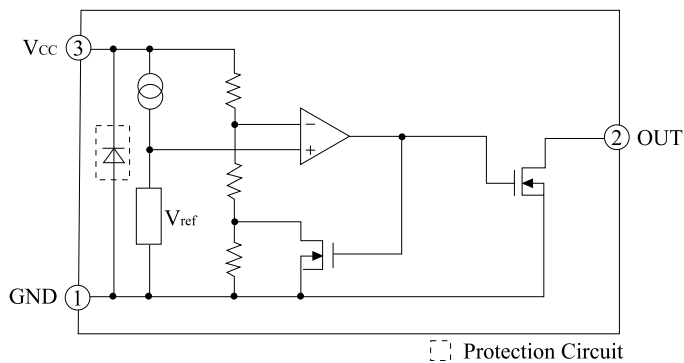


Pin No	Symbol	Function
1	GND	GND
2	V_{OUT}	Detector Output
3	V_{DD}	Power Supply Voltage



KIC7508~7560M

Block Diagram



Pin No	Symbol	Function
1	GND	GND
2	V _{OUT}	Detector Output
3	V _{DD}	Power Supply Voltage

Figure. 1

Selection Guide

The output voltage, package type for the ICs can be selected at the user's request.
The selection can be made with designating the part number as shown below

KIC7 **5**
① ② ③

NO.	Specifications	Description
①	Voltage Detector Code	-
②	Detection Voltage	0.8 ~ 6.0V
③	Package	M SOT - 23

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

ITEM	SYMBOL	RATING	UNITS
Supply voltage	V _{DD} MAX.	-0.3 +12.0	V
Output voltage	V _{OUT}	-0.3 (V _{DD} +0.3)	V
Input current (V _{DD})	I _{DD}	20	mA
Output current ($\overline{\text{RESET}}$)	I _{OUT}	20	mA
Power Dissipation	P _D	350 * Note)	mW
Operating temperature	T _{OPR}	-40~+105	°C
Storage temperature	T _{STG}	-65~+150	°C

* Note) Package mounted on 99.5% Alumina 10×8×0.6mm.

KIC7508~7560M

Recommended Operating Conditions

ITEM	SYMBOL	RATINGS	UNITS
Operating Temperature	T _{opr}	-40 +105	°C
Supply voltage	V _{DD}	0.70 ~ 10.0	V

Electrical characteristics : Unless Otherwise Specified Ta=25°C

ITEM	SYMBOL	MEASUREMENT CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
Supply current	I _{DD}	V _{DD} = V _{TH} + 1V	-	0.25	1.0	uA	(1)
Reset threshold	V _{TH}	Ta = +25°C V _{TH} ≤ 1.9V	V _{TH} -1%	V _{TH} 0.8~ 6.0V (0.1Vstep)	V _{TH} +1%	V	(2)
		Ta = -40~+85°C (note1)	V _{TH} -2.5%		V _{TH} +2.5%		
		Ta = +25°C V _{TH} ≥ 2.0V	V _{TH} -1%		V _{TH} +1%		
		Ta = -40~+85°C (note1)	V _{TH} -2.5%		V _{TH} +2.5%		
Reset threshold hysteresis	Δ V _{TH}	V _{DD} =0V→V _{TH} +1V→0V	V _{TH} ×0.03	-	V _{TH} ×0.08	V	(2)
Reset threshold temp. coefficient	Δ V _{TH} /°C	Ta=-40~+85°C (note1)	-	± 100	-	ppm/°C	(2)
L transfer delay time	t _{PHL}	V _{DD} =V _{TH} +0.4V→V _{TH} -0.4V(note2)	-	-	100	us	(4)
H transfer delay time	t _{PLH}	V _{DD} =V _{TH} +0.4V→V _{TH} -0.4V(note2)	-	-	100	us	(4)
"L" output current	I _{OL1}	V _{DD} =0.7V, V _{DS} =0.05V	0.01	0.10	-	mA	(3)
	I _{OL2}	V _{DD} =1.2V, V _{DS} =0.5V V _{TH} 1.3V	0.23	2.00			
	I _{OL3}	V _{DD} =2.4V, V _{DS} =0.5V V _{TH} 2.5V	1.60	8.00			
	I _{OL4}	V _{DD} =3.6V, V _{DS} =0.5V V _{TH} 3.7V	3.20	12.0			
Output leakage current	I _{leak}	V _{DD} =10V, V _{OUT} =10V	-	-	0.1	uA	(3)

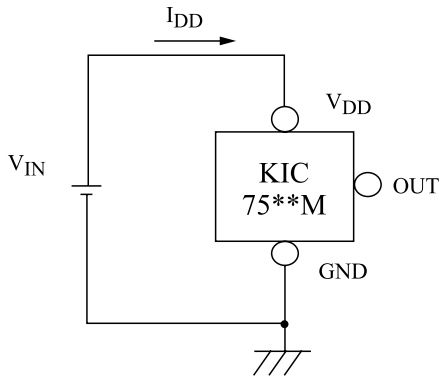
note1: This device is tested at Ta=25°C, over temperature limits guaranteed by design only.

note2: The parameter is guaranteed by design.

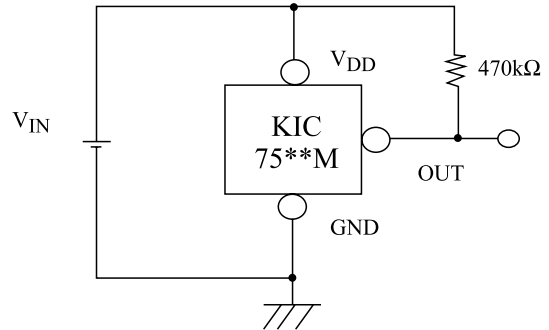
KIC7508~7560M

Test Circuits

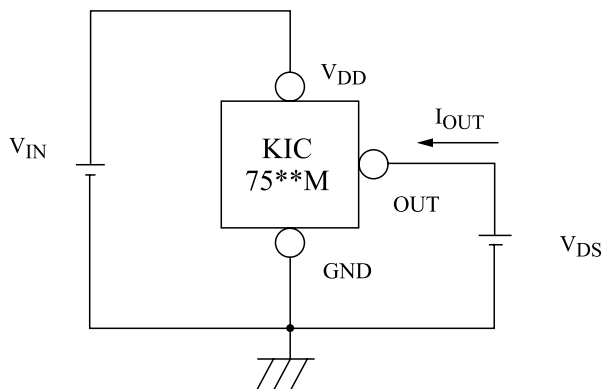
(1) I_{DD}



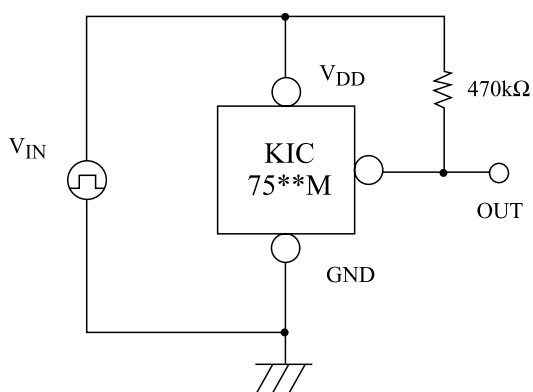
(2) V_{TH} , ΔV_{TH} , $\Delta V_{TH}/^{\circ}C$



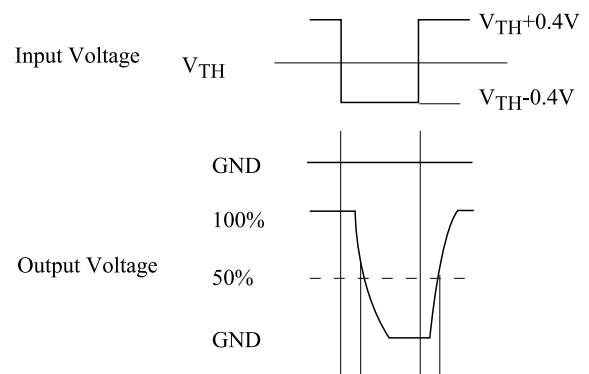
(3) I_{OL1} , I_{OL2} , I_{OL3} , I_{OL4} , I_{LEAK}



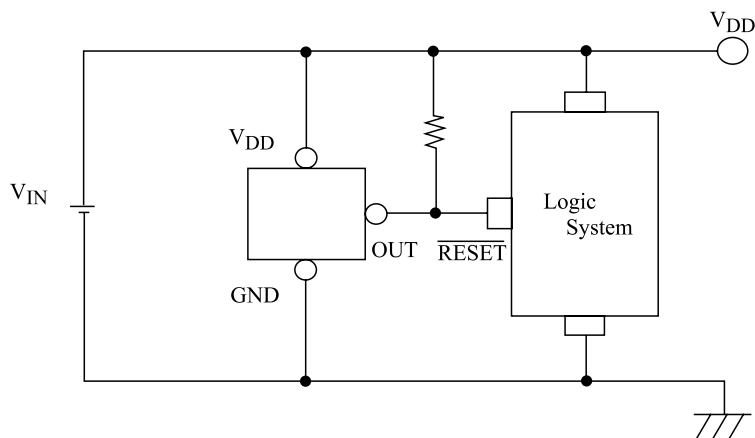
(4) T_{PLH} , T_{PHL}



Test Wave



Application Circuits



- We shall not be liable for any trouble or damage caused by using this circuit.
- In the event a problem which may affect industrial property or any other rights of us or a third party is encountered during the use of information described in this circuit, KEC shall not be liable for any such problem, nor grant a license therefore.

Typical Characteristics (Typical Performance Characteristics 2.8V)

note : These are typical characteristics

Fig1. Detecting Voltage

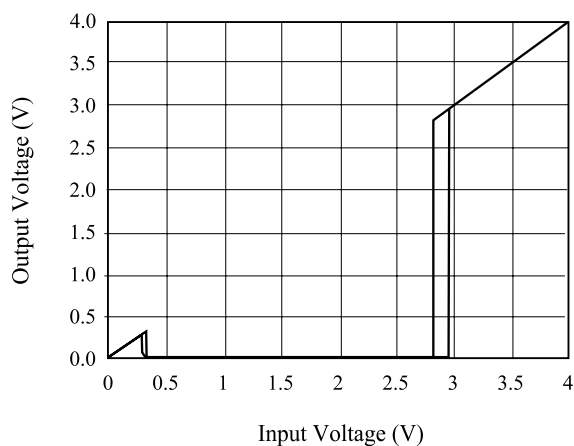


Fig2. Supply Current

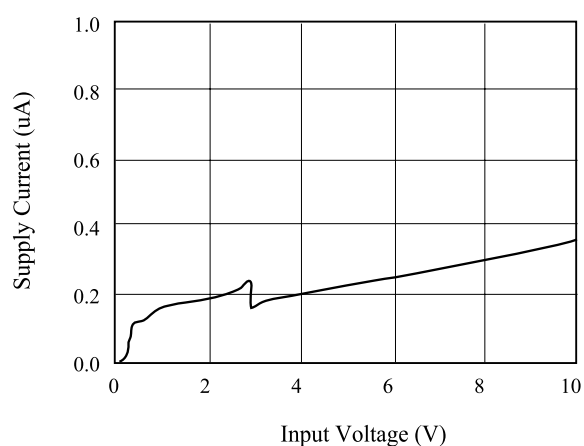


Fig3. Detecting Voltage vs Temperature

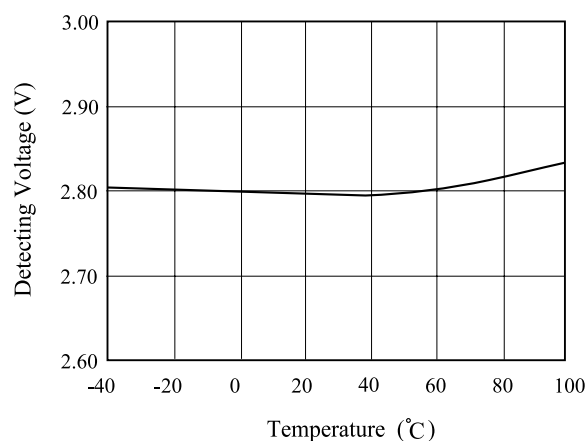


Fig4. Hysteresis Voltage vs Temperature

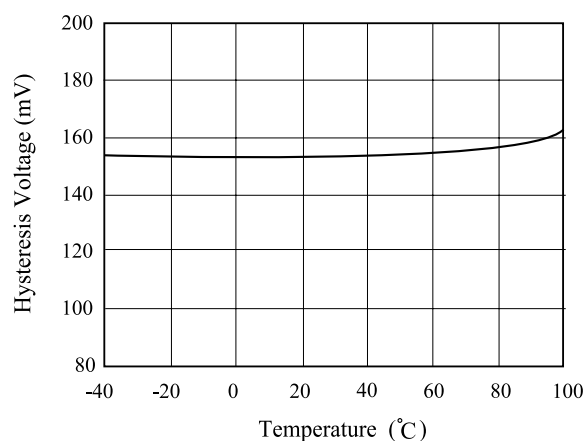


Fig5. "L" Output Current 1 vs Temperature

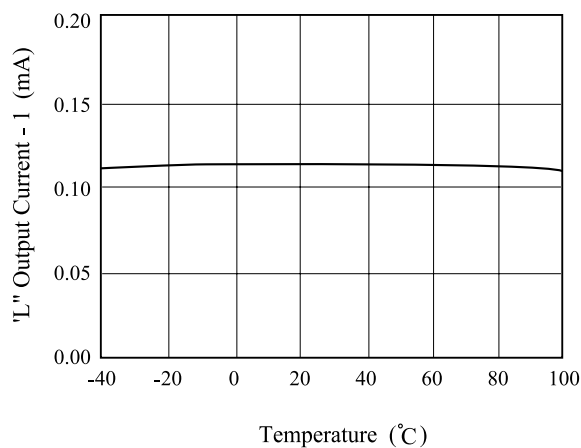


Fig6. "L" Output Current 2 vs Temperature

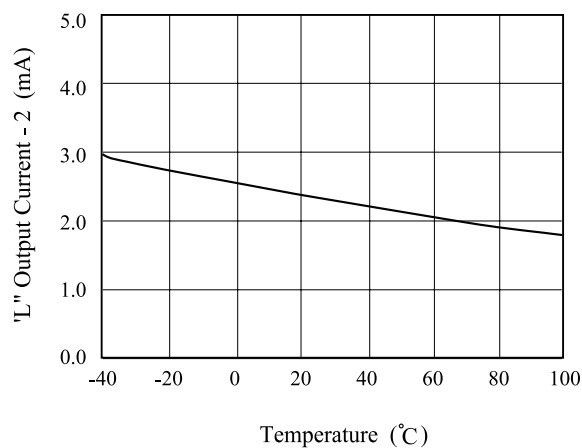


Fig7. "L" Output Current 3 vs Temperature

