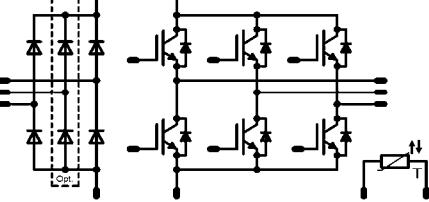
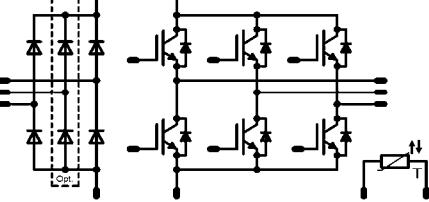
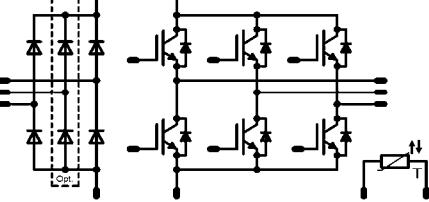


MiniSkip 0		600V/10A				
<table border="1"> <thead> <tr> <th>Features</th> </tr> </thead> <tbody> <tr> <td> <ul style="list-style-type: none"> Solderless interconnection Trench Fieldstop IGBT's for low saturation losses Optional 2- and 3-leg rectifier </td> </tr> </tbody> </table>	Features	<ul style="list-style-type: none"> Solderless interconnection Trench Fieldstop IGBT's for low saturation losses Optional 2- and 3-leg rectifier 				
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<table border="1"> <thead> <tr> <th>Types</th> </tr> </thead> <tbody> <tr> <td>80-M006PNB010SA01-K615D, 2-leg rectifier 80-M006PNB010SA-K615C, 3-leg rectifier</td> </tr> </tbody> </table>	Types	80-M006PNB010SA01-K615D, 2-leg rectifier 80-M006PNB010SA-K615C, 3-leg rectifier				
Types						
80-M006PNB010SA01-K615D, 2-leg rectifier 80-M006PNB010SA-K615C, 3-leg rectifier						

Maximum Ratings

$T_j=25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Input Rectifier Diode				
Repetitive peak reverse voltage	V_{RRM}		1600	V
DC forward current	I_{FAV}	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	25 25	A
Surge forward current	I_{FSM}	$t_p=10\text{ms}$	220	A
I^2t -value	I^2t	$T_j=25^\circ\text{C}$	240	A^2s
Power dissipation per Diode	P_{tot}	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	38 58	W
Maximum Junction Temperature	$T_{j\max}$		150	$^\circ\text{C}$

Inverter Transistor

Collector-emitter break down voltage	V_{CE}		600	V
DC collector current	I_C	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	15 15	A
Repetitive peak collector current	I_{Cpulse}	t_p limited by $T_{j\max}$	30	A
Turn off safe operating area		$V_{CE} \leq 1200\text{V}$, $T_j \leq T_{j\max}$	30	A
Power dissipation per IGBT	P_{tot}	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	39 59	W
Gate-emitter peak voltage	V_{GE}		± 20	V
Short circuit ratings	t_{SC} V_{CC}	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	6 360	μs V
Maximum Junction Temperature	$T_{j\max}$		175	$^\circ\text{C}$

Maximum Ratings

$T_j=25^\circ\text{C}$, unless otherwise specified

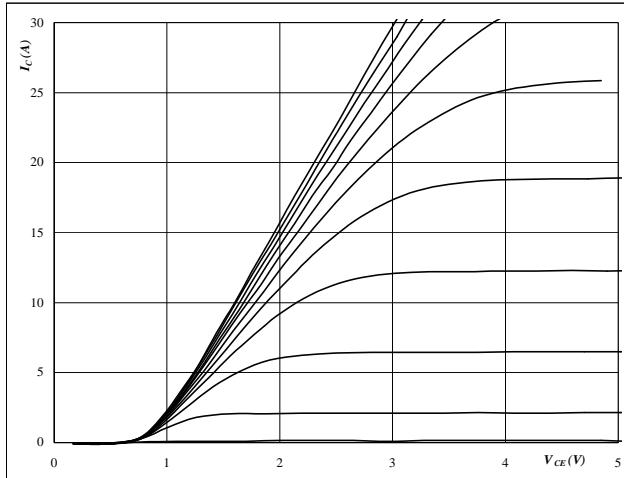
Parameter	Symbol	Condition	Value	Unit
Inverter Diode				
Peak Repetitive Reverse Voltage	V_{RRM}	$T_j=25^\circ\text{C}$	600	V
DC forward current	I_F	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	15 15	A
Repetitive peak forward current	I_{FRM}	t_p limited by $T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	30	A
Power dissipation per Diode	P_{tot}	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	33 49	W
Maximum Junction Temperature	$T_j\text{max}$		175	$^\circ\text{C}$
Thermal Properties				
Storage temperature	T_{stg}		-40...+125	$^\circ\text{C}$
Operation temperature under switching condition	T_{op}		-40...+($T_j\text{max} - 25$)	$^\circ\text{C}$
Insulation Properties				
Insulation voltage	V_{is}	$t=2\text{s}$ DC voltage	4000	V
Creepage distance			min 12,7	mm
Clearance			min 12,7	mm

Characteristic Values

Parameter	Symbol	Conditions				Value			Unit			
		V_{GE} [V] or V_{GS} [V]	V_r [V] or V_{CE} [V] or V_{DS} [V]	I_c [A] or I_F [A] or I_D [A]	T_j	Min	Typ	Max				
Input Rectifier Diode												
Forward voltage	V_F			25	$T_j=25^\circ C$ $T_j=125^\circ C$		1,43 1,44	1,64	V			
Threshold voltage (for power loss calc. only)	V_{to}			25	$T_j=25^\circ C$ $T_j=125^\circ C$		0,92 0,79		V			
Slope resistance (for power loss calc. only)	r_t			25	$T_j=25^\circ C$ $T_j=125^\circ C$		20,29 26,11		$m\Omega$			
Reverse current	I_r		1600		$T_j=25^\circ C$ $T_j=125^\circ C$			0,05	mA			
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness 50um $\lambda = 1 W/mK$					1,83		K/W			
Inverter Transistor												
Gate emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=V_{GE}$		0,00015	$T_j=25^\circ C$ $T_j=150^\circ C$	5	5,8	6,5	V			
Collector-emitter saturation voltage	$V_{CE(sat)}$		15	10	$T_j=25^\circ C$ $T_j=150^\circ C$	1,19	1,64 1,89	1,99	V			
Collector-emitter cut-off current incl. Diode	I_{CES}		0	600	$T_j=25^\circ C$ $T_j=150^\circ C$			0,0006	mA			
Gate-emitter leakage current	I_{GES}		20	0	$T_j=25^\circ C$ $T_j=150^\circ C$			300	nA			
Integrated Gate resistor	R_{gint}								Ω			
Turn-on delay time	$t_{d(on)}$	$R_{goff}=64 \Omega$ $R_{gon}=64 \Omega$	± 15	300	$T_j=25^\circ C$ $T_j=150^\circ C$		90 91		ns			
Rise time	t_r						22 25					
Turn-off delay time	$t_{d(off)}$						133 156					
Fall time	t_f						120 144					
Turn-on energy loss per pulse	E_{on}						0,26 0,38		mWs			
Turn-off energy loss per pulse	E_{off}						0,26 0,34					
Input capacitance	C_{ies}						551		pF			
Output capacitance	C_{oss}						40					
Reverse transfer capacitance	C_{rss}						17					
Gate charge	Q_{Gate}		± 15		$T_j=25^\circ C$		55	62	nC			
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness 50um $\lambda = 1 W/mK$					2,46		K/W			
Inverter Diode												
Diode forward voltage	V_F			10	$T_j=25^\circ C$ $T_j=150^\circ C$		1,39 1,32		V			
Peak reverse recovery current	I_{RRM}	$R_{gon}=64 \Omega$	± 15	300	$T_j=25^\circ C$ $T_j=150^\circ C$		6,77 9,87		A			
Reverse recovery time	t_{rr}						233 352		ns			
Reverse recovered charge	Q_{rr}						0,66 1,46		μC			
Peak rate of fall of recovery current	$di(rec)max/dt$						105 109		$A/\mu s$			
Reverse recovered energy	E_{rec}						0,13 0,30		mWs			
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness 50um $\lambda = 1 W/mK$					2,92		K/W			
Thermistor												
Rated resistance	R				$Tr=25^\circ C$		1000		Ω			
Deviation of R	$\Delta R/R$	$R25=1000 \Omega$ $R100=1670 \Omega$			$Tr=25^\circ C$ $Tr=100^\circ C$	-3 -2		3 2	%			
R100	R_{100}				$Tr=25^\circ C$		1670		Ω			
Temperature coefficient							0,76		$\% /K$			
A-value	$B_{(25/50)}$	Tol. %			$Tr=25^\circ C$		7,635*10-3		$1/K$			
B-value	$B_{(25/100)}$	Tol. %			$Tr=25^\circ C$		1,731*10-5		$1/K^2$			
Vincotech NTC Reference								E				

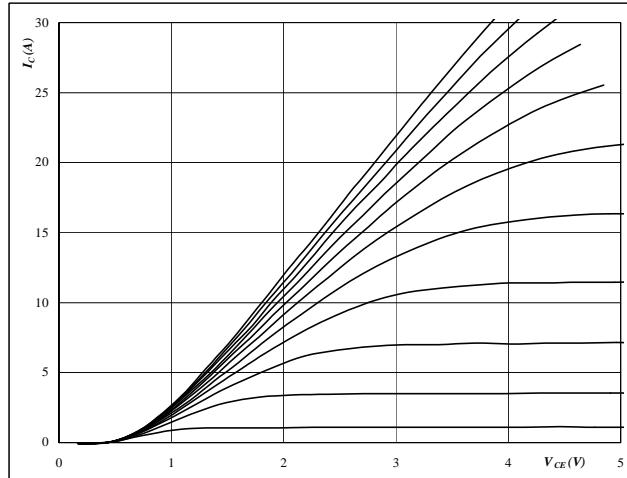
Output Inverter

Figure 1
Typical output characteristics
 $I_C = f(V_{CE})$



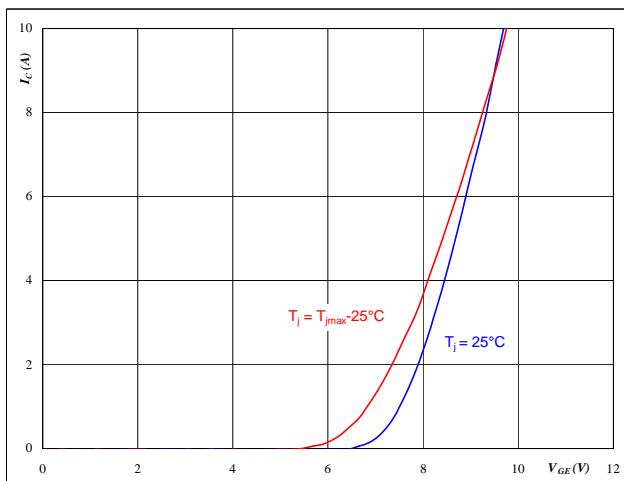
$t_p = 250 \mu s$
 $T_j = 25 ^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 2
Typical output characteristics
 $I_C = f(V_{CE})$



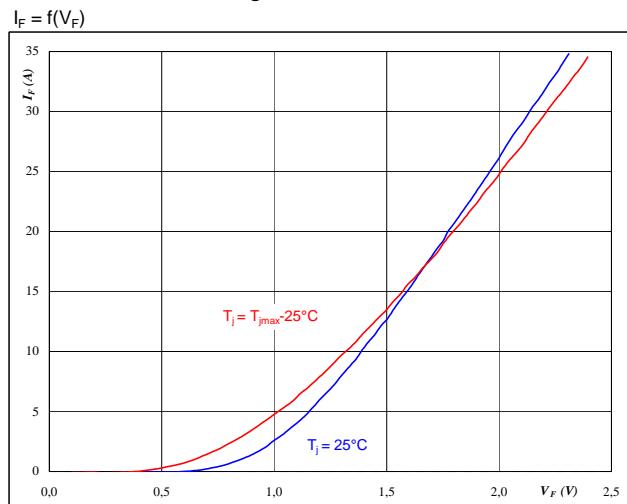
$t_p = 250 \mu s$
 $T_j = 150 ^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 3
Typical transfer characteristics
 $I_C = f(V_{GE})$



$t_p = 250 \mu s$
 $V_{CE} = 10 V$

Figure 4
Typical diode forward current as a function of forward voltage
 $I_F = f(V_F)$



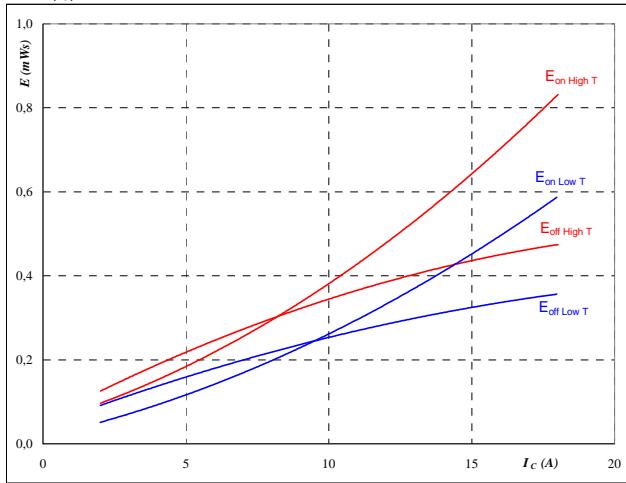
$t_p = 250 \mu s$

Output Inverter

Figure 5

**Typical switching energy losses
as a function of collector current**

$$E = f(I_C)$$



inductive load

$$T_j = 25/150 \quad ^\circ C$$

$$V_{CE} = 300 \quad V$$

$$V_{GE} = \pm 15 \quad V$$

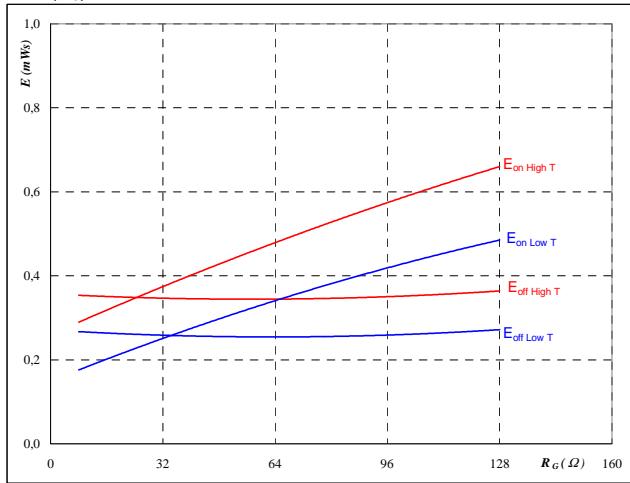
$$R_{gon} = 32 \quad \Omega$$

$$R_{goff} = 32 \quad \Omega$$

Output inverter IGBT
Figure 6

**Typical switching energy losses
as a function of gate resistor**

$$E = f(R_G)$$



inductive load

$$T_j = 25/150 \quad ^\circ C$$

$$V_{CE} = 300 \quad V$$

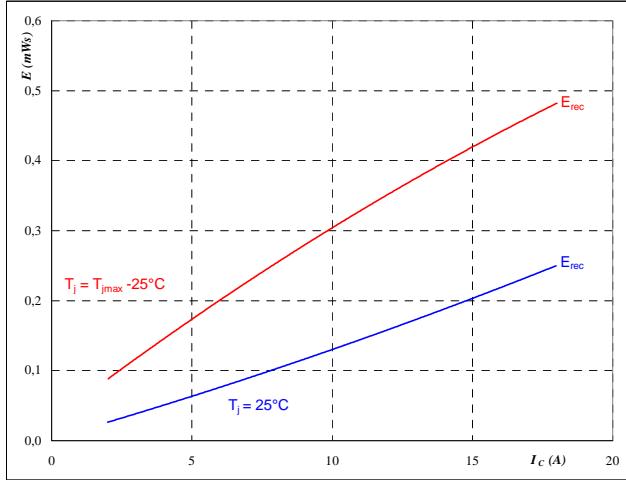
$$V_{GE} = \pm 15 \quad V$$

$$I_C = 10 \quad A$$

Figure 7
Output inverter FWD

**Typical reverse recovery energy loss
as a function of collector current**

$$E_{rec} = f(I_C)$$



inductive load

$$T_j = 25/150 \quad ^\circ C$$

$$V_{CE} = 300 \quad V$$

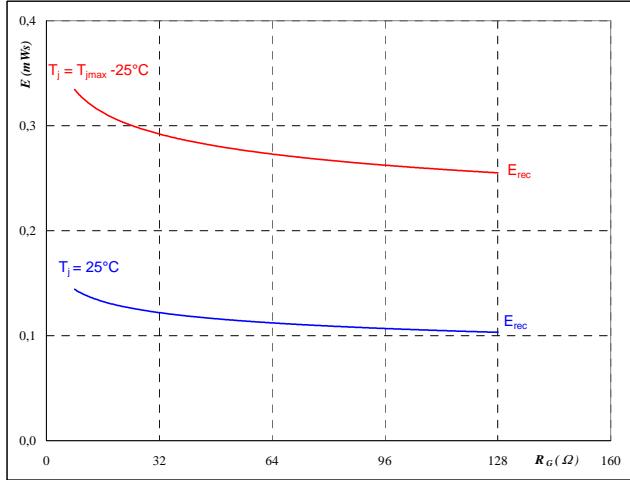
$$V_{GE} = \pm 15 \quad V$$

$$R_{gon} = 32 \quad \Omega$$

Figure 8
Output inverter FWD

**Typical reverse recovery energy loss
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



inductive load

$$T_j = 25/150 \quad ^\circ C$$

$$V_{CE} = 300 \quad V$$

$$V_{GE} = \pm 15 \quad V$$

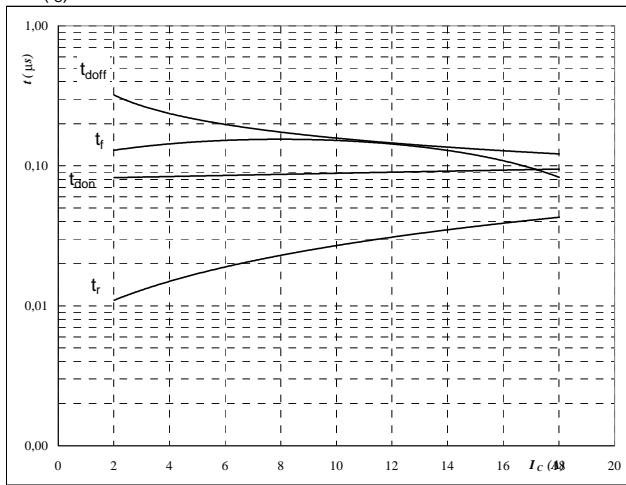
$$I_C = 10 \quad A$$

Output Inverter

Figure 9

Typical switching times as a function of collector current

$$t = f(I_c)$$



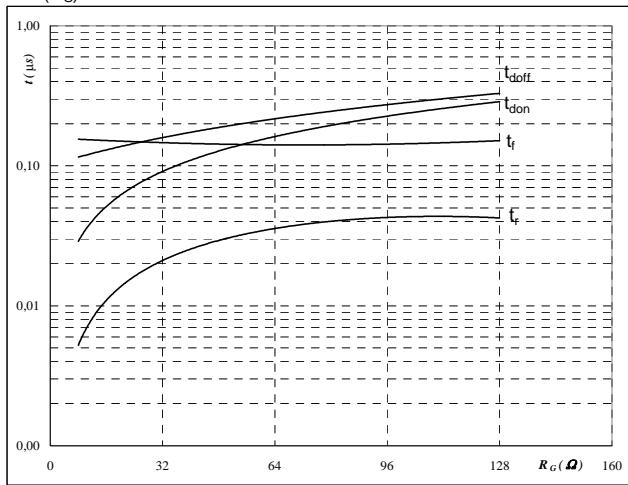
inductive load

$T_j = 150 \text{ } ^\circ\text{C}$
 $V_{CE} = 300 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 32 \Omega$
 $R_{goff} = 32 \Omega$

Output inverter IGBT
Figure 10

Typical switching times as a function of gate resistor

$$t = f(R_G)$$



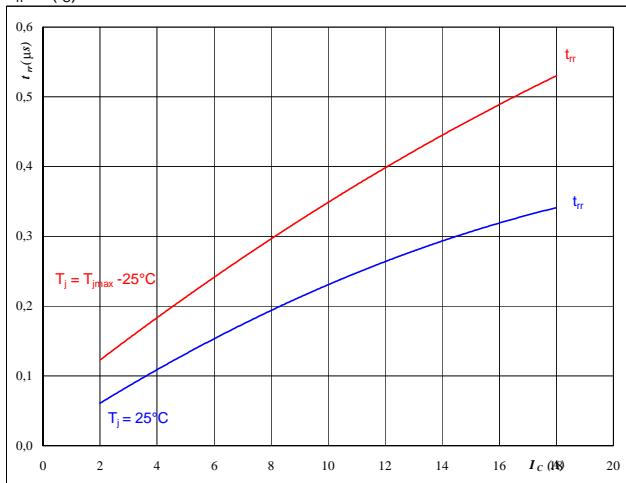
inductive load

$T_j = 150 \text{ } ^\circ\text{C}$
 $V_{CE} = 300 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 10 \text{ A}$

Figure 11
Output inverter FWD

Typical reverse recovery time as a function of collector current

$$t_{rr} = f(I_c)$$

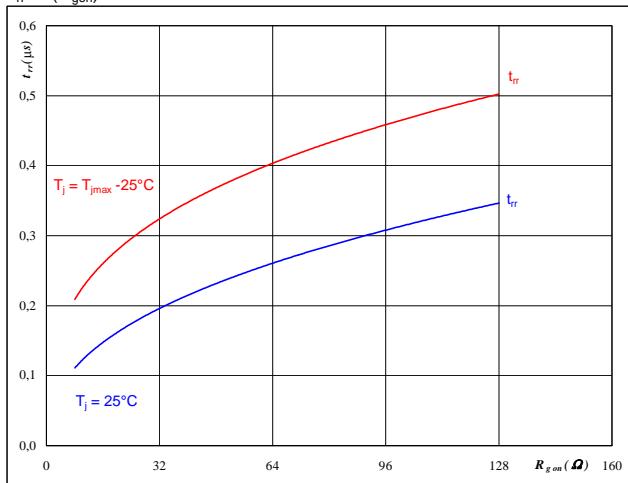


$T_j = 25/150 \text{ } ^\circ\text{C}$
 $V_{CE} = 300 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 32 \Omega$

Figure 12
Output inverter FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor

$$t_{rr} = f(R_{gon})$$



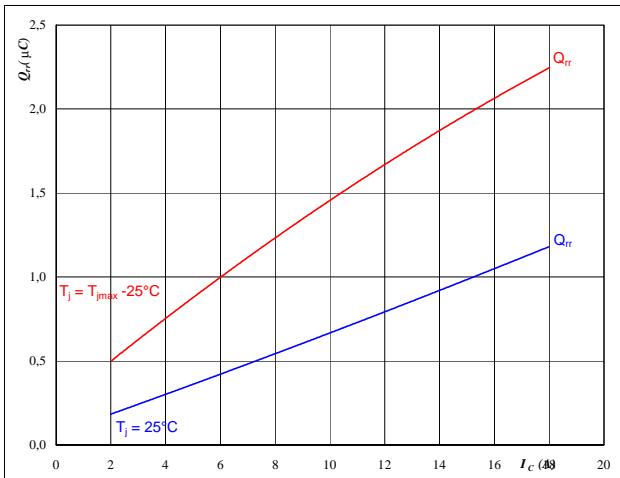
$T_j = 25/150 \text{ } ^\circ\text{C}$
 $V_R = 300 \text{ V}$
 $I_F = 10 \text{ A}$
 $V_{GE} = \pm 15 \text{ V}$

Output Inverter

Figure 13

Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_C)$$

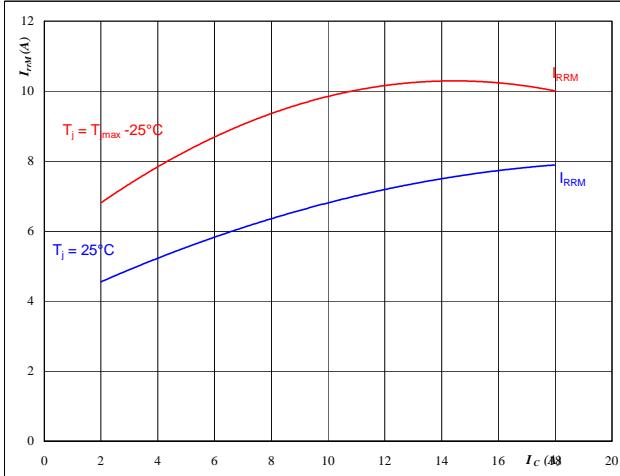


$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 300 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 32 \quad \Omega \end{aligned}$$

Figure 15

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_C)$$

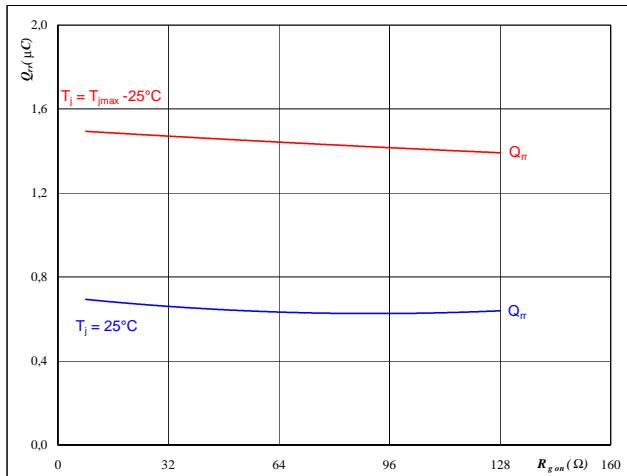


$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 300 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 32 \quad \Omega \end{aligned}$$

Figure 14

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$$Q_{rr} = f(R_{gon})$$

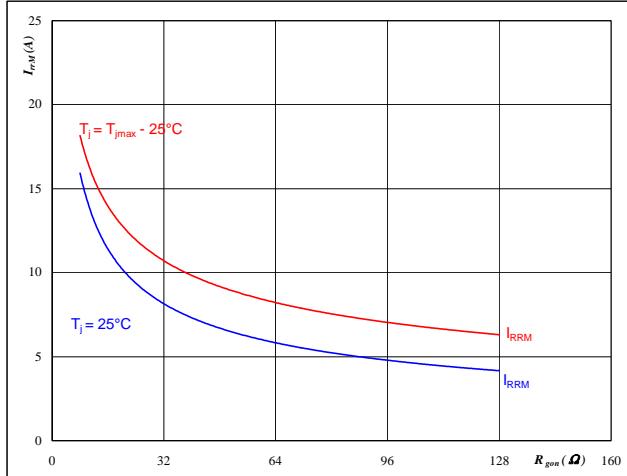


$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_R &= 300 \quad \text{V} \\ I_F &= 10 \quad \text{A} \\ V_{GE} &= \pm 15 \quad \text{V} \end{aligned}$$

Figure 16

Typical reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RRM} = f(R_{gon})$$

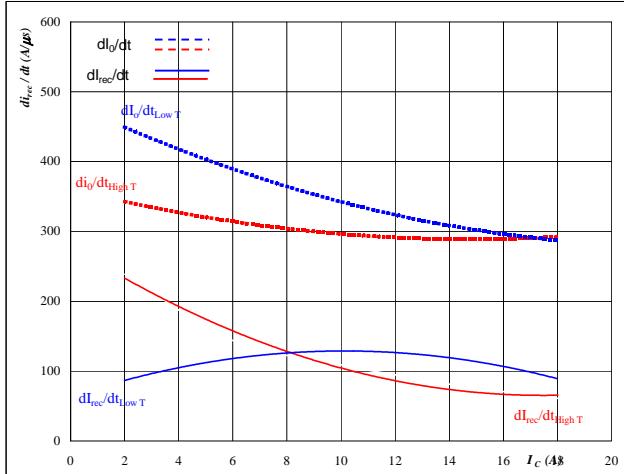


$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_R &= 300 \quad \text{V} \\ I_F &= 10 \quad \text{A} \\ V_{GE} &= \pm 15 \quad \text{V} \end{aligned}$$

Output Inverter

Figure 17 Output inverter FWD

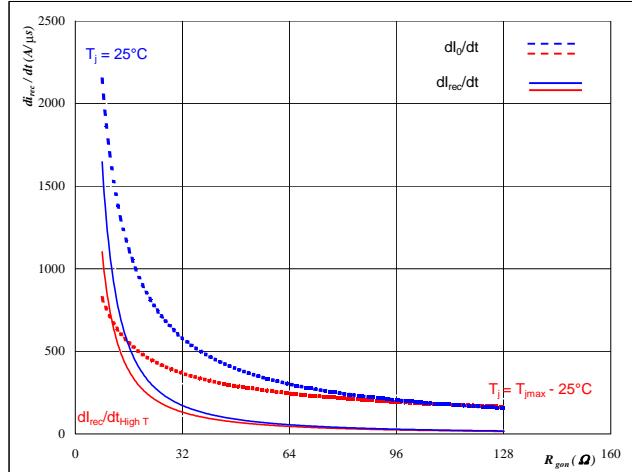
Typical rate of fall of forward and reverse recovery current as a function of collector current
 $dI_0/dt, dI_{rec}/dt = f(I_C)$



$T_j = 25/150 \text{ } ^\circ\text{C}$
 $V_{CE} = 300 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 32 \Omega$

Figure 18 Output inverter FWD

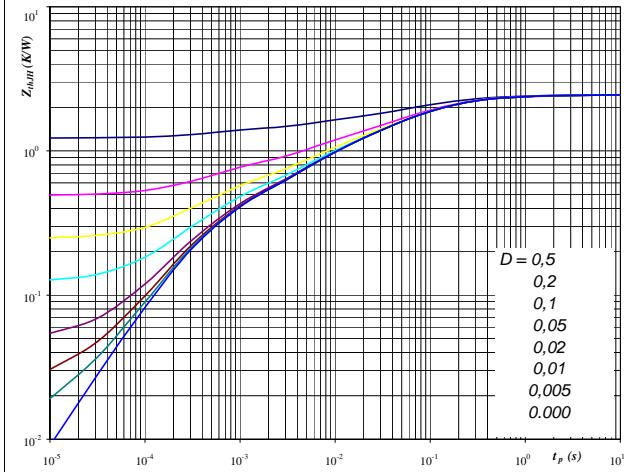
Typical rate of fall of forward and reverse recovery current as a function of IGBT turn on gate resistor
 $dI_0/dt, dI_{rec}/dt = f(R_{gon})$



$T_j = 25/150 \text{ } ^\circ\text{C}$
 $V_R = 300 \text{ V}$
 $I_F = 10 \text{ A}$
 $V_{GE} = \pm 15 \text{ V}$

Figure 19 Output inverter IGBT

IGBT transient thermal impedance as a function of pulse width
 $Z_{thJH} = f(t_p)$



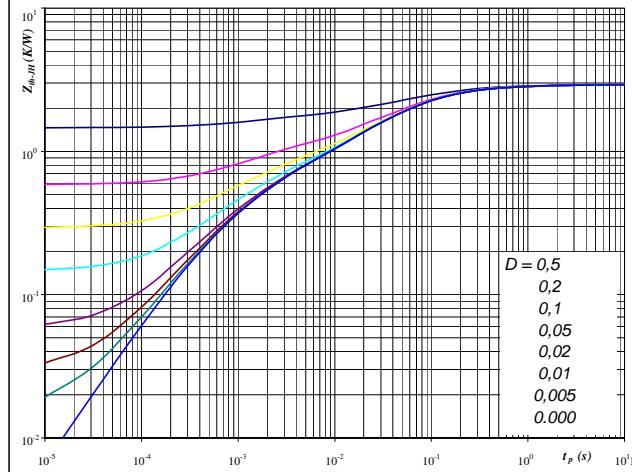
$D = t_p / T$
 $R_{thJH} = 2,46 \text{ K/W}$ 1,99

IGBT thermal model values

Thermal grease		Phase change interface	
R (C/W)	Tau (s)	R (C/W)	Tau (s)
0,13	1,9E+00	0,11	1,5E+00
0,80	1,6E-01	0,65	1,3E-01
0,76	3,0E-02	0,61	2,5E-02
0,45	4,5E-03	0,37	3,6E-03
0,32	4,0E-04	0,26	3,3E-04

Figure 20 Output inverter FWD

FWD transient thermal impedance as a function of pulse width
 $Z_{thJH} = f(t_p)$



$D = t_p / T$
 $R_{thJH} = 2,92 \text{ K/W}$ 2,37

FWD thermal model values

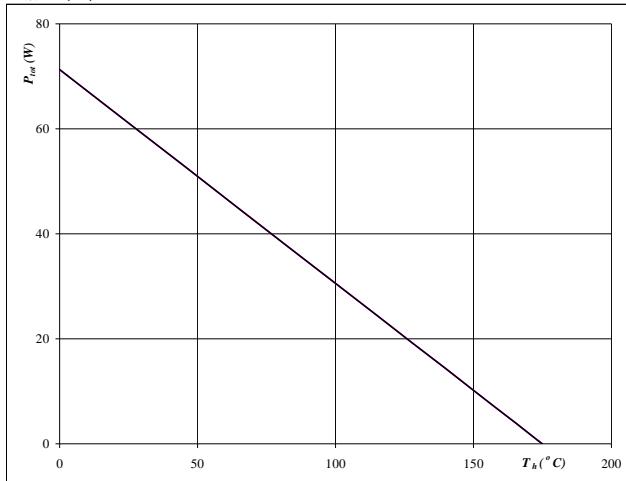
Thermal grease		Phase change interface	
R (C/W)	Tau (s)	R (C/W)	Tau (s)
0,14	1,8E+00	0,11	1,5E+00
0,70	1,8E-01	0,57	1,4E-01
1,13	4,6E-02	0,92	3,7E-02
0,48	9,5E-03	0,39	7,7E-03
0,40	1,2E-03	0,32	9,8E-04
0,07	2,9E-04	0,06	2,4E-04

Output Inverter

Figure 21

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

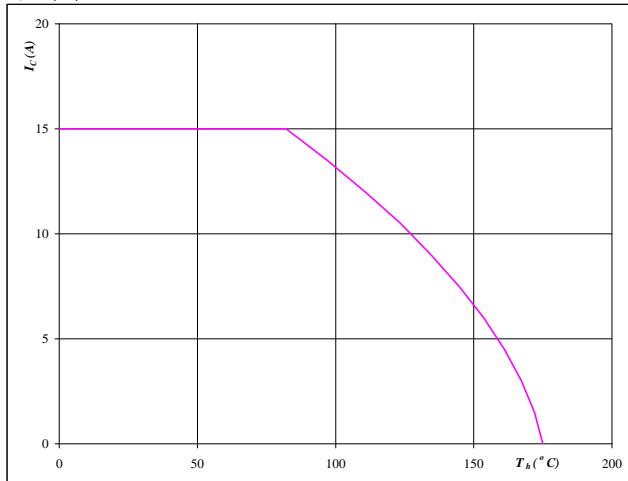


$$T_j = 175 \quad ^\circ\text{C}$$

Figure 22

Collector current as a function of heatsink temperature

$$I_C = f(T_h)$$



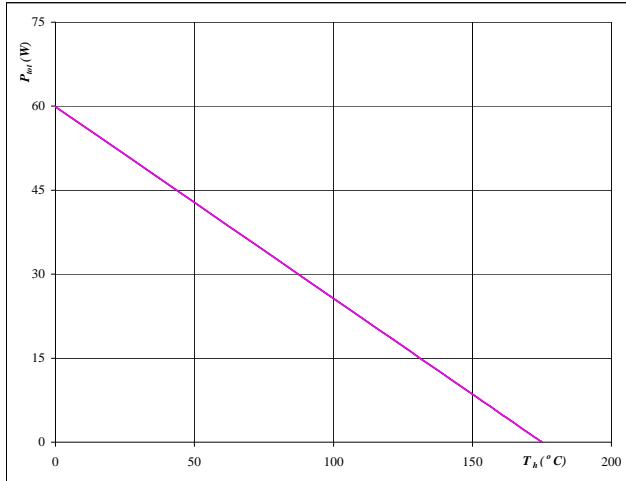
$$T_j = 175 \quad ^\circ\text{C}$$

$$V_{GE} = 15 \quad \text{V}$$

Figure 23
Output inverter FWD

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

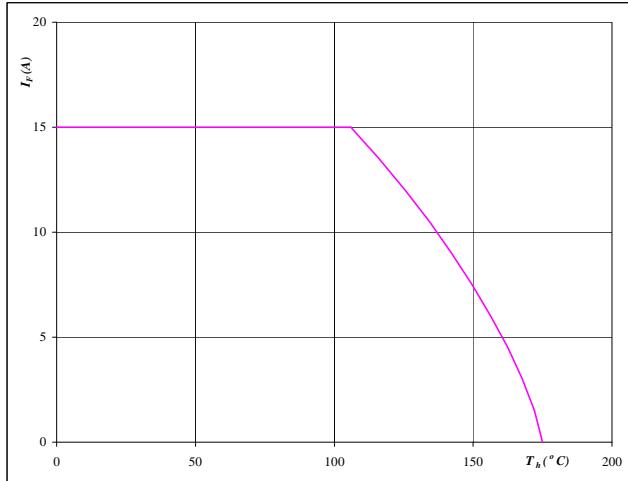


$$T_j = 175 \quad ^\circ\text{C}$$

Figure 24
Output inverter FWD

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$



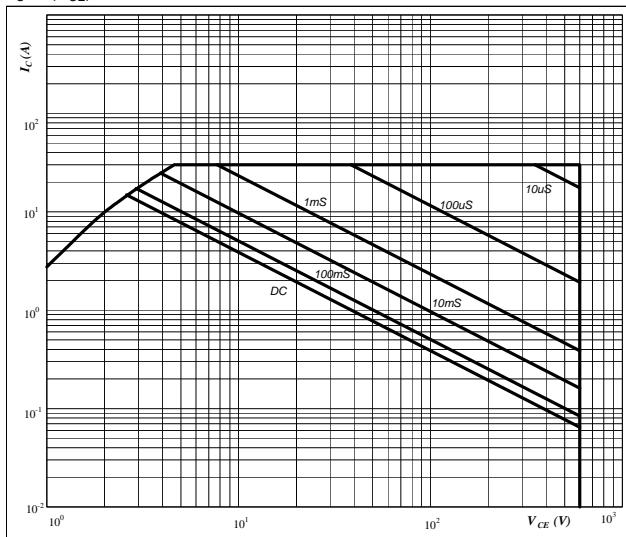
$$T_j = 175 \quad ^\circ\text{C}$$

Output Inverter

Figure 25

Safe operating area as a function of collector-emitter voltage

$$I_C = f(V_{CE})$$



D = single pulse

T_h = 80 °C

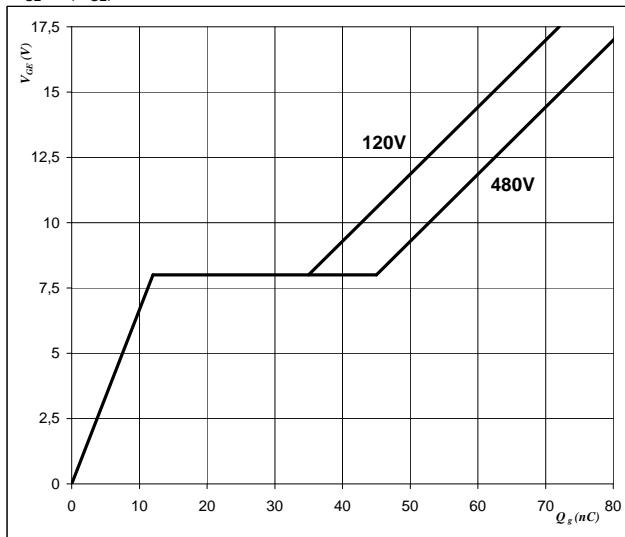
V_{GE} = ±15 V

T_j = T_{jmax} °C

Output inverter IGBT
Figure 26

Gate voltage vs Gate charge

$$V_{GE} = f(Q_{GE})$$

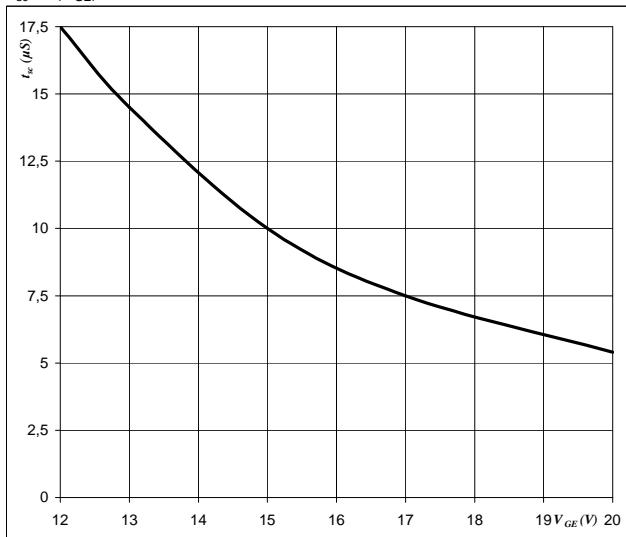


I_C = 10 A

Figure 27
Output inverter IGBT

Short circuit withstand time as a function of gate-emitter voltage

$$t_{sc} = f(V_{GE})$$

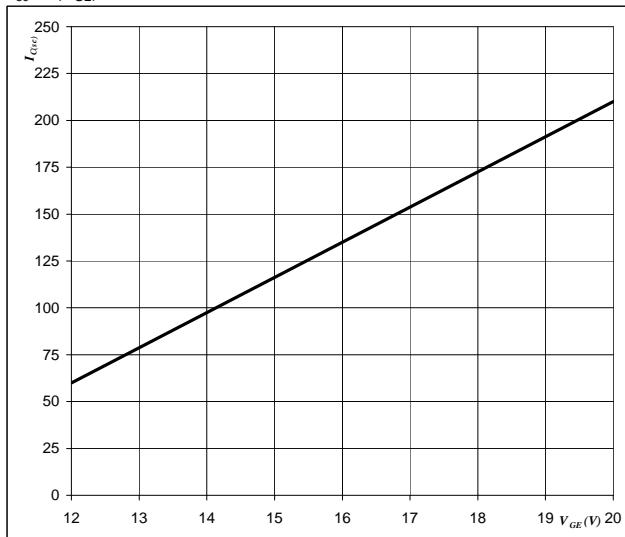


V_{CE} = 600 V
 T_j ≤ 175 °C

Figure 28
Output inverter IGBT

Typical short circuit collector current as a function of gate-emitter voltage

$$I_{sc} = f(V_{GE})$$



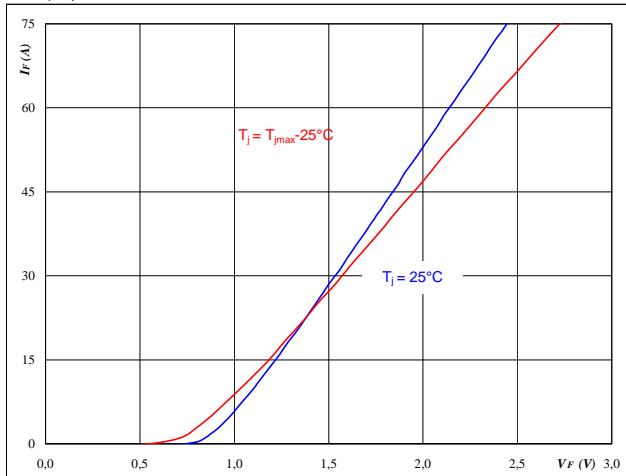
V_{CE} ≤ 600 V
 T_j = 175 °C

Input Rectifier Bridge

Figure 1

Typical diode forward current as a function of forward voltage

$$I_F = f(V_F)$$



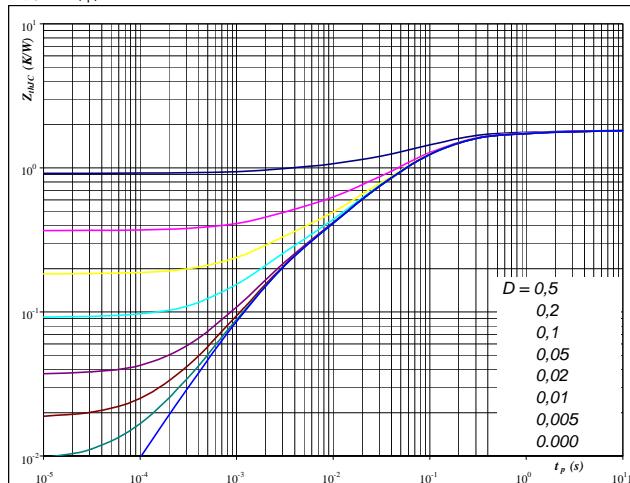
$$t_p = 250 \mu\text{s}$$

Rectifier diode

Figure 2

Diode transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$

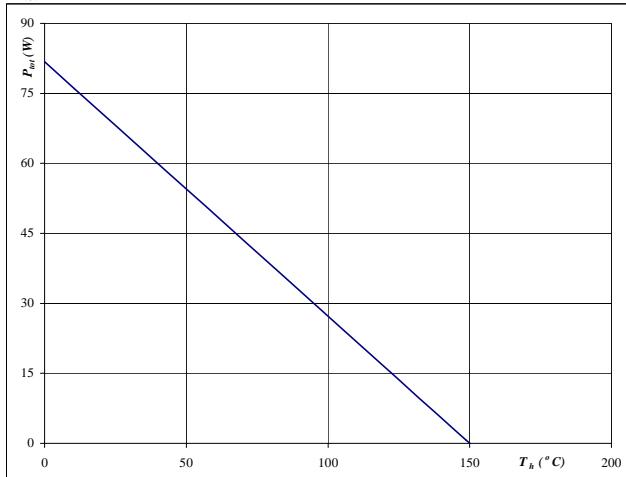


$$\begin{aligned} D &= t_p / T \\ R_{thJH} &= 1,834 \text{ K/W} \end{aligned}$$

Figure 3

Power dissipation as a function of heatsink temperature

$$P_{tot} = f(T_h)$$



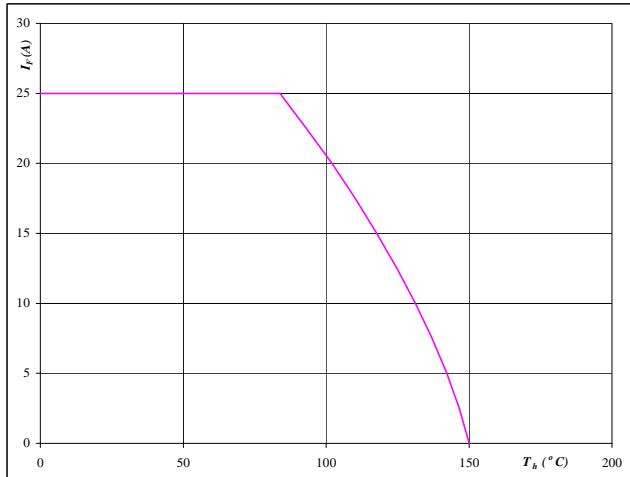
$$T_j = 150 \text{ }^{\circ}\text{C}$$

Rectifier diode

Figure 4

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$



$$T_j = 150 \text{ }^{\circ}\text{C}$$

Thermistor

Figure 1

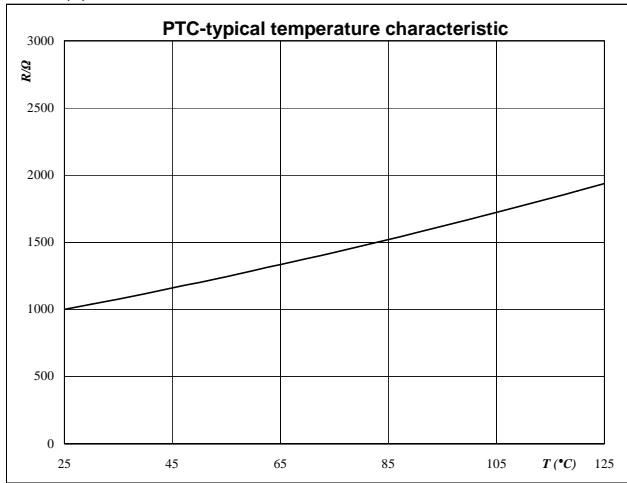
**Typical PTC characteristic
as a function of temperature**

$$R_T = f(T)$$

Thermistor
Thermistor

Equation of PTC resistance temperature dependency

$$R(T) = 1000 \Omega [1 + A*(T-25^\circ C) + B*(T-25^\circ C)^2] \quad [\Omega]$$



Switching Definitions Output Inverter

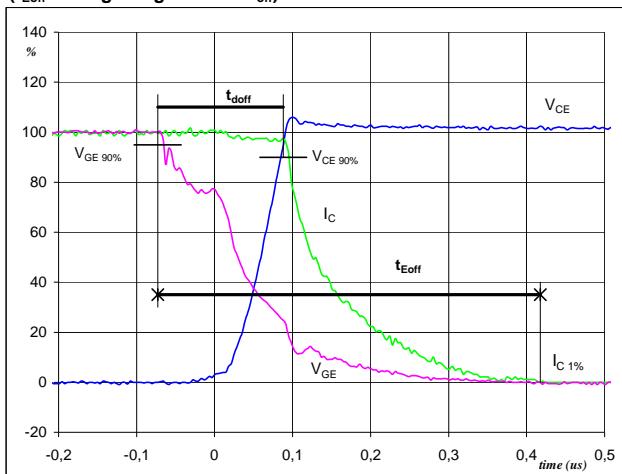
General conditions

T_j	=	150 °C
R_{gon}	=	32 Ω
R_{goff}	=	32 Ω

Figure 1

Output inverter IGBT

Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff}
 (t_{Eoff} = integrating time for E_{off})

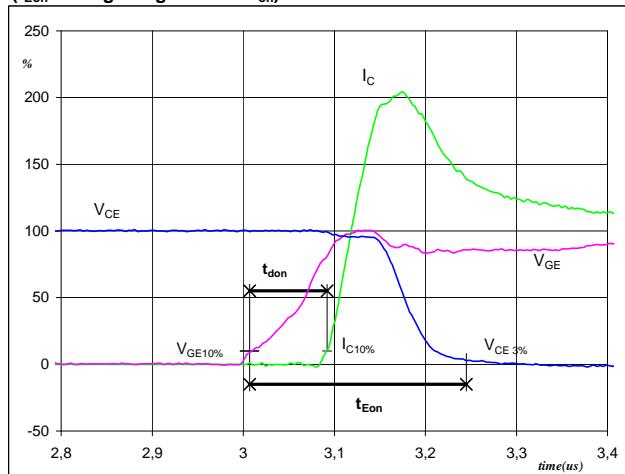


$V_{GE}(0\%) = -15$ V
 $V_{GE}(100\%) = 15$ V
 $V_C(100\%) = 300$ V
 $I_C(100\%) = 10$ A
 $t_{doff} = 0,15$ μs
 $t_{Eoff} = 0,49$ μs

Figure 2

Output inverter IGBT

Turn-on Switching Waveforms & definition of t_{don} , t_{Eon}
 (t_{Eon} = integrating time for E_{on})

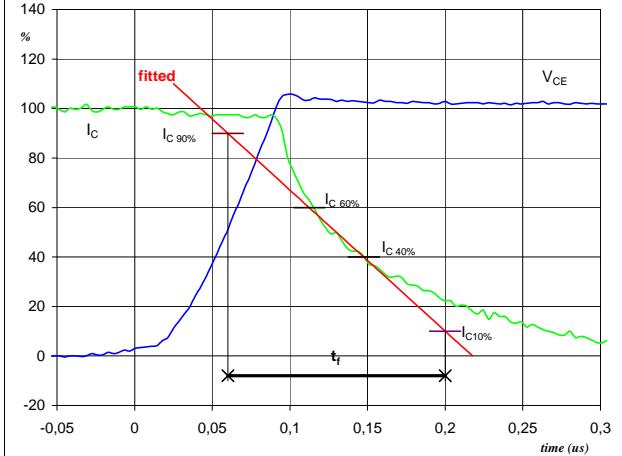


$V_{GE}(0\%) = -15$ V
 $V_{GE}(100\%) = 15$ V
 $V_C(100\%) = 300$ V
 $I_C(100\%) = 10$ A
 $t_{don} = 0,09$ μs
 $t_{Eon} = 0,24$ μs

Figure 3

Output inverter IGBT

Turn-off Switching Waveforms & definition of t_f

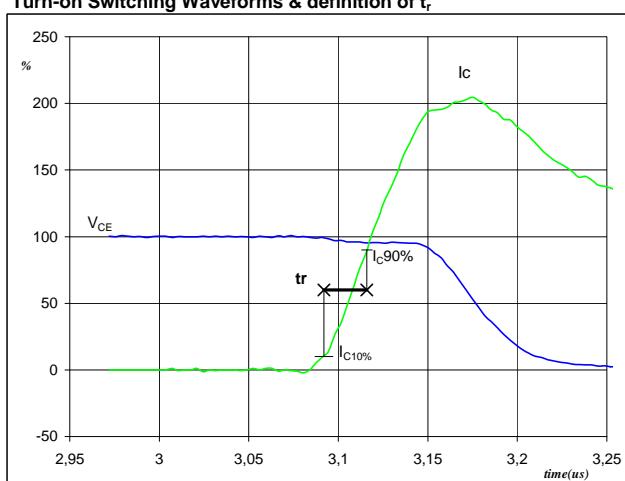


$V_C(100\%) = 300$ V
 $I_C(100\%) = 10$ A
 $t_f = 0,13$ μs

Figure 4

Output inverter IGBT

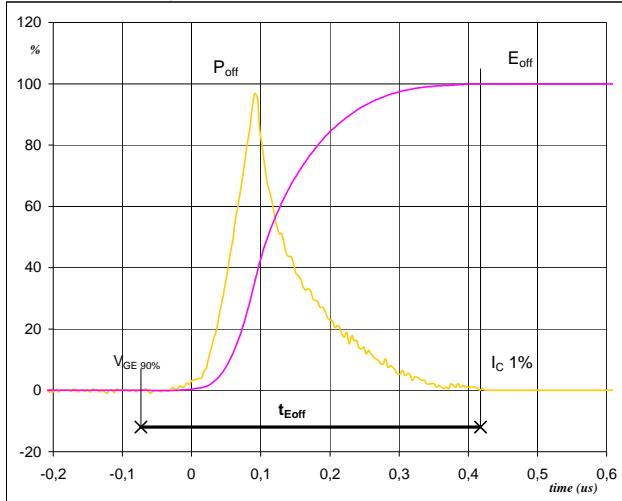
Turn-on Switching Waveforms & definition of t_r



$V_C(100\%) = 300$ V
 $I_C(100\%) = 10$ A
 $t_r = 0,02$ μs

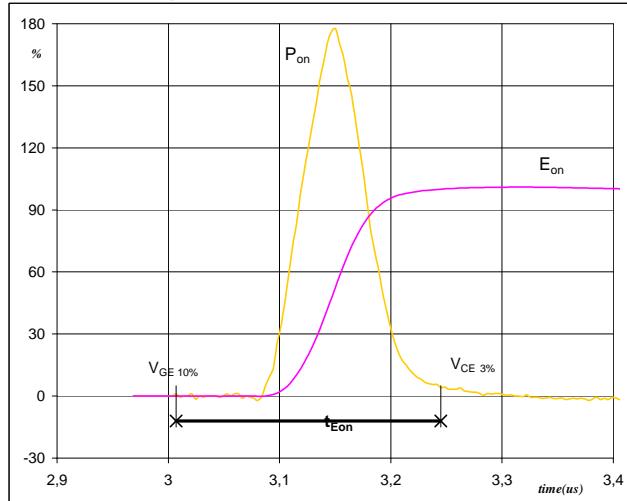
Switching Definitions Output Inverter

Figure 5 Output inverter IGBT
Turn-off Switching Waveforms & definition of t_{Eoff}



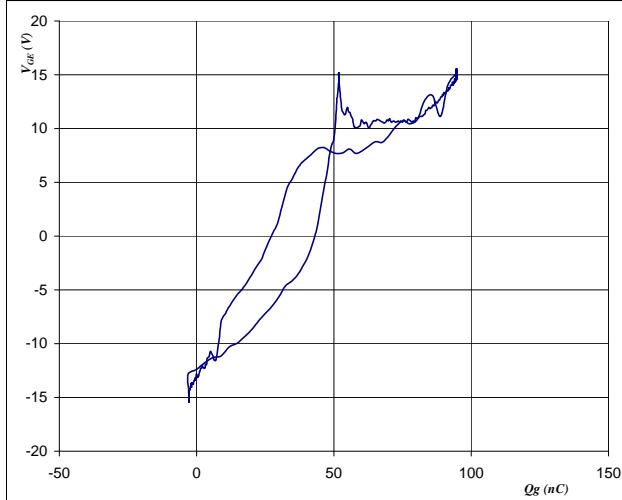
P_{off} (100%) = 3,01 kW
 E_{off} (100%) = 0,32 mJ
 t_{Eoff} = 0,49 μ s

Figure 6 Output inverter IGBT
Turn-on Switching Waveforms & definition of t_{Eon}



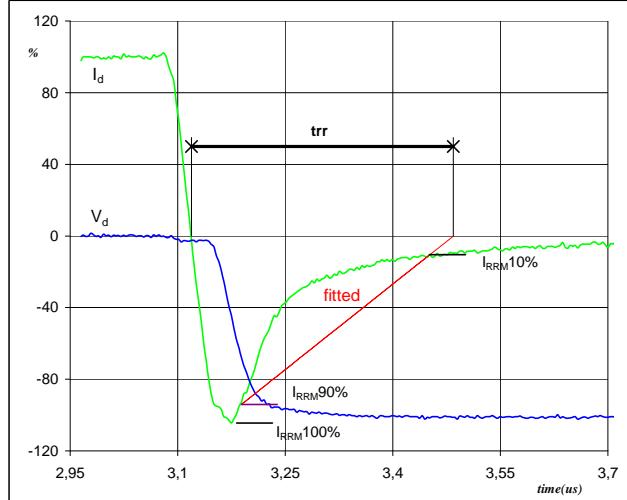
P_{on} (100%) = 3,01 kW
 E_{on} (100%) = 0,36 mJ
 t_{Eon} = 0,24 μ s

Figure 7 Output inverter FWD
Gate voltage vs Gate charge (measured)



V_{GEoff} = -15 V
 V_{GEon} = 15 V
 V_C (100%) = 300 V
 I_C (100%) = 10 A
 Q_g = 1073,31 nC

Figure 8 Output inverter IGBT
Turn-off Switching Waveforms & definition of t_{rr}



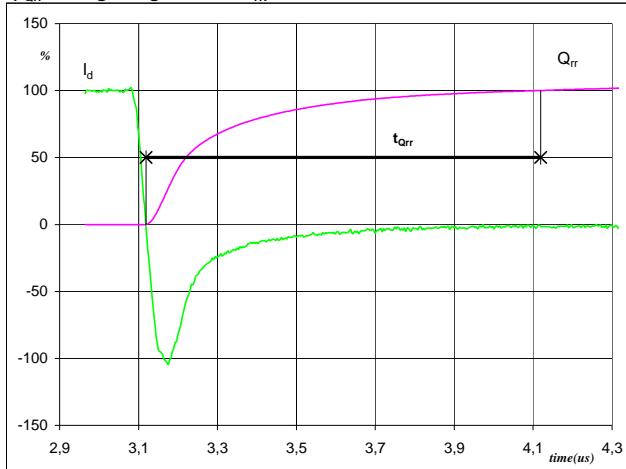
V_d (100%) = 300 V
 I_d (100%) = 10 A
 I_{RRM} (100%) = -10 A
 t_{rr} = 0,34 μ s

Switching Definitions Output Inverter

Figure 9

Output inverter FWD

Turn-on Switching Waveforms & definition of t_{Qrr}
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$

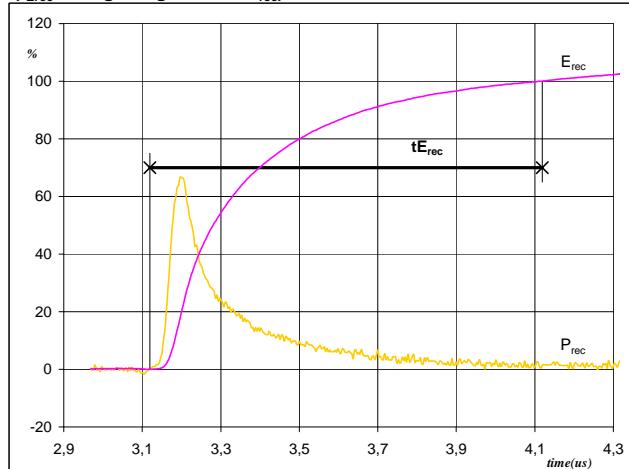


$I_d(100\%) = 10 \text{ A}$
 $Q_{rr}(100\%) = 1,50 \mu\text{C}$
 $t_{Qrr} = 1,00 \mu\text{s}$

Figure 10

Output inverter FWD

Turn-on Switching Waveforms & definition of t_{Erec}
 $(t_{Erec} = \text{integrating time for } E_{rec})$



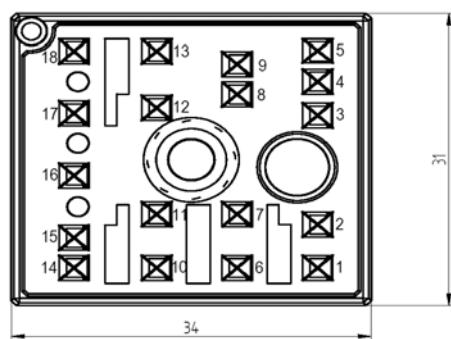
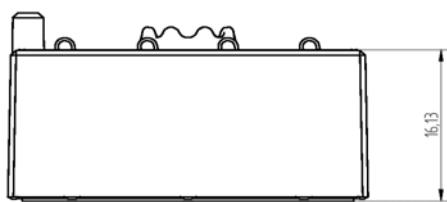
$P_{rec}(100\%) = 3,01 \text{ kW}$
 $E_{rec}(100\%) = 0,32 \text{ mJ}$
 $t_{Erec} = 1,00 \mu\text{s}$

Ordering Code and Marking - Outline - Pinout

Ordering Code & Marking

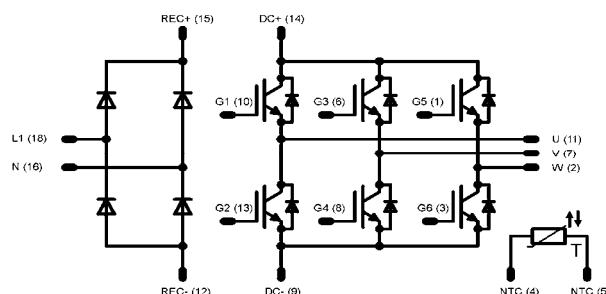
Version	Ordering Code	in DataMatrix as	in packaging barcode as
2-leg rectifier	80-M006PNB010SA01-K615D	K615D	K615D
3-leg rectifier	80-M006PNB010SA-K615C	K615C	K615C

Outline

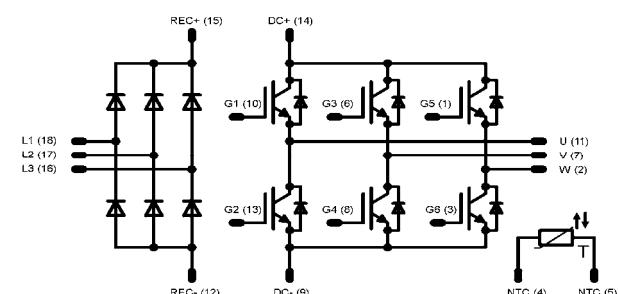


Pinout

for K61x-Dxx types



for K61x-Cxx types



PRODUCT STATUS DEFINITIONS

Datasheet Status	Product Status	Definition
Target	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. The data contained is exclusively intended for technically trained staff.
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