

# **SUR499H**

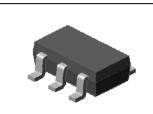
#### Epitaxial planar PNP silicon transistor

### **Descriptions**

• Dual chip digital transistor

#### **Features**

- Two SRA2205 chips in SOT-353 package
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process



Package: SOT-353

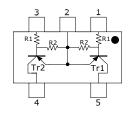
### **Ordering Information**

Type NO.	Marking	Package Code	
SUR499H	Y1□	SOT-353	

□ : Year & Week Code

## **Equivalent circuit & PIN Connections**

#### • Equivalent Circuit



	$\mathbf{R}_{1}$	$\mathbf{R}_2$
Tr1	2.2ΚΩ	47ΚΩ
Tr2	2.2ΚΩ	47ΚΩ

#### **PIN Connections**

- 1. IN 1
- 2. COMMON 1,2
- 3. IN 2
- 4. OUT 2
- 5. OUT 1

## **Absolute Maximum Ratings** [Tr1,Tr2]

 $(Ta=25^{\circ}C)$ 

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	V <sub>I</sub>	-15, 5	V
Output current	I <sub>O</sub>	-100	mA
Power dissipation	P <sub>D</sub> *	200	mW
Junction temperature	T <sub>3</sub>	150	°C
Storage temperature range	$T_{stg}$	-55 ~ 150	°C

\*: Total rating

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# SUR499H

# **Electrical Characteristics** [Tr1,Tr2]

(Ta=25°C)

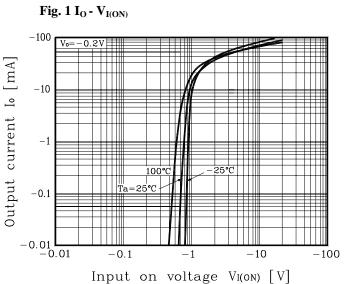
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I <sub>O(OFF)</sub>	V <sub>O</sub> =-50V, V <sub>I</sub> =0	-	-	-500	nA
DC current gain	$G_{\mathrm{I}}$	V <sub>O</sub> =-5V, I <sub>O</sub> =-10mA	80	200	-	1
Output voltage	V <sub>O(ON)</sub>	I <sub>O</sub> =-10mA, I <sub>I</sub> =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	$V_{I(ON)}$	V <sub>O</sub> =-0.2V, I <sub>O</sub> =-5mA	-	-	-1.1	٧
Input voltage (OFF)	V <sub>I(OFF)</sub>	V <sub>O</sub> =-5V, I <sub>O</sub> =-0.1mA	-0.5	-	-	V
Transition frequency	$f_T^*$	V <sub>O</sub> =-10V, I <sub>O</sub> =-5mA, f=1MHz	-	200	-	MHz
Input current	I <sub>I</sub>	$V_{\rm I}$ =-5V, $I_{\rm O}$ =0	-	-	-3.6	mA
Input resistor (Input to base)	R <sub>1</sub>	-	1.54	2.2	2.86	ΚΩ
Input resistor (Base to common)	R <sub>2</sub>	-	33	47	61	<b>K</b> Ω

<sup>\* :</sup> Characteristic of transistor only

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## **Electrical Characteristic Curves**

### [Tr1,Tr2]



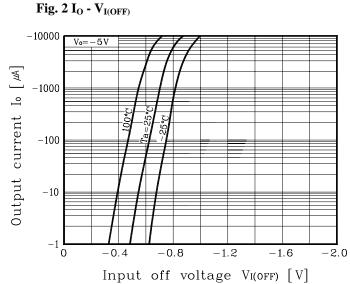
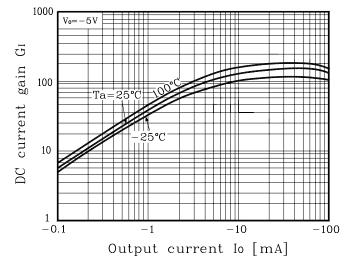
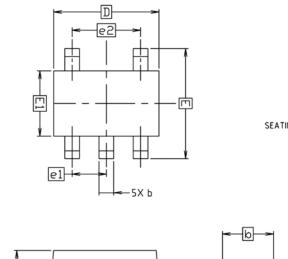


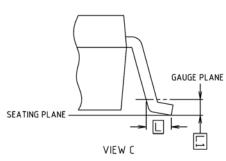
Fig. 3  $G_I$  -  $I_O$ 

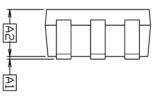


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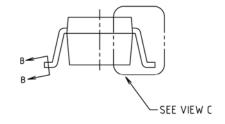
## **Outline Dimension**





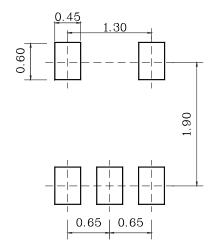






		MILLIMETERS		
SYMBOL	MINIMUM	NOMINAL	MAXIMUM	NOTE
A1	0.00	_	0.10	
A2	0.90	0.95	1.00	
Ь	0.25	_	0.40	
С	0.10	_	0.25	
D	1.90	2.00	2.10	
E	1.95	2.10	2.25	
E1	1.15	1.25	1.35	
e1	0.65 BSC			
e2	1.30 BSC			
L	0.25	_	_	
1.1	0.15 RSC			

### \* Recommend PCB solder land [Unit: mm]



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