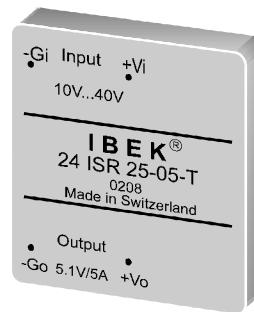


Single outputs

Non-isolated

- Ultra wide input voltage ranges
- Very high efficiency up to 93%
- Inhibit input (optional)
- Excellent dynamic load regulation
- Continuous no-load and short-circuit proof
- ISR15: Extended ambient temperature range -40...+85°C
- 2" x 2" case, h = 0.41"
- Made in Switzerland, accordingly ISO 9001:2000
- 2 years warranty



Electromagnetic emission EN 55022 < B

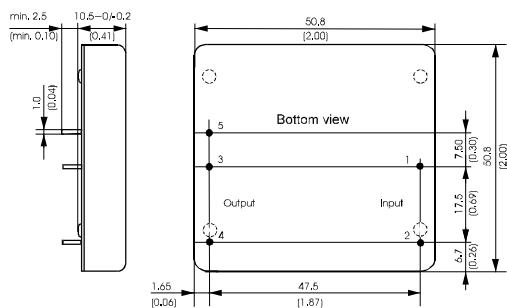
Conducted and radiated emissions

No load input current	15 mA max.
Input current in inhibit mode	3 mA max.
Switching frequency	150 kHz typ.
Output voltage accuracy	± 1%; 5.1 VDC output ± 1.4% max.
Static line regulation	± 0.3% typ.
Static load regulation FL to NL	0...0.7% typ.
Output voltage noise (20 MHz BW)	50 mVpp; 5.1 VDC output 30 mVpp typ.
Efficiency	87% to 93% typ.
Dynamic load regulation	ISR15: ± 100mV; 5.1 VDC output ± 80 mV max. ¹ ISR25: ± 150mV; 5.1 VDC output ± 100 mV max. ¹
Load transient recovery time	ISR15: 300 µs; 5.1 VDC output 100 µs typ. ¹ ISR25: 500 µs; 5.1 VDC output 200 µs typ. ¹
Inhibit input (i)	Open or apply -0.3...+0.8 VDC for normal operation. Apply +2.4...+5.5 VDC to switch off the output voltage.
Ambient temperature range	-T: -25...+71°C ² ; -S: -40...+85°C
Case temperature range	-T: -25...+95°C; -S: -40...+105°C
Storage temperature range	-T: -55...+105°C; -S: -55...+105°C
Case material	Metal with plastic bottom cover, UL94 V-0 rated
MTBF, GB @ 40°C case (MIL-HDBK-217F, Notice 2)	> 2.150.000 hours

¹ For 33% load variation with a rise/fall time of ≥ 50 µs

² 24 ISR25-05-T: Power derating 2.9%/°C above 60°C

Dimensions in mm (inches)



Standard Types	Input voltage VDC	Output VDC mA	
Single output 24 ISR15-05-T ³	8...40	5.1	3000
	16...40	12	1250
	19...40 (nominal 24)	15	1000
Single output 24 ISR25-05-T 24 ISR25-12-T 24 ISR25-15-T	10...40 ⁴	5.1	5000
	16...40	12	2100
	19...40 (nominal 24)	15	1700

³ Extended temperature range -S is available for all ISR15-types. Replace -T by -S on type number, e.g. 24 ISR15-05-S.

Attention: Operation in extended temperature range requires an external input capacitor of 220 µF, connected as close as possible to the input pins.

⁴ Down to 8 VDC at reduced output current of 2000 mA.

Modifications and customs are available upon request.

Pin Configuration	
Pin	Single output
1	+Vi
2	-Gi
3	+Vo
4	-Go
5 ⁵	i

⁵ Optional inhibit pin. If required add i to type number, e.g. 24 ISR15-05-Ti.

Specifications subject to change without notice