

Product Features

- Solid-state linear amplifier design
- GaN on SiC HEMT
- Small and light weight
- Wide Band Operation 500 ~ 2500MHz
- 50 Ohm Input/Output impedance matched
- Highly reliable and rugged design
- Harsh environmental condition
- High efficiency
- 10W typical Psat

Applications

- Broadband communication
- Broadcasting
- General purpose RF amplifier
- Linear applications in the L/S Frequency Bands

**Description**

RUP15010-10 has been designed for RF system application frequencies from 500 ~ 2500MHz.

This Pallet Amplifier uses GaN on SiC HEMT technology which performs high breakdown voltage, high linearity, wide bandwidth and high efficiency.

Electrical Specifications @ VDD=28VDC, T=25°C, 50Ω System

PARAMETER	UNIT	MIN	TYP	MAX	SYMBOL
Operating Frequency	MHz	500	-	2500	f_o
Operating Bandwidth	MHz	-	2000	-	BW
Output Power CW	W	-	10	-	P_{SAT}
Output Power @ P3dB G.C.P	W	-	5	-	P_{3dB}
Small Signal Gain	dB	13	17	-	G_S
Small Signal Gain Flatness	dB	-	± 1.5	± 2.0	ΔG_S
Input VSWR	-	-	-	8.5 : 1	S_{11}
Harmonics @ P1dB G.C.P	dBc	10	-	-	H_{P1dB}
Spurious Signals	dBc	60	70	-	Spur
Operating Voltage	V	27	28	30	V
Supply Current @ P_{sat}	A	-	1.0	-	I_{DD}
Supply Current @ P_{3dB}	A	-	0.7	-	I_{DD}

* Please DO NOT ENTER RF INPUT POWER OVER +39dBm. (to prevent the main transistor from damaging)

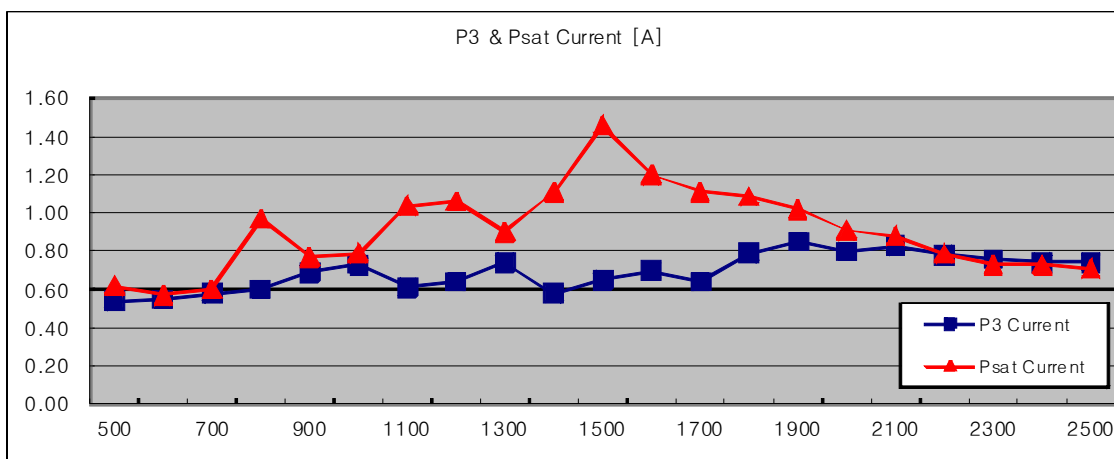
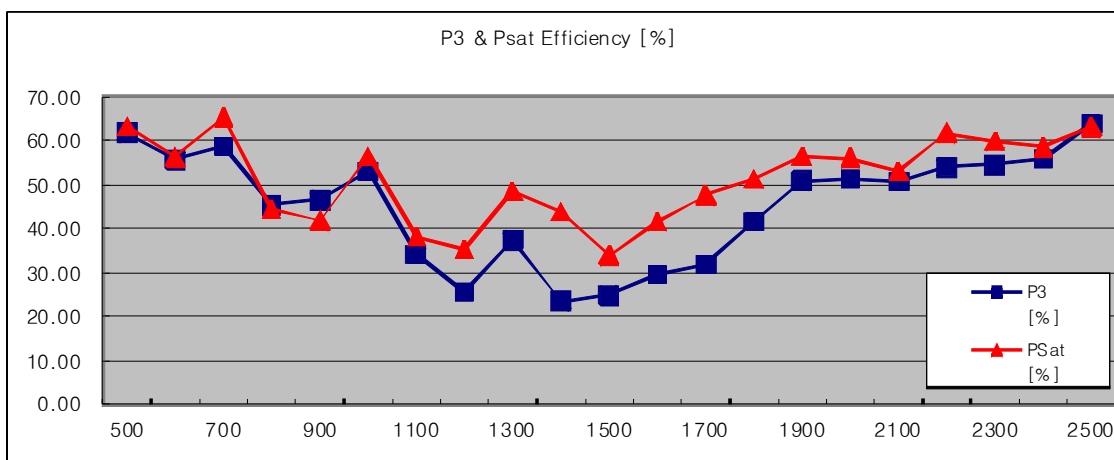
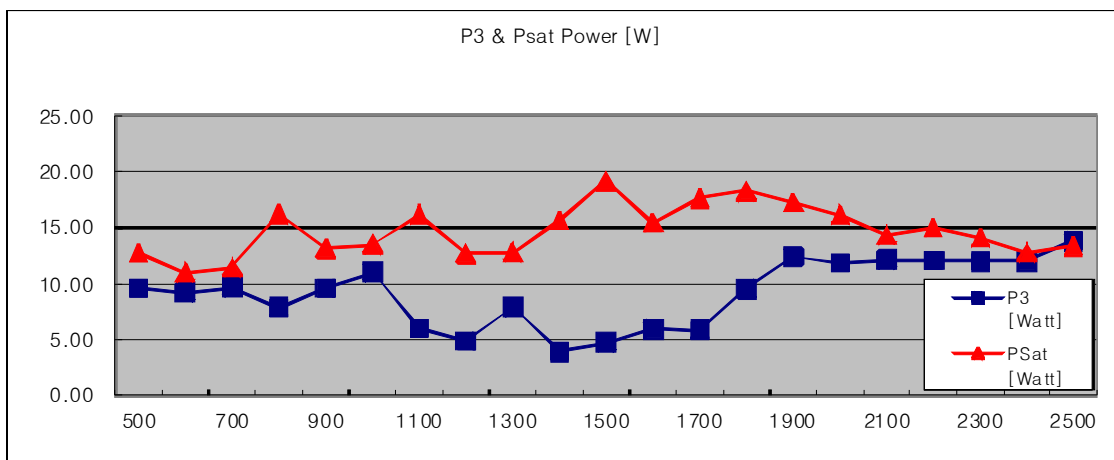
Environmental Characteristics

PARAMETER	UNIT	MIN	TYP	MAX	SYMBOL
Operating Case Temperature	°C	0	-	70	T_C
Storage Temperature	°C	-40	-	85	T_S

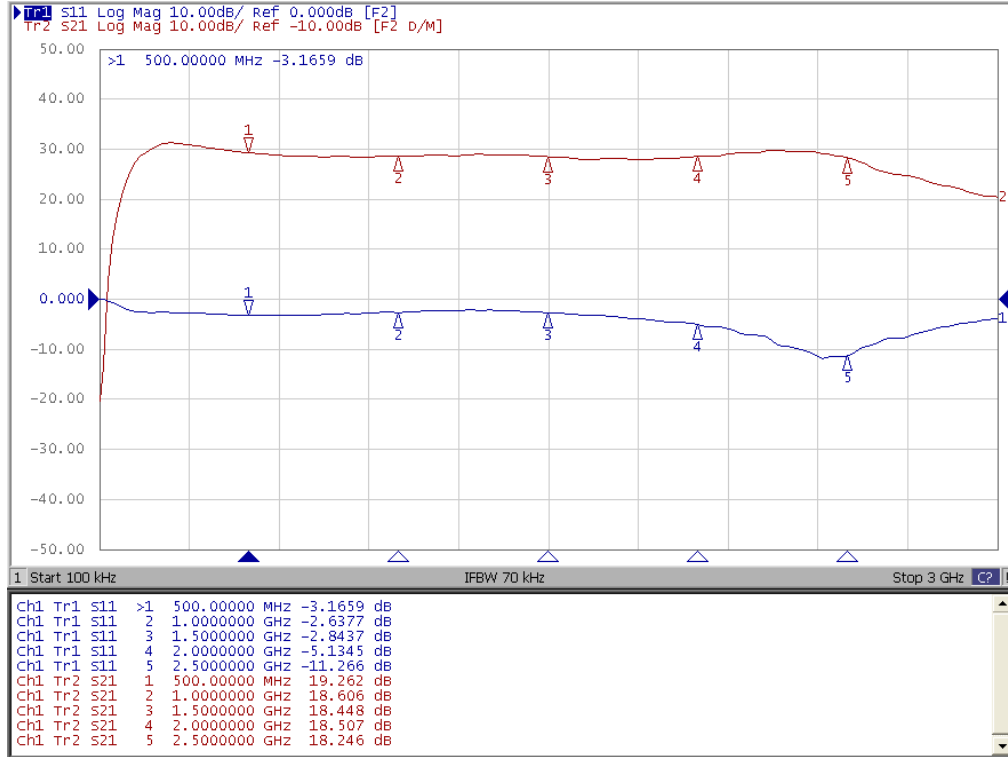
Typical Performance @ 25°C

Freq.	P3 Output Power, Current, Efficiency				Psat Output Power, Current, Efficiency			
	Output	Output	Current	Efficiency	Output	Output	Current	Efficiency
MHz	dBm	W	A	%	dBm	W	A	%
500	39.83	9.62	0.54	61.83	41.05	12.74	0.62	63.23
600	39.66	9.25	0.55	55.76	40.42	11.02	0.57	56.31
700	39.86	9.68	0.58	58.67	40.60	11.48	0.60	65.41
800	38.96	7.87	0.60	45.43	42.10	16.22	0.97	44.51
900	39.82	9.59	0.69	46.58	41.19	13.15	0.77	41.84
1000	40.43	11.04	0.73	52.99	41.31	13.52	0.79	56.40
1100	37.81	6.04	0.61	34.08	42.09	16.18	1.04	38.16
1200	36.87	4.84	0.64	25.52	41.04	12.71	1.06	35.28
1300	38.98	7.91	0.74	37.47	41.07	12.79	0.90	48.51
1400	35.97	3.95	0.58	23.54	41.95	15.67	1.11	43.87
1500	36.78	4.76	0.65	24.79	42.83	19.91	1.46	33.83
1600	37.73	5.93	0.70	29.51	41.88	15.42	1.20	41.66
1700	37.70	5.89	0.64	31.81	42.46	17.62	1.11	47.60
1800	39.80	9.55	0.79	41.63	42.62	18.28	1.09	51.28
1900	40.94	12.42	0.85	50.96	42.37	17.26	1.02	56.45
2000	40.75	11.89	0.80	51.38	42.09	16.18	0.91	56.16
2100	40.84	12.13	0.83	50.78	41.58	14.39	0.88	53.07
2200	40.81	12.05	0.78	53.97	41.76	15.00	0.79	61.82
2300	40.78	11.97	0.76	54.48	41.50	14.13	0.73	59.98
2400	40.79	11.99	0.74	55.84	41.08	12.82	0.73	58.57
2500	41.39	13.77	0.74	63.98	41.26	13.37	0.71	63.12

Typical Performance @ 25°C



Small Signal Gain @ Input Power : -10dBm, 25°C



Precautions

This product is a Wideband Pallet Amplifier based on a Gallium Nitride Transistor.

The Gallium Nitride Transistor requires a Negative Voltage Bias which operates alongside a Positive Voltage Bias. These Biases are applied in accordance to the Sequence during Turn-On and Turn-Off.

The Pallet Amplifier does not have a built-in Bias Sequence Circuit. Therefore, users need to either apply positive voltages and negative voltages in the required sequence, or add an external Bias Circuit to this Amplifier.

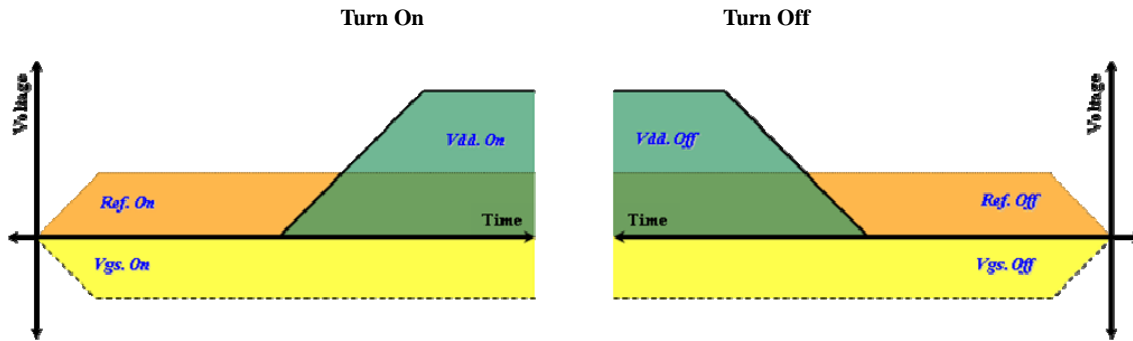
The required sequence for power supply is as follows.

During Turn-On

1. Connect GND to Pin 4(GND).
2. Apply 5V to Pin 2(Ref.).
3. Apply -5V to Pin 3(V_{GS}).
4. Apply 28V to Pin 1(V_{DD}).
5. Turn on the pin 2 and pin 3, then turn on the pin 1.
6. Apply the RF Power.

During Turn-Off

1. Turn off RF power.
2. Turn off pin 1, then turn off the pin 3 and pin 2.
3. Remove all connections.

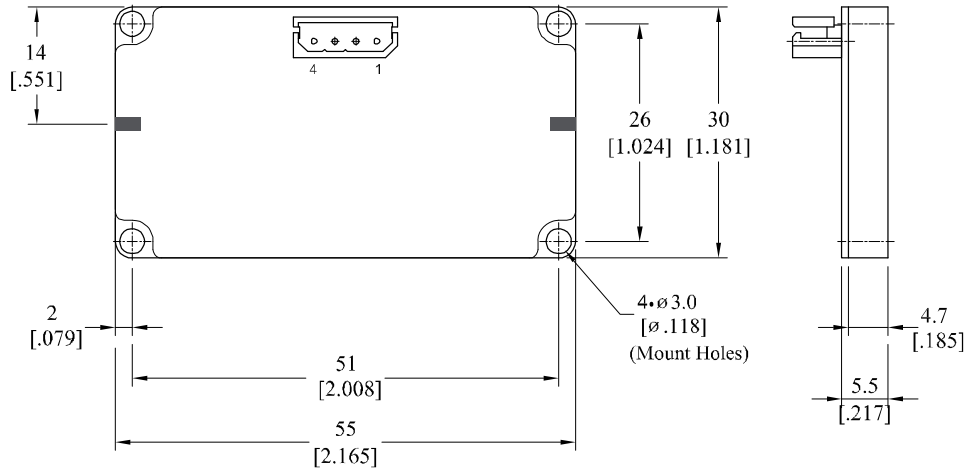


Sequence Timing Diagram

Mechanical Specifications

PARAMETER	UNIT	VALUE	LIMIT
Dimensions (L x W x H)	mm	55.0 x 30.0 x 16.0	Max
RF Connectors In/Out	-	Available SMA Female	-
Cooling	-	External Heat sink + airflow	-

Outline Drawing

* Unit: mm[inch] | Tolerance ± 0.15 [.006]

Pin Description

Pin No	Description	Specifications
1	V_{DD}	+28 Drain Voltage
2	Ref.	+5V Op-Amp Operate Voltage
3	V_{GS}	-5V Gate Voltage
4	GND	Ground

Revision History

Part Number	Release Date	Version	Modification	Data Sheet Status
RUP15010-10	2012.02.18	2.0	TR Package Change	-
RUP15010-10	2010.09.07	1.0	-	-

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