VRG8657/VRG8658

Low Drop Out(LDO)Adjustable Voltage Regulators

Dual Positive Radiation Tolerant

www.aeroflex.com/voltreg

June 21, 2010





FEATURES

- ☐ Radiation performance
 - Total dose \geq 100 krad (Si)
- □ Two-Independent voltage regulators
- □ Thermal shutdown
- □ Output voltage adjustable: 1.25V to 23V
- □ Dropout voltage: 1.3V at 1.0Amp
- □ 3-Terminal
- □ Output current: 1.0A (See Note 1 Pg 2)
 □ Voltage reference: 1.25V +2%, -3.2%
- □ Load regulation: 0.3% max□ Line regulation: 0.25% max□ Ripple rejection: >60dB

- □ Packaging Hermetic metal
 - Thru-hole or Surface mount
 - 6 Leads, .42"L x .65"W x .200"Ht
 - Power package
 - Weight 5 gm max
- Designed for aerospace and high reliability space applications
- □ Available on DSCC SMD: 5962-09201

DESCRIPTION

The Aeroflex Plainview VRG8657/8658 consists of two Positive Adjustable (RH1086) LDO voltage regulators each capable of supplying 1.0Amps over the output voltage range as defined under recommended operating conditions. The VRG8657/8658 offers excellent line and load regulation specifications and ripple rejection. There is full electrical isolation between the regulators and each regulator to the package.

Dropout (VIN - VOUT) decreases at lower load currents for both regulators.

The VRG8657/8658 serves a wide variety of applications including SCSI-2 Active Terminator, High Efficiency Linear Regulators, Post Regulators for Switching Supplies, Constant Current Regulators, Battery Chargers and Microprocessor Supply.

The VRG8657/8658 has been specifically designed to meet exposure to radiation environments. The VRG8657 is configured for a Thru-Hole 6 lead metal power package and the VRG8658 is configured for a Surface Mount 6 lead metal power package. It is guaranteed operational from -55°C to +125°C. Available screened to MIL-STD-883, the VRG8657/8658 is ideal for demanding military and space applications.

For detailed performance characteristic curves, applications information and typical applications see the latest Linear Technology Corporation®data sheets for their RH/LT1086, which is available on-line at www.linear.com.

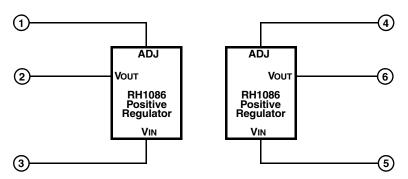


FIGURE 1 – BLOCK DIAGRAM / SCHEMATIC

ABSOLUTE MAXIMUM RATINGS

PARAMETER	RANGE	UNITS
Input Voltage	25+VREF	VDC
Lead temperature (soldering 10 Sec)	300	°C
Input Output Differential	25	VDC
Output Voltage	+25	VDC
DC Output Current	1.5	A
ESD (MIL-STD-883, M3015, Class 3A)	>4000	V
Operating Junction Temperature Range	-55 to +150	°C
Storage Temperature Range	-65 to +150	°C

NOTICE: Stresses above those listed under "Absolute Maximums Rating" may cause permanent damage to the device. These are stress rating only; functional operation beyond the "Operation Conditions" is not recommended and extended exposure beyond the "Operation Conditions" may effect device reliability.

RECOMMENDED OPERATING CONDITIONS

PARAMETER	RANGE	UNITS
Output Voltage Range	1.275 to 23	VDC
Input Output Differential 3/	1.5 to 25	VDC
Case Operating Temperature Range	-55 to +125	°C
Thermal Resistance, Junction to case θ _{JC}	5	°C/W

ELECTRICAL PERFORMANCE CHARACTERISTICS

Unless otherwise specified, -55°C≤TC≤+125°C

PARAMETER	SYM	CONDITIONS (P≤PMAX)	MIN	MAX	UNITS
Reference Voltage 2/3/	Vref	$1.5 \text{V} \le \text{Vin}$ - $\text{Vout} \le 15 \text{V}$, $10 \text{mA} \le \text{Iout} \le 1.0 \text{A}$	1.210	1.275	V
Line Regulation 2/3/	$\frac{\Delta Vout}{\Delta Vin}$	Iload = 10mA , $1.5\text{V} \le \text{Vin}$ - $\text{Vout} \le 15\text{V}$	1	0.25	%
Load Regulation 2/3/	ΔV OUT ΔI OUT	$10\text{mA} \le \text{Iout} \le 1.0\text{A}, \text{Vin} - \text{Vout} = 3\text{V}$	1	0.3	%
Dropout Voltage 2/4/	VDROP	$\Delta V_{REF} = 1\%$, IOUT = 1.0A	-	1.30	V
Adjust Pin Current 2/	-		-	120	μA
Adjust Pin Current Change 2/	ı	$10~\text{mA} \leq \text{IOUT} \leq 1\text{A},~1.5\text{V} \leq \text{Vin} - \text{Vout} \leq 15\text{V}$	-	5	μA
Current Limit 2/ 6/	IMAX	V_{IN} - $V_{OUT} = 5V$ V_{IN} - $V_{OUT} \le 25V$	1.5 0.047		A A
Minimum Load Current <u>5</u> /	IMIN	V_{IN} - $V_{OUT} = 25V$	-	10	mA
Ripple Rejection 3/	ı	$\begin{aligned} &\text{Iout} = 1.0\text{A, Vin - Vout} = 3\text{V, f} = 120\text{Hz,} \\ &\text{Cadj} = \text{Cout} = 25\mu\text{F} \end{aligned}$	60	1	dB
Thermal Regulation	-	30ms pulse, $TC = +25^{\circ}C$	-	0.04	%/W
VREF Long-Term Stability <u>5</u> /	-	Burn In: $TC = +125^{\circ}C$ @ 1000hrs minimum, tested @ 25°C	-	0.3	%

Notes

- 1/ For Compliance with MIL-STD-883 Rev. C current density specification, the RH1086MK is derated to 1.0 Amp but is capable of 1.5Amps.
- 2/ Specification derated to reflect Total Dose exposure to 100Krad (Si) @ +25°C.
- 3/ Line and load regulation are guaranteed up to the maximum power dissipation of 15W. Power dissipation is determined by the input/output differential voltage and the output current. Guaranteed maximum power dissipation will not be available over the full input/output voltage range.
- 4/ Dropout voltage is specified over the full output current range of the device.
- 5/ Not tested. Shall be guaranteed by design, characterization, or correlation to other tested parameters.
- 6/ Pulsed @ <10% duty cycle @ +25°C (See Note 1).

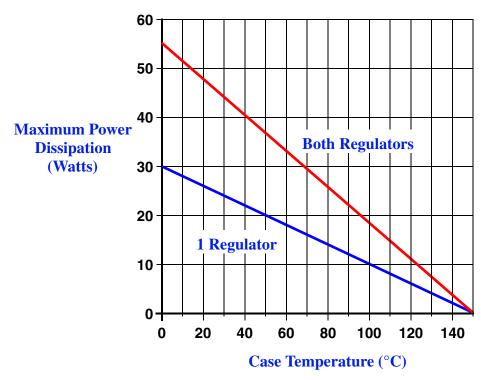
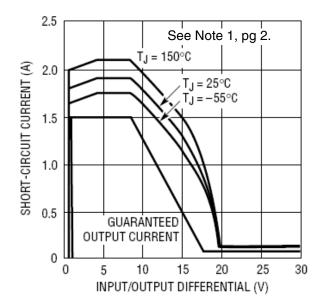


FIGURE 2 – MAXIMUM POWER vs CASE TEMPERATURE

The maximum Power dissipation is limited by the thermal shutdown function of each regulator chip in the VG8657/8658. The graph above represents the achievable power before the chip shuts down. The first line in the graph represents the maximum power dissipation of the VG8657/8658 with one regulator on (the other off) and the other line represents both regulators on dissipating equal power. If both regulators are on and one regulator is dissipating more power that the other, the maximum power dissipation of the VG8657/8658 will fall between the two lines. This graph is based on the maximum junction temperature of 150° C and a thermal resistance (Θ JC) of 5° C/W.



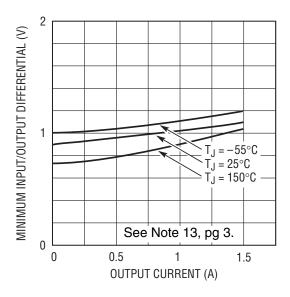


FIGURE 3 - RH1086 SHORT CIRCUIT CURRENT

FIGURE 4 – RH1086 DROPOUT VOLTAGE TYPICAL CURVE

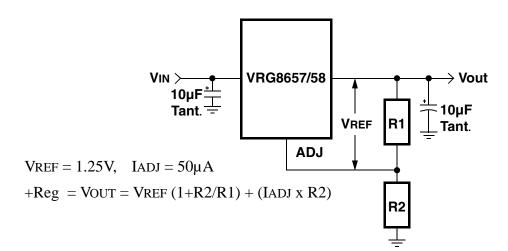
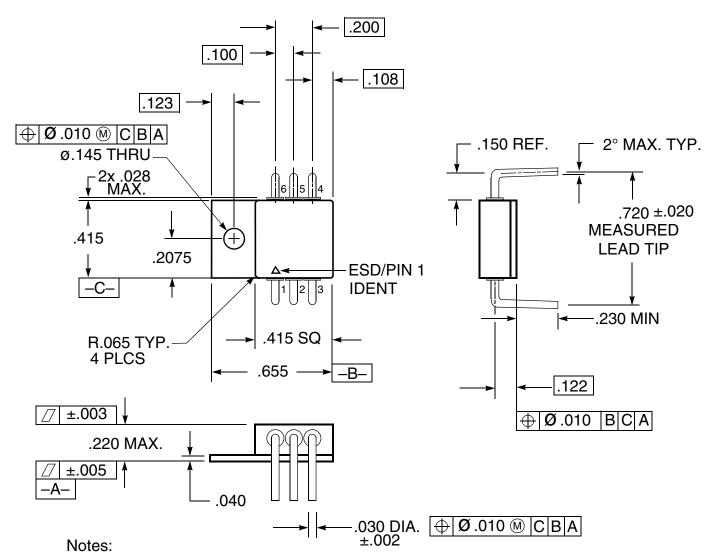


FIGURE 5 – BASIC RH1086 ADJUSTABLE REGULATOR APPLICATION

TABLE I – PIN NUMBERS vs FUNCTION

PIN	FUNCTION
1	POS_ADJ_1
2	POS_VOUT_1
3	POS_VIN_1
4	POS_ADJ_2
5	POS_VIN_2
6	POS_VOUT_2

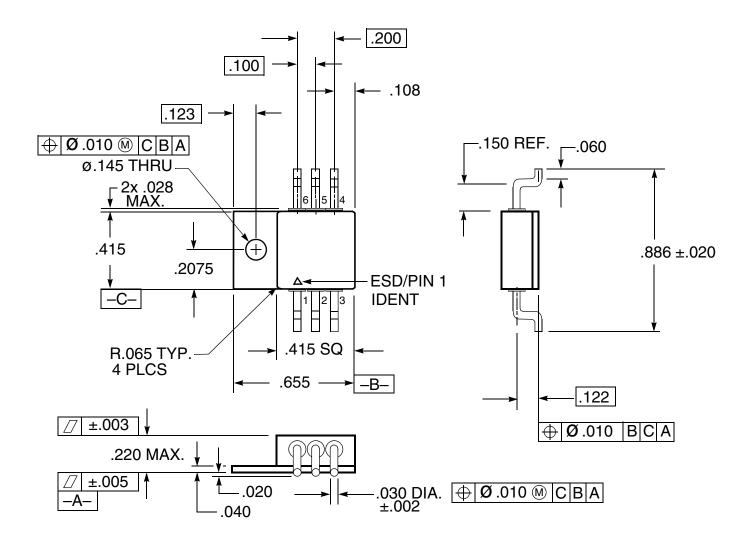


- 1. Dimension Tolerance: ±.005 inches
- 2. Package contains BeO substrate
- 3. Case electrically isolated

FIGURE 6 - PACKAGE OUTLINE — THRU-HOLE POWER PACKAGE

TABLE II - PIN NUMBERS vs FUNCTION

PIN	FUNCTION
1	POS_ADJ_1
2	POS_VOUT_1
3	POS_VIN_1
4	POS_ADJ_2
5	POS_VIN_2
6	POS_VOUT_2



Notes:

- 1. Dimension Tolerance: ±.005 inches
- 2. Package contains BeO substrate
- 3. Case electrically isolated

FIGURE 7 - PACKAGE OUTLINE — SURFACE MOUNT POWER PACKAGE

ORDERING INFORMATION

MODEL	DSCC SMD #	SCREENING	PACKAGE	
VRG8657-S		Military Temperature, -55°C to +125°C Screened in accordance with MIL-PRF-38534, Class K.	6 Lead Thru-Hole	
VRG8657-7		Commercial Flow, +25°C testing only	Power Pkg	
VRG8658-S	-	Military Temperature, -55°C to +125°C Screened in accordance with MIL-PRF-38534, Class K	6 Lead Surface Mount Power Pkg	
VRG8658-7		Commercial Flow, +25°C testing only		
VRG8657-201-1S VRG8657-201-2S	5962-0920102KXC 5962-0920102KXA	In accordance with DSCC SMD	6 Lead Thru-Hole Power Pkg	
VRG8658-201-1S VRG8658-201-2S	5962-0920102KYC 5962-0920102KYA		6 Lead Surface Mount Power Pkg	

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