

Surface Mount Super Fast Recovery Rectifiers

Features

- Glass passivated chip junctions
- Ideal for automated placement
- Ultrafast reverse recovery time for high efficiency
- Low profile package
- High forward surge capability
- High temperature soldering: 260°C/10 seconds at terminals
- Component in accordance to RoHS 2002/95/1 and WEEE 2002/96/EC

Mechanical Date

- **Case:** JEDEC MSMA molded plastic body over glass passivated chip
- Terminals: Solder plated, solderable per J-STD-002B and JESD22-B102D
- Polarity: Laser band denotes cathode end

Maximum Ratings & Thermal Characteristics

Po

MASF2A~MASF2J

Major Ratings and Characteristics

I _{F(AV)}	2.0 A
V _{RRM}	50 V to 600 V
I _{FSM}	50 A
t _{rr}	35 nS
V _F	0.95 V, 1.25 V, 1.7 V
T _j max.	150 °C

$(T_A = 25 \text{ °C unless otherwise noted})$									
Items	Symbol	MASF 2A	MASF 2B	MASF 2C	MASF 2D	MASF 2E	MASF 2G	MASF 2J	UNIT
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	150	200	300	400	600	V
Maximum RMS voltage	V _{RMS}	35	70	105	140	210	280	420	V
Maximum DC blocking voltage	V _{DC}	50	100	150	200	300	400	600	V
Maximum average forward rectified current	I _{F(AV)}	2.0					А		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	50					А		
Thermal resistance from junction to lead ⁽¹⁾	R _{θJL}	35					°C /W		
Operating junction and storage temperature range	T _{J,} T _{STG}	G −55 to +150					°C		

Note 1: Mounted on P.C.B. with 0.2 x 0.2" (5.0 x 5.0mm) copper pad areas.

Electrical Characteristics (T_A = 25 °C unless otherwise noted)

ltems	Test conditions		Symbol	MASF2A ~ MASF2D	MASF2E~ MASF2G	MASF2J	UNIT
Instantaneous forward voltage	I _F =2.0A ⁽²⁾		V _F	0.95	1.25	1.70	V
Reverse current	V _R =V _{DC}	T _A =25℃ T _A =125℃	I _R	5 100			μA
Reverse recovery time	$I_{F} = 0.5 \text{ A}$, $I_{rr} = 0$	I _R = 1.0 A , 0.25 A	t _{rr}	35			nS
Typical junction capacitance	4.0V,1.0MHz		CJ	20	15		pF

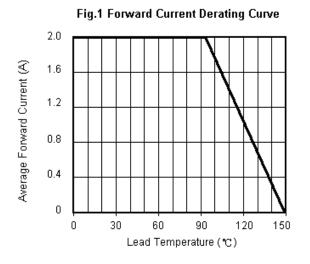
Note 2: Pulse test:300 μs pulse width,1% duty cycle.

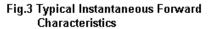




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Characteristic Curves (T_A=25 °C unless otherwise noted)





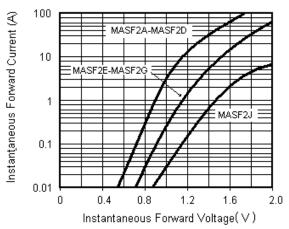
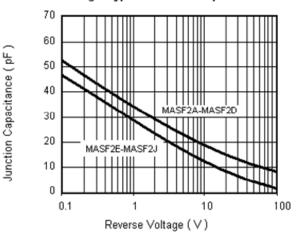


Fig.5 Typical Junction Capacitance



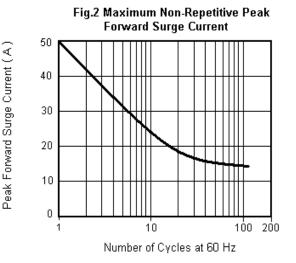
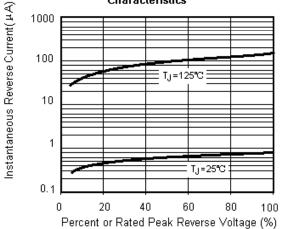


Fig.4 Typical Reverse Leakage Characteristics



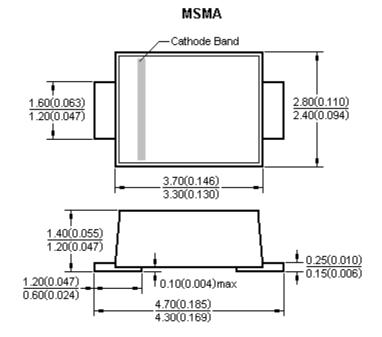
州市钜兴电子有限之

GUANGZHOU JUXING ELECTRONICS CO



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Package Outline



Dimentsions in millimeters and (inches)

Notice

- Product is intended for use in general electronics applications.
- Product should be worked less than the ratings; if exceeded, may cause permanent damage.or introduce latent failure mechanisms.
- The absolute maximum ratings are rated values and must not be exceeded during operation. The following are the general derating methods you design a circuit with a device.
 - $I_{\mathsf{F}(\mathsf{AV})}$: We recommend that the worst case current be no greater than 80% .
 - I_{FSM} : This rating specifies the non-repetitive peak current. This is only applied for an abnormal operation, which the general during the lifespan of the device.
 - T_J : Derate this rating when using a device in order to ensure high reliability. We recommend that the device be used at a T_J of below 125°C.
- TRR is registered trademark of Rising-sun Technology. Rising-sun Technology reserves the right to make changes to any product in this
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- Rising-sun Technology does not assure any liability arising out of the applications or any product described in this specification.
- Rising-sun Technology advises customers to obtain the latest version of the device information before placing orders to verify that the
- required information is current.