



SPN4428

N-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPN4428 is the N-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application , notebook computer power management and other battery powered circuits where high-side switching .

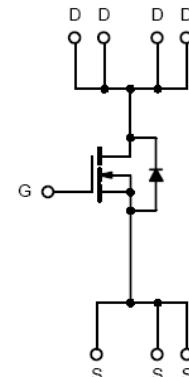
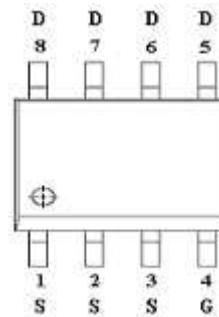
FEATURES

- ◆ 20V/14A,R_{DS(ON)}= 20mΩ@V_{GS}= 4.5V
- ◆ 20V/7.0A,R_{DS(ON)}= 28mΩ@V_{GS}= 2.5V
- ◆ Super high density cell design for extremely low RDS (ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOP – 8P package design

APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

PIN CONFIGURATION(SOP – 8P)



PART MARKING





SPN4428

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PIN DESCRIPTION

Pin	Symbol	Description
1	S	Source
2	S	Source
3	S	Source
4	G	Gate
5	D	Drain
6	D	Drain
7	D	Drain
8	D	Drain

ORDERING INFORMATION

Part Number	Package	Part Marking
SPN4428S8RGB	SOP- 8P	SPN4428

※ SPN4428S8RGB : 13" Tape Reel ; Pb – Free ; Halogen - Free

ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V _{DSS}	20	V
Gate –Source Voltage	V _{GSS}	+20	V
Continuous Drain Current(T _J =150°C)	T _A =25°C	9	A
	T _A =70°C		
Pulsed Drain Current	I _{DM}	30	A
Continuous Source Current(Diode Conduction)	I _S	2.3	A
Power Dissipation	T _A =25°C	2.8	W
	T _A =70°C		
Operating Junction Temperature	T _J	-55/150	°C
Storage Temperature Range	T _{STG}	-55/150	°C
Thermal Resistance-Junction to Ambient	R _{θJA}	80	°C/W



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ELECTRICAL CHARACTERISTICS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, ID=250uA	20			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , ID=250uA	0.5		1.2	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±16V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =16V, V _{GS} =0V V _{DS} =16V, V _{GS} =0V T _J =85°C			1 5	uA
On-State Drain Current	I _{D(on)}	V _{DS} ≥5V, V _{GS} =10V	25			A
Drain-Source On-Resistance	R _{DSS(on)}	V _{GS} =4.5V, ID=14A V _{GS} =2.5V, ID=7.0A		0.016 0.022	0.020 0.028	Ω
Forward Transconductance	g _f	V _{DS} =15V, ID=6.2A		30		S
Diode Forward Voltage	V _{SD}	I _S =2.3A, V _{GS} =0V		0.8	1.2	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} =15V, V _{GS} =4.5V ID=14A		9.8		nC
Gate-Source Charge	Q _{gs}			2.1		
Gate-Drain Charge	Q _{gd}			3		
Input Capacitance	C _{iss}	V _{DS} =15, V _{GS} =0V f=1MHz		772		pF
Output Capacitance	C _{oss}			83		
Reverse Transfer Capacitance	C _{rss}			79		
Turn-On Time	t _{d(on)}	V _{DD} =10V, ID=14A, V _{GS} =4.5V, R _G =3.3Ω		4		nS
	t _r			12.5		
Turn-Off Time	t _{d(off)}			20		
	t _f			8		



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TYPICAL CHARACTERISTICS

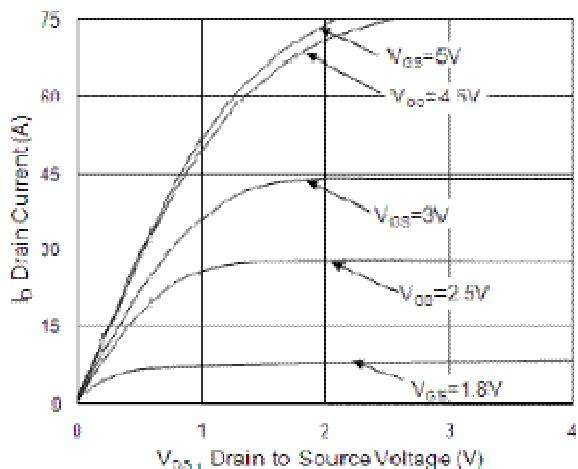


Fig. 1 Typical Output Characteristics

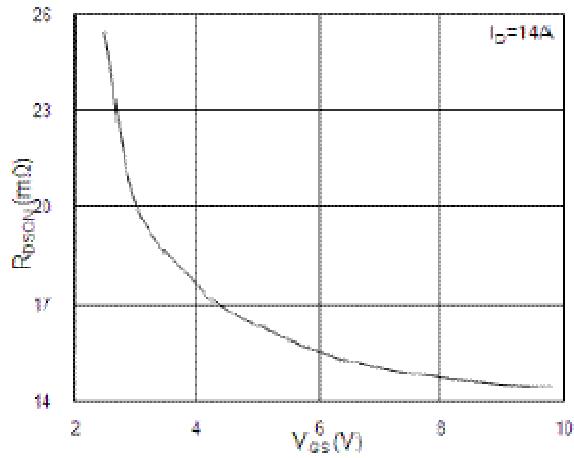


Fig. 2 On-Resistance vs Gate Voltage

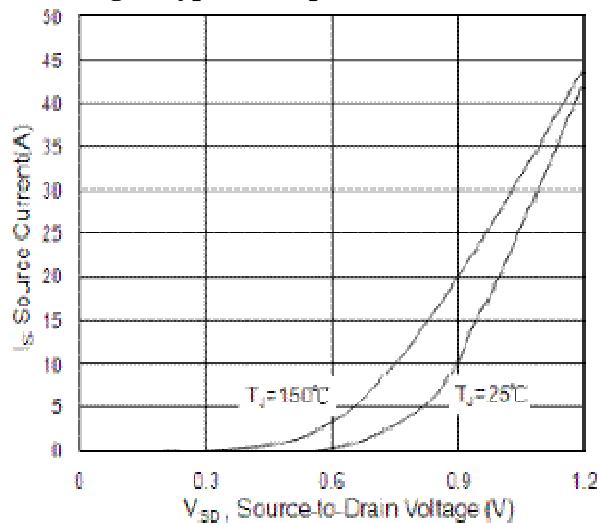


Fig. 3 Forward Characteristics

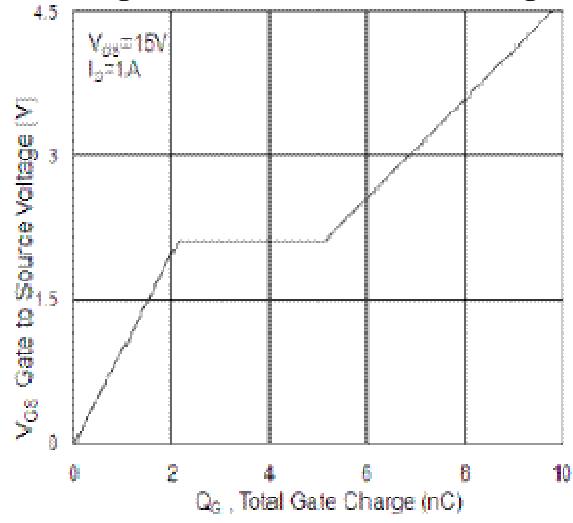


Fig. 4 Total Gate Charge

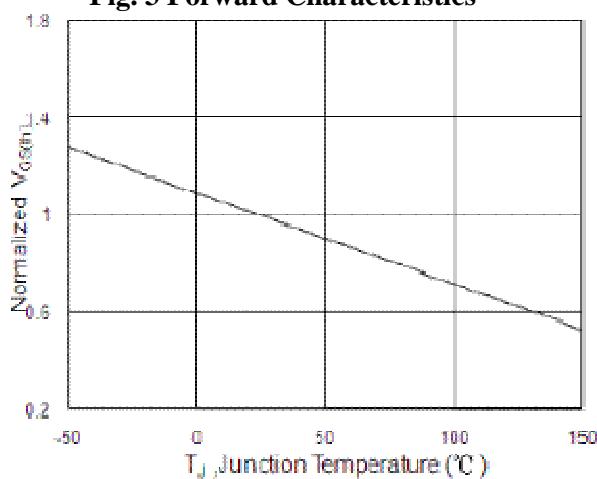


Fig. 5 V_{GS} vs Temperature

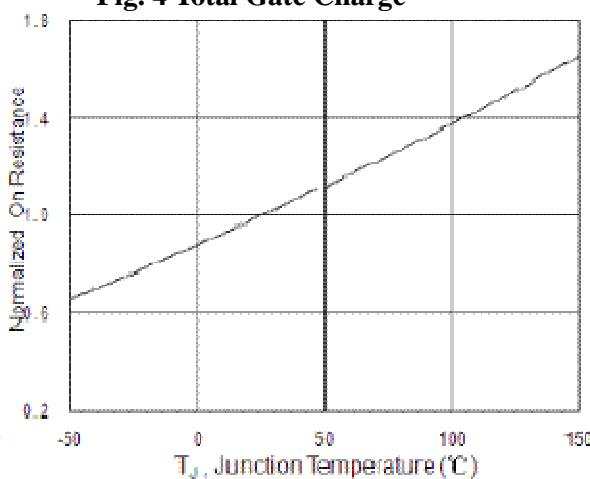


Fig. 6 $R_{DS(on)}$ vs Temperature



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TYPICAL CHARACTERISTICS

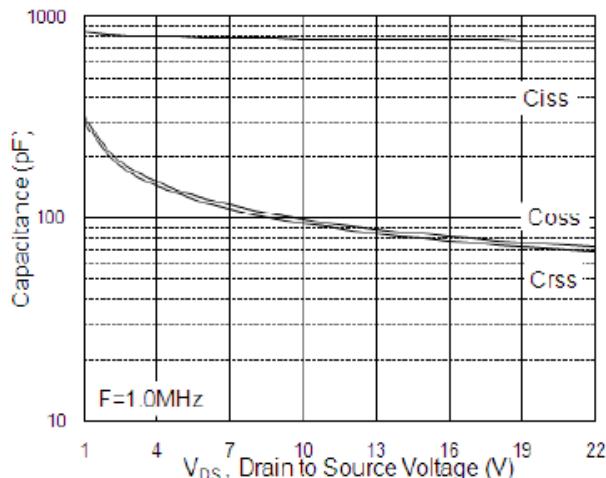


Fig. 7 Capacitance vs V_{ds}

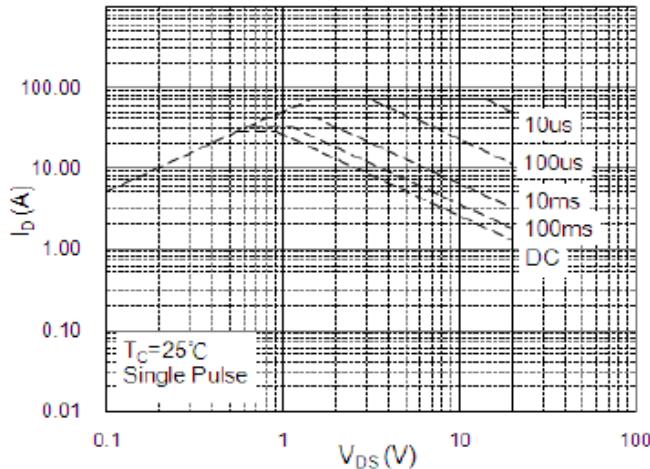


Fig. 8 Safe Operation Region

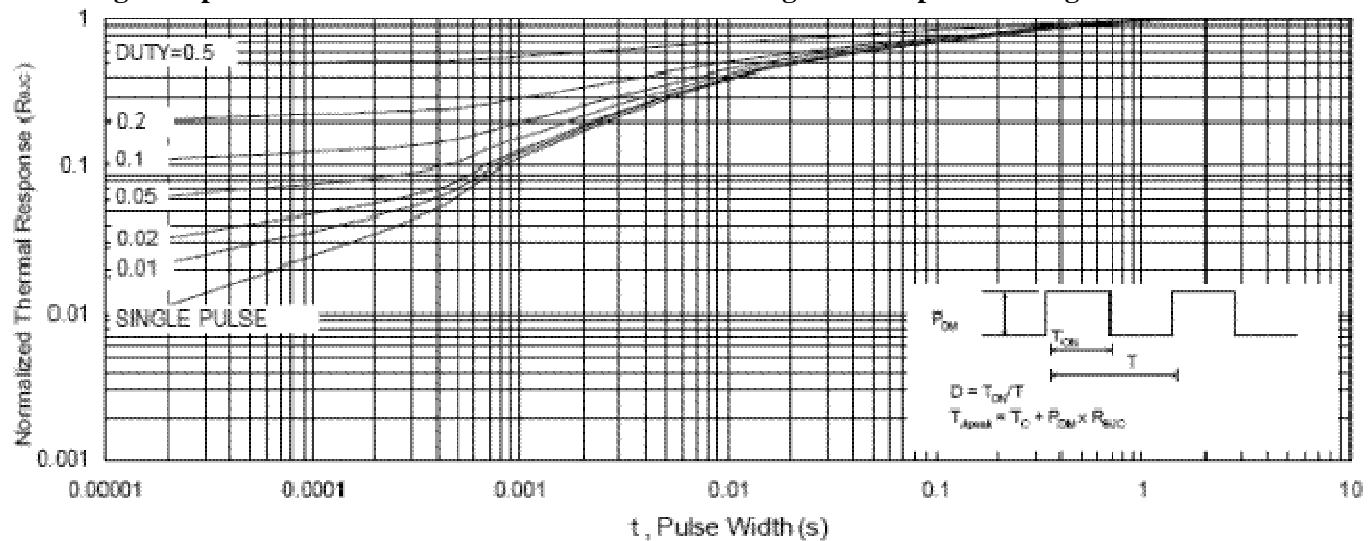


Fig. 9 Maximum Transient Thermal Impedance

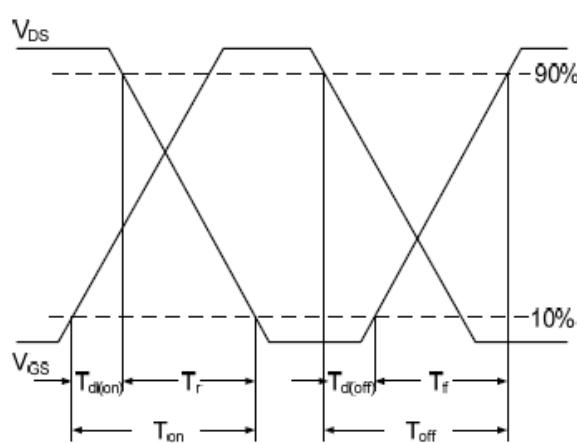


Fig. 10 Switching Time Waveform

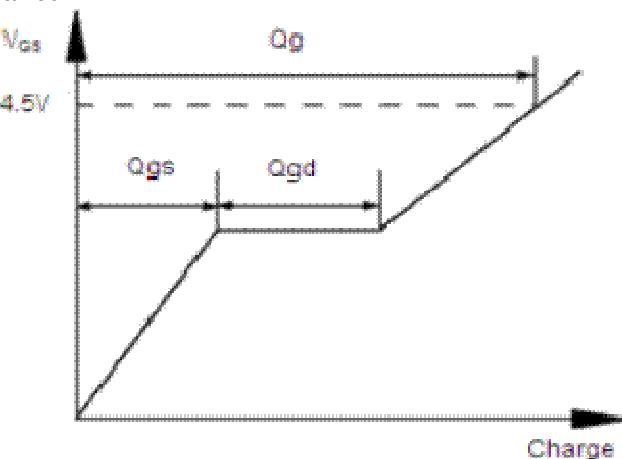


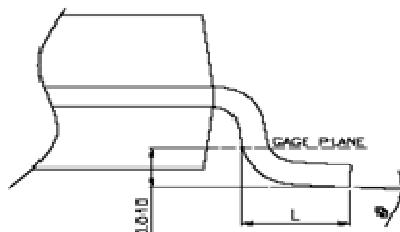
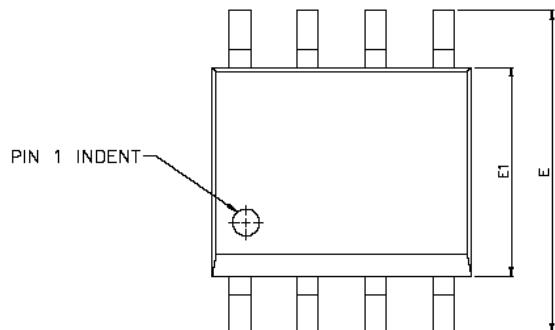
Fig. 11 Gate Charge Waveform



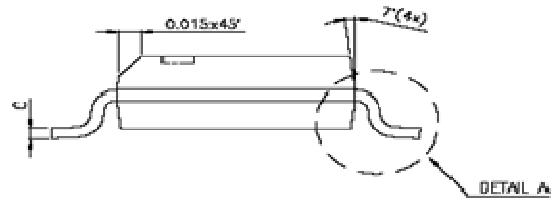
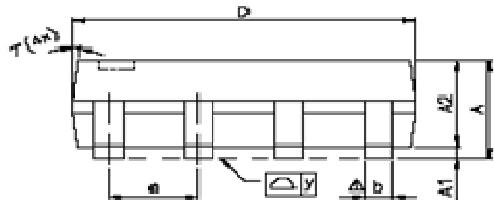
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SOP- 8 PACKAGE OUTLINE



DETAIL A



DETAIL A

SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.47	1.60	1.73	0.058	0.063	0.068
A1	0.10	—	0.25	0.004	—	0.010
A2	—	1.45	—	—	0.057	—
b	0.33	0.41	0.51	0.013	0.016	0.020
C	0.19	0.20	0.25	0.0075	0.008	0.0098
D	4.80	4.85	4.95	0.189	0.191	0.195
E	5.80	6.00	6.20	0.228	0.236	0.244
E1	3.80	3.90	4.00	0.150	0.154	0.157
e	—	1.27	—	—	0.050	—
L	0.38	0.71	1.27	0.015	0.028	0.050
Δy	—	—	0.076	—	—	0.003
θ	0°	—	8°	0°	—	8°



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