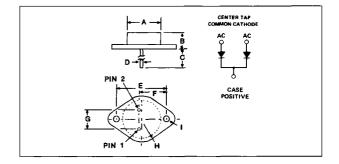


30 Amp Center Tapped Schottky Rectifiers

150°C MAX TJ

20 Volt, 30 Volt and 40 Volt V_{RRM} .640 Volt V_{F} at $I_{\text{F}}=15.0$ Amps Very Fast Switching Speed Standard TO-3 Case

LTR.	INCHES	MILLIMETERS
Α	.7476 Dia.	18,80-19,30
B	.323342	8,20-8,69
	.40 Min.	10,16
D	.038–.043 Dia.	.97-1,09
E	1.180-1.194	29,97–30,33
[F [.665675	16,89–17,15
G	.426–.440	10,82-11,18
н	.525R Max.	13,34
	.151161 Dia.	3,84-4,09



MAXIMUM RATINGS, per diode (At $T_A = 25^{\circ}C$ unless otherwise noted)

RATINGS	SYMBOL	VSK3020T	VSK3030T	VSK3040T	UNITS
DC Blocking Voltage Working Peak Reverse Voltage Peak Repetitive Reverse Voltage	V _{RM} V _{RMM} V _{RBM}	20	30	40	Volts
RMS Reverse Voltage	V _{R(RMS)}	14	21	28	Volts
Average Rectified Forward Current (Fig. 5)**	lo	30.0			Amps
Ambient Temp. @ Rated V _{RM} , R _{sJA} ≤4.5°C/W* Individual Junction	TA	95	90	85	°C
Peak Surge Current (non-rep), 300µs Pulse Width (Fig. 4)	I _{FSM}	500			Amps
Peak Surge Current (non-rep), 1/2 Cycle, 60Hz (Fig. 4)	IFSM	300			Amps
Operating Junction Temperature	TJ	- 65 to + 150*			°C
Storage Temperature	T _{STG}	- 65 to + 150			°C
Thermal Resistance, Junction to Case**	Reuc	1.5			°C/W

^{*}At one-half rated V_{RRM}, R_{BJA} \$4.5°C/W

ELECTRICAL CHARACTERISTICS, per diode (At T_A = 25°C unless otherwise noted)

CHARACTERISTICS	SYMBOL	VSK3020T	VSK3030T	VSK3040T	UNITS
	V _F	.530 .640 1.04			Volts
	l _B		10 75		mA

^{**}Both junctions

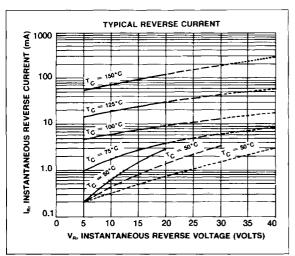


FIGURE 1

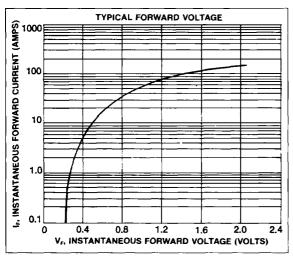


FIGURE 2

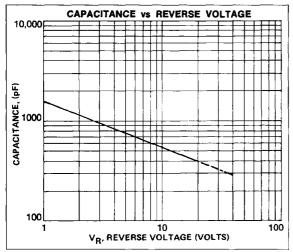


FIGURE 3

VSK3020T

VSK3030T

VSK3040T

PULSE WIDTH = 300μsec

T_c = CASE TEMP. MEASURED

WITH SENSOR CENTERED

ON BOTTOM OF CASE.

CURVES OF FIGURES 1,2,3 AND 4 ARE BASED ON INDIVIDUAL JUNCTIONS. CURVES OF FIGURE 5 ARE BASED ON TOTAL PACKAGE.

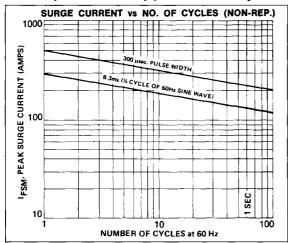
 $\begin{aligned} \text{PULSE WIDTH} &= 300 \mu \text{sec} \\ \text{T}_{\text{A}} &= 25^{\circ}\text{C} \end{aligned}$



The current flow in a Schottky barrier rectifier is due to majority carrier conduction and is not affected by reverse recovery transients due to stored charge and minority carrier injection as in conventional PN diodes.

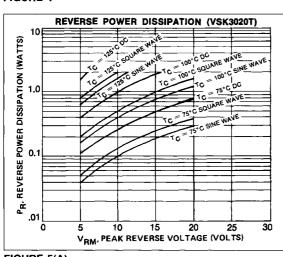
The Schottky barrier rectifier may be considered for purposes of circuit analysis, as an ideal diode in parallel with a variable capacitance equal in value to the junction capacitance. See Figure 3.

30 Amp Center Tapped Schottky Rectifiers



 $T_A = 25^{\circ}C$

FIGURE 4



REVERSE POWER MULTIPLIES 1.32x FOR EACH 5°C TEMP. INCREASE.

USE THIS MULTIPLIER FOR INTERPOLATION BETWEEN CURVES SHOWN ON FIGURES 5(A), 5(B), 5(C).

USE 75°C CURVES FOR ALL CASE TEMP. BELOW 75°C.

FIGURE 5(A)

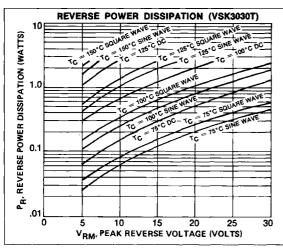
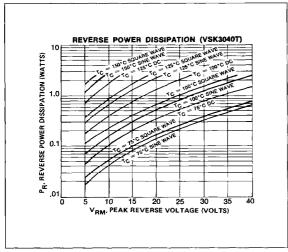


FIGURE 5(B)



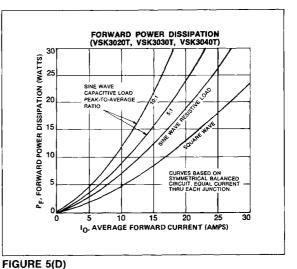


FIGURE 5(C)

ridone 3(D)

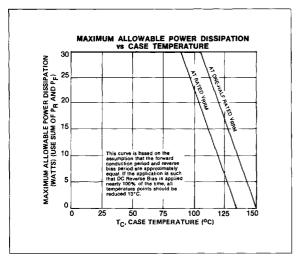


FIGURE 5(E)

Thermal Considerations:

- Use the curves of Figure 5 to study the voltage/current/temperature parameters. To use the curves, add the reverse power dissipation from Figure 5 (A), (B) or (C) to the forward power dissipation from Figure 5 (D). Then go to Figure 5 (E) to find the maximum allowable case temperature.
- 2. Thermal runaway is entirely possible on marginal designs due to the inherently large reverse leakage of Schottky barrier rectifiers and the fact that reverse power multiplies about 1.32 times for each 5°C of junction temperature increase.
- 3. Slightly higher case temperatures can be tolerated when the reverse voltage is lower than that shown in Figure 5 (E).
- 4. We recommend that all designs be verified at an ambient temperature at least 10°C higher than the maximum at which the equipment will ever have to operate.