MGBR40L60C **Preliminary DIODE**

DUAL MOS GATED BARRIER RECTIFIER

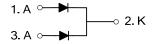
DESCRIPTION

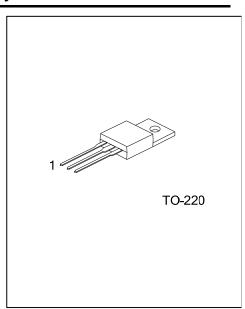
The UTC MGBR40L60C is a dual mos gated barrier rectifiers, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

FEATURES

- * Low forward voltage drop
- * High switching speed

SYMBOL

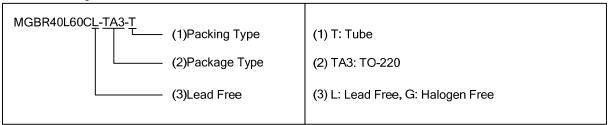




ORDERING INFORMATION

Ordering Number		Doolsogo	Pin Assignment			Packing	
Lead Free	Halogen Free	Package	1	2	3	Facking	
MGBR40L60CL-TA3-T	MGBR40L60CG-TA3-T	TO-220	Α	K	Α	Tube	

Note: Pin Assignment: A: Anode, K: Cathode



www.unisonic.com.tw 1 of 3 QW-R601-172.a

■ ABSOLUTE MAXIMUM RATINGS (PER LEG) (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER		SYMBOL	RATINGS	UNIT
DC Blocking Voltage		V_{RM}	60	V
Working Peak Reverse Voltage		V_{RWM}	60	V
Peak Repetitive Reverse Voltage		V_{RRM}	60	V
Average Destified Output Comment Day Device	Per Leg	-	20	Α
Average Rectified Output Current Per Device	Total	Io	40	Α
Non-Repetitive Peak Forward Surge Current 8.3 Half Sine-Wave Superimposed on Rated Load	3ms Single	I _{FSM}	SM 280	
Operating Junction Temperature		T _J -65~+150		°C
Storage Temperature		T_{STG}	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS (PER LEG)

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	62.5	°C/W	
Junction to Case	$\theta_{ m JC}$	2	°C/W	

■ ELECTRICAL CHARACTERISTICS (PER LEG) (T_A =25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I _R =0.50mA	60			V
Forward Voltage Dress	VEM	I _F =20A, T _J =25°C			0.65	V
Forward Voltage Drop		I _F =20A, T _J =125°C			0.60	V
Landana Occurrent (Nata 4)	I _{RM}	V _R =60V, T _J =25°C			300	μΑ
Leakage Current (Note 1)		V _R =60V, T _J =125°C			100	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

2. Thermal resistance junction to case mounted on heatsink.

UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice.