

FEATURES

- 10 μ s Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density Enhanced DMOS SPT
- Isolated AlSiC Base With AlN Substrates

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM1200ASM45-TS000 is a single switch 4500V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10 μ s short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM1200ASM45-TS000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V_{CES}	4500V
$V_{CE(sat)}$ * (typ)	2.7V
I_C (max)	1200A
$I_{C(PK)}$ (max)	2400A

* Measured at the auxiliary terminals

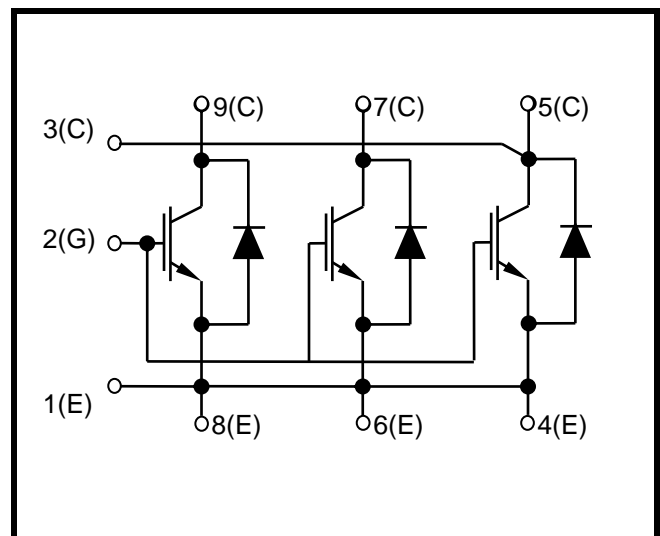


Fig. 1 Circuit configuration



Outline type code: A

(See Fig. 11 for further information)

Fig. 2 Package

ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

$T_{case} = 25^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V_{CES}	Collector-emitter voltage	$V_{GE} = 0V$	4500	V
V_{GES}	Gate-emitter voltage		± 20	V
I_C	Continuous collector current	$T_{case} = 90^{\circ}\text{C}$	1200	A
$I_{C(PK)}$	Peak collector current	1ms, $T_{case} = 120^{\circ}\text{C}$	2400	A
P_{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}\text{C}$, $T_j = 125^{\circ}\text{C}$	12.5	kW
I^2t	Diode I^2t value	$V_R = 0$, $t_p = 10\text{ms}$, $T_j = 125^{\circ}\text{C}$	460	kA^2s
V_{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	7.4	KV
Q_{PD}	Partial discharge – per module	IEC1287, $V_1 = 4800V$, $V_2 = 3500V$, 50Hz RMS	10	pC

THERMAL AND MECHANICAL RATINGS

Internal insulation material:	AlN
Baseplate material:	AlSiC
Creepage distance:	56mm
Clearance:	26mm
CTI (Comparative Tracking Index):	>600

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$R_{th(j-c)}$	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	8	$^{\circ}\text{C}/\text{kW}$
$R_{th(j-c)}$	Thermal resistance – diode	Continuous dissipation - junction to case	-	-	16	$^{\circ}\text{C}/\text{kW}$
$R_{th(c-h)}$	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	6	$^{\circ}\text{C}/\text{kW}$
T_j	Junction temperature	Transistor	-	-	125	$^{\circ}\text{C}$
		Diode	-	-	125	$^{\circ}\text{C}$
T_{stg}	Storage temperature range	-	-40	-	125	$^{\circ}\text{C}$
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

ELECTRICAL CHARACTERISTICS
T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I _{CES}	Collector cut-off current	V _{GE} = 0V, V _{CE} = V _{CES}			4	mA
		V _{GE} = 0V, V _{CE} = V _{CES} , T _{case} = 125°C			90	mA
I _{GES}	Gate leakage current	V _{GE} = ± 20V, V _{CE} = 0V			1	µA
V _{GE(TH)}	Gate threshold voltage	I _C = 120mA, V _{GE} = V _{CE}		5.8		V
V _{CE(sat)}	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 1200A		2.7		V
		V _{GE} = 15V, I _C = 1200A, T _j = 125°C		3.5		V
I _F	Diode forward current	DC		1200		A
I _{FM}	Diode maximum forward current	t _p = 1ms		2400		A
V _F	Diode forward voltage	I _F = 1200A		2.8		V
		I _F = 1200A, T _j = 125°C		3.2		V
C _{ies}	Input capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		150		nF
Q _g	Gate charge	±15V Including external C _{ge}		17		µC
C _{res}	Reverse transfer capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		12		nF
L _M	Module inductance			10		nH
R _{INT}	Internal transistor resistance			90		µΩ
SC _{Data}	Short circuit current, I _{SC}	T _j = 125°C, V _{CC} = 3400V t _p ≤ 10µs, V _{GE} ≤ 15V V _{CE(max)} = V _{CES} - L* x dl/dt IEC 60747-9		4800		A

Note:

 * L is the circuit inductance + L_M

ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(off)}$	Turn-off delay time	$I_C = 1200\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 2800\text{V}$ $R_{G(ON)} = 2.4\Omega$ $R_{G(OFF)} = 2.7\Omega$ $C_{ge} = 220\text{nF}$ $L_S \sim 165\text{nH}$		3000		ns
t_f	Fall time			600		ns
E_{OFF}	Turn-off energy loss			4500		mJ
$t_{d(on)}$	Turn-on delay time			900		ns
t_r	Rise time			350		ns
E_{ON}	Turn-on energy loss			4800		mJ
Q_{rr}	Diode reverse recovery charge	$I_F = 1200\text{A}$ $V_{CE} = 2800\text{V}$ $dI_F/dt = 3000\text{A}/\mu\text{s}$		1340		μC
I_{rr}	Diode reverse recovery current			1030		A
E_{rec}	Diode reverse recovery energy			2220		mJ

$T_{case} = 125^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(off)}$	Turn-off delay time	$I_C = 1200\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 2800\text{V}$ $R_{G(ON)} = 2.4\Omega$ $R_{G(OFF)} = 2.7\Omega$ $C_{ge} = 220\text{nF}$ $L_S \sim 165\text{nH}$		3100		ns
t_f	Fall time			560		ns
E_{OFF}	Turn-off energy loss			4650		mJ
$t_{d(on)}$	Turn-on delay time			900		ns
t_r	Rise time			360		ns
E_{ON}	Turn-on energy loss			6450		mJ
Q_{rr}	Diode reverse recovery charge	$I_F = 1200\text{A}$ $V_{CE} = 2800\text{V}$ $dI_F/dt = 3000\text{A}/\mu\text{s}$		2200		μC
I_{rr}	Diode reverse recovery current			1100		A
E_{rec}	Diode reverse recovery energy			3750		mJ

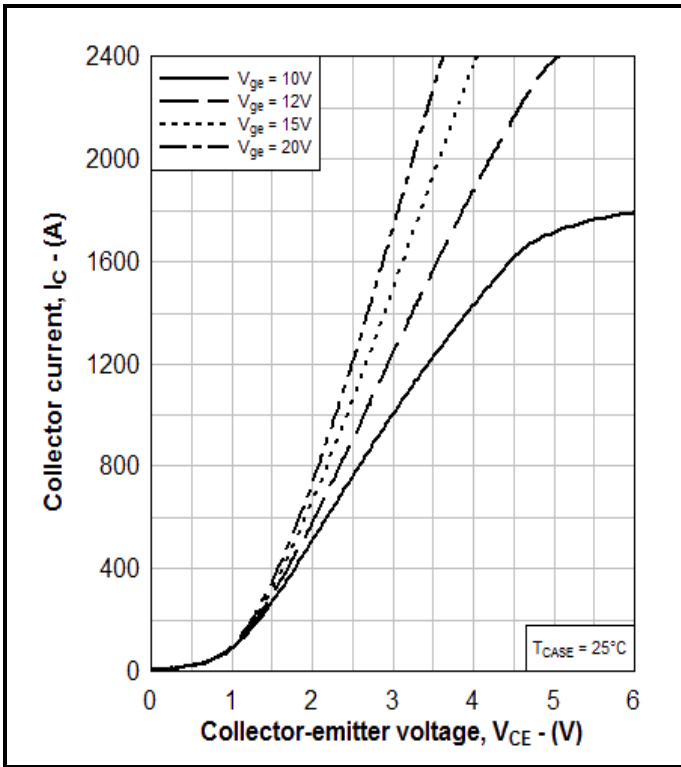


Fig. 3 Typical output characteristics

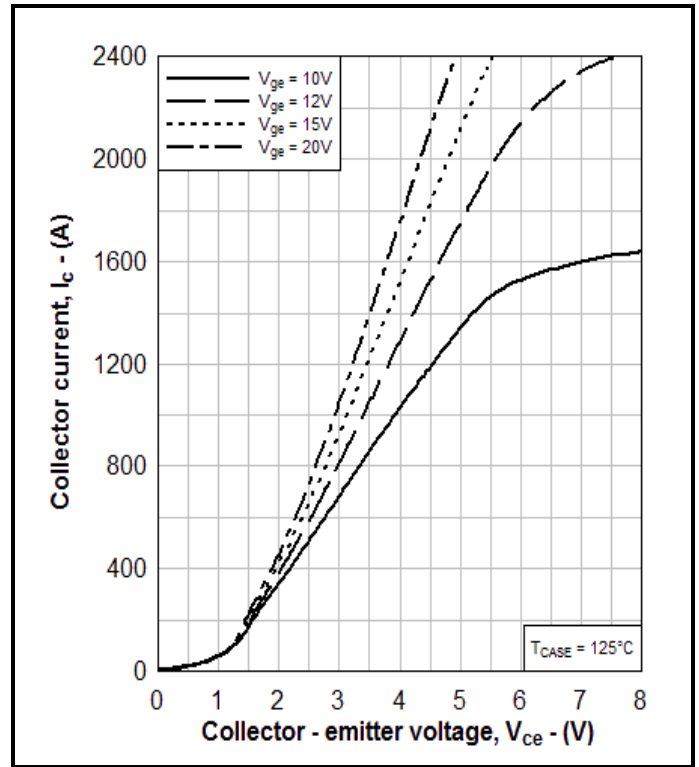


Fig. 4 Typical output characteristics

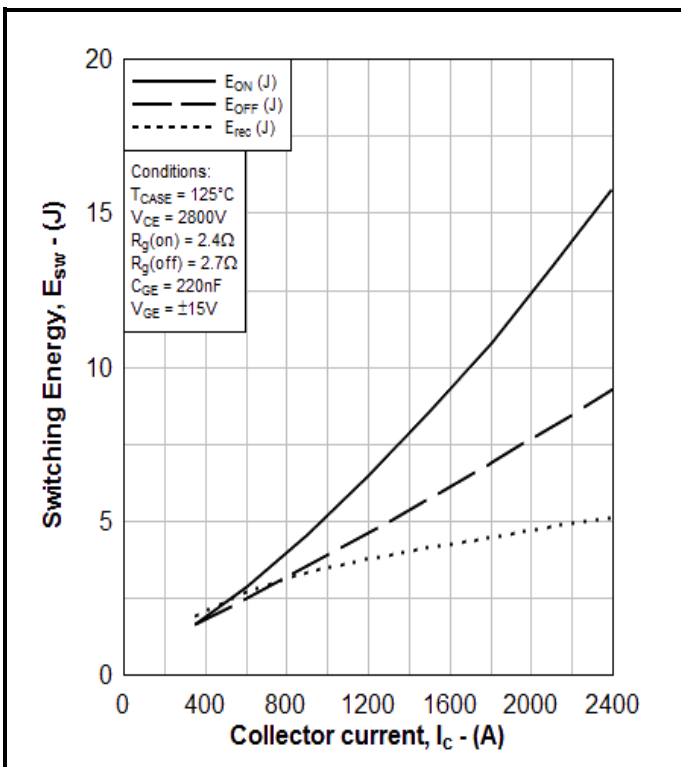


Fig. 5 Typical switching energy vs collector current

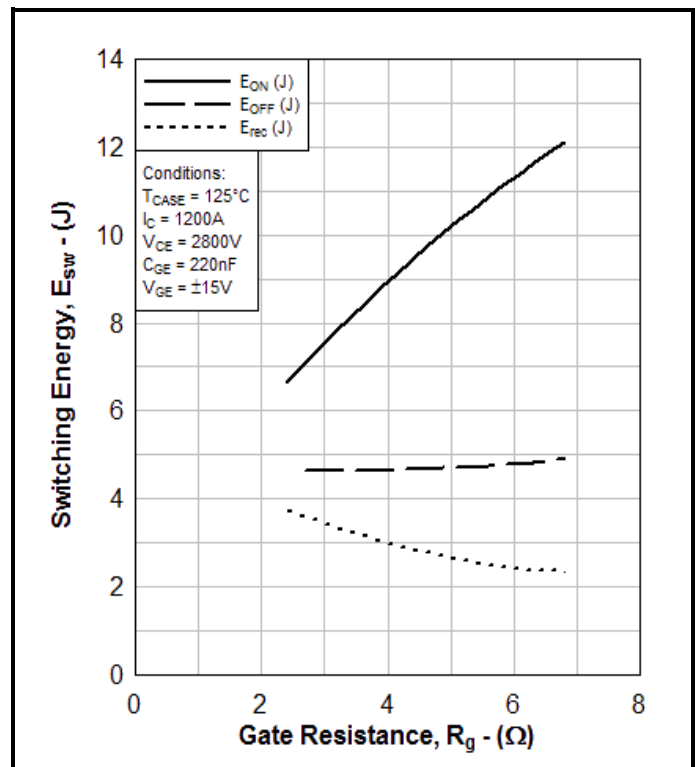


Fig. 6 Typical switching energy vs gate resistance

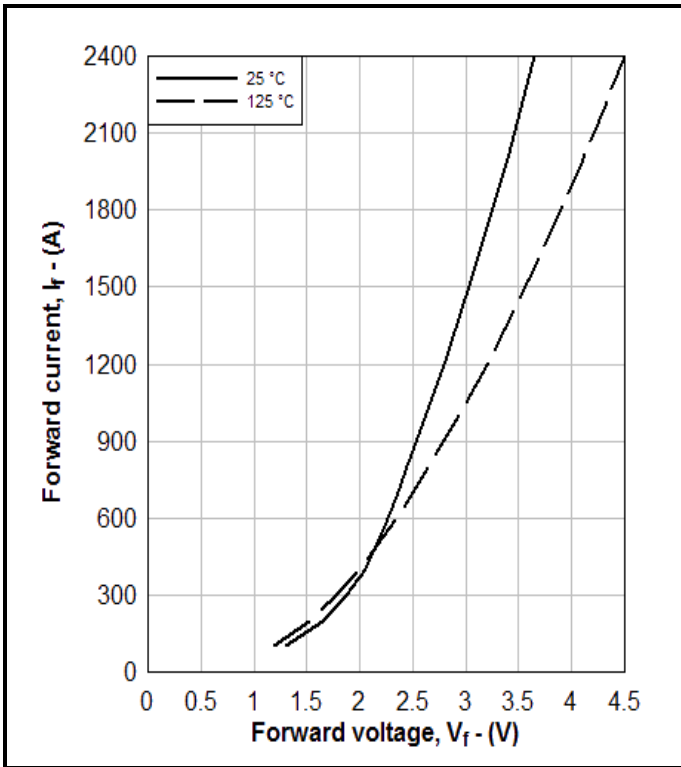


Fig. 7 Diode typical forward characteristics

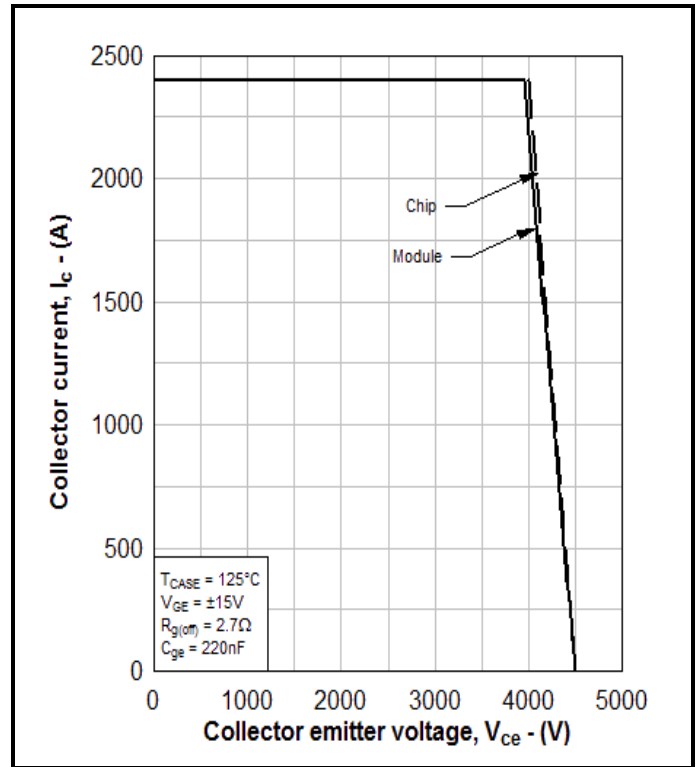


Fig. 8 Reverse bias safe operating area

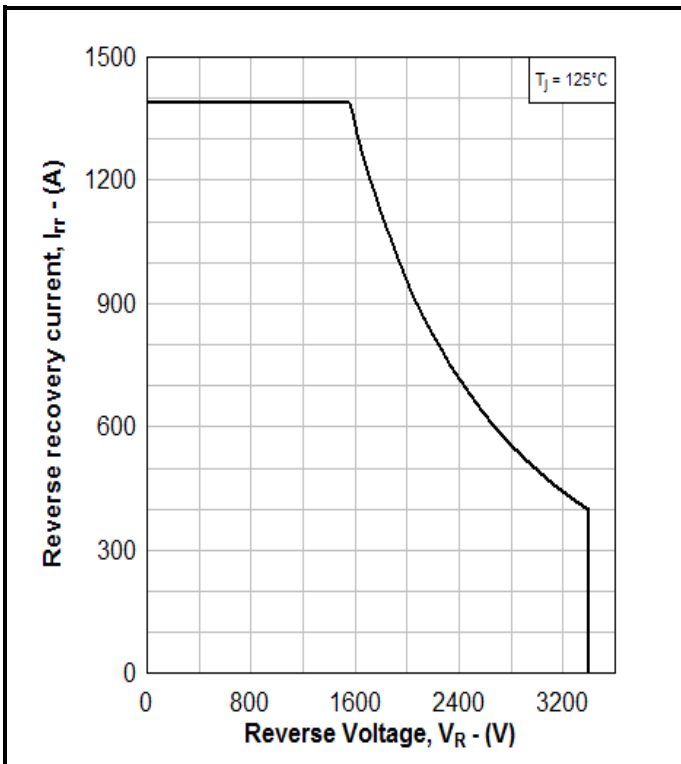


Fig. 9 Diode reverse bias safe operating area

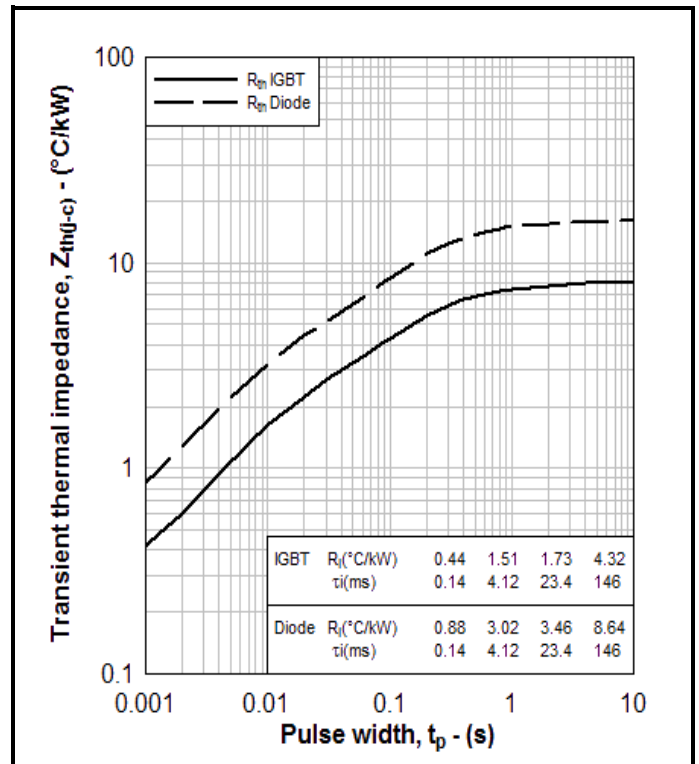


Fig. 10 Transient thermal impedance

PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services.
 All dimensions in mm, unless stated otherwise.
DO NOT SCALE.

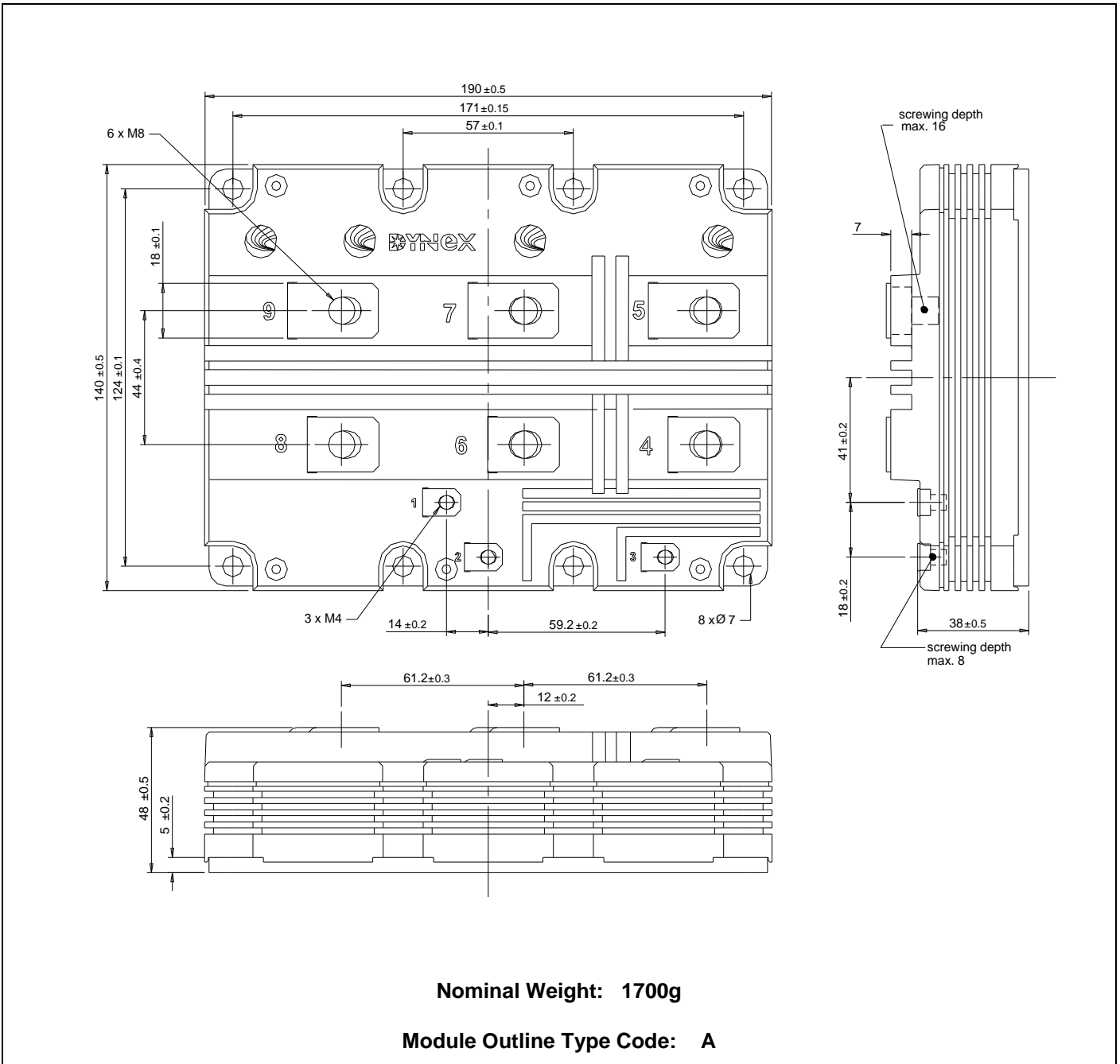


Fig. 11 Module outline drawing

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