

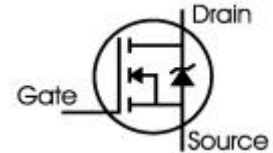
Fast CoolMOS™ Power Transistor

FEATURES:

- New revolutionary high voltage technology
- Ultra low gate charge
- Worldbest $R_{DS(on)}$ per chip area
- Ultra low effective capacitances
- Improved noise immunity

Applications:

- SMPS, resonant applications



Chip Type	V_{DS}	I_D	Die Size	Package	Ordering Code
SIPC06N60C2	600V	4A	2.45 x 2.60 mm ²	sawn on foil	tbd

MECHANICAL PARAMETER:

Raster size	2.45 x 2.60	mm
Source pad size	1.64 x 2.02	
Gate pad size	0.47 x 0.53	
Area total / active	6.37 / 3.96	mm ²
Thickness	175	µm
Wafer size	150	mm
Flat position	0	grd
Max.possible chips per wafer	2388	
Passivation frontside	Photoimide	
Emitter metallization	3200 nm Al Si 1%	
Collector metallization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding	
Die bond	electrically conductive glue or solder	
Wire bond (proposed)	Source: Al, ≤ 500µm; Gate: Al, ≤ 125µm	
Reject Ink Dot Size	Ø 0.65mm ; max 1.2mm	
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month	

MAXIMUM RATINGS:

Parameter	Symbol	Value	Unit
Drain-Source voltage	V_{DS}	600	V
DC drain current, limited by T_{jmax}	I_D	4	A
Pulsed drain current, t_p limited by T_{jmax}	I_{Dpuls}	8	A
Gate source voltage	V_{GS}	± 20	V
Operating junction and storage temperature	T_j, T_{stg}	-55 ... +150	°C
Reverse diode dv/dt $I_S=4A, V_{DS}<V_{DSS}, di/dt=100 A/\mu s, T_{jmax}=150^\circ C$	dv/dt	6	KV/ μs

STATIC CHARACTERISTICS (tested on chip), $T_j=25^\circ C$, unless otherwise specified:

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=0.25mA$			600	V
Gate-source on-state resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=2.8A$		0.95	1.2 ¹⁾	Ω
Gate threshold voltage	$V_{GS(th)}$	$I_D=191\mu A, V_{GS}=V_{DS}$	3.5	4.5	5.5	V
Zero gate voltage drain current	I_{DSS}	$V_{DS}=600V, V_{GS}=0V$		0.1	25	μA
Gate-source leakage current	I_{GSS}	$V_{DS}=0V, V_{GS}=25V$			100	nA

¹⁾ this correlates to a max. $R_{DS(on)}$ -value of 0.95 Ω at $V_{GS}=10V, I_D=2.8A$ of this chip packaged in a TO220-package

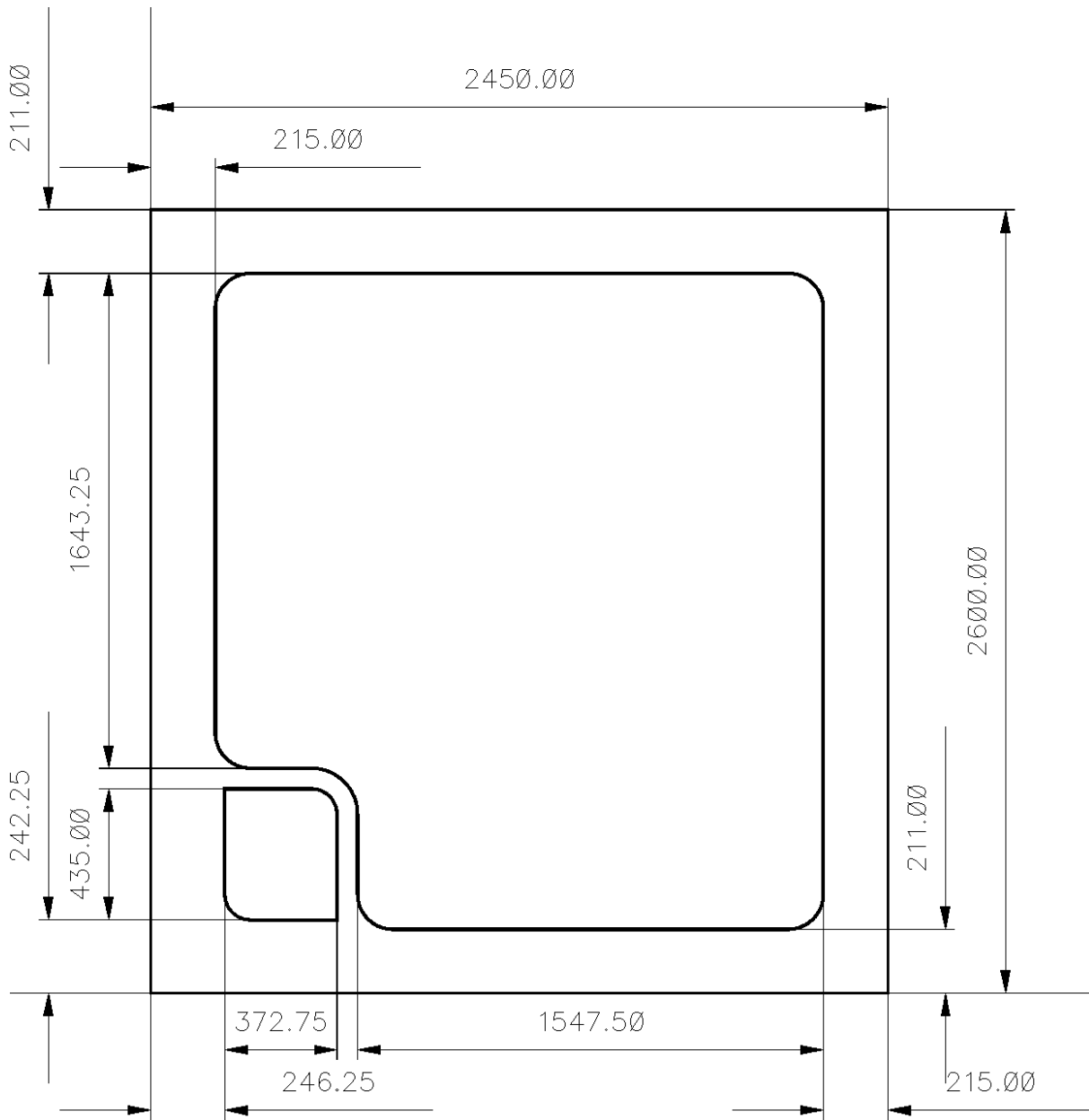
ELECTRICAL CHARACTERISTICS (tested at component):

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Input capacitance	C_{iss}	$V_{DS}=25V,$	-	580	-	pF
Output capacitance	C_{oss}	$V_{GS}=0V,$	-	220	-	
Reverse transfer capacitance	C_{riss}	$f=1MHz$	-	7	-	

SWITCHING CHARACTERISTICS (tested at component), Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Turn-on delay time	$t_{d(on)}$	$T_j=25^\circ C$	-	55	-	ns
Rise time	t_r	$V_{DD}=350V,$	-	30	-	
Turn-off delay time	$t_{d(off)}$	$I_D=4.5 A,$	-	60	-	
Fall time	t_f	$V_{GS}=10V,$	-	15	-	
		$R_G=18\Omega$				

CHIP DRAWING:





Preliminary

SIPC06N60C2

FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the device data sheet		
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DESCRIPTION:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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