TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSⅢ)

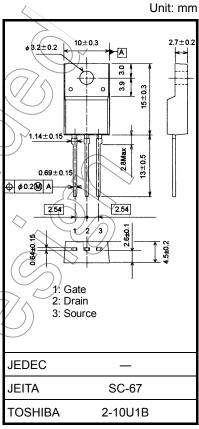
TK30A06J3A

Switching Regulator Applications

- Low drain-source ON-resistance: R_{DS} (ON) = 19 m Ω (typ.)
- High forward transfer admittance: $|Y_{fS}| = 34 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 60 \text{ V)}$
- Enhancement mode: V_{th} = 1.3 to 2.5 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	60	$(/// \wedge$	
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	60	(A)	
Gate-source voltage		V_{GSS}	±20	y	
Drain current	DC (Note 1)	ΙD	30	> A	
	Pulse (Note 1)	I_{DP}	90	Α	
Drain power dissipatio	n (Tc = 25°C)	P_{D}	25	W	
Single pulse avalanche	e energy (Note 2)	EAS	40	mJ	
Avalanche current		IAR	30	A	
Repetitive avalanche energy (Note 3)		EAR)) 2.5	mJ	
Channel temperature		Tch	150	∕ °C	
Storage temperature ra	ange	T _{stg}	-55 to 150	7,¢	



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

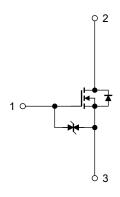
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	5.0	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 25 V, T_{ch} = 25°C (initial), L = 60 μ H, R_{G} = 25 Ω , I_{AR} = 30 A

Note 3: Repetitive rating: Pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

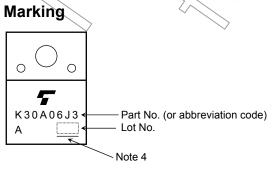


Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μА
Drain cut-off cui	rrent	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V	_		10	μА
Drain-source breakdown voltage		V _{(BR) DSS}	I _D = 10 mA, V _{GS} = 0 V	60	_	_	· V
		V _{(BR) DSX}	I _D = 10 mA, V _{GS} = -20 V	35	_	_	V
Gate threshold v	voltage	V_{th}	V _{DS} = 10 V, I _D = 1 mA	1.3) >'-	2.5	V
Drain-source ON-resistance		Pro (ou)	V _{GS} = 4.5 V, I _D = 15 A) \	24	35	mΩ
		R _{DS} (ON)	V _{GS} = 10V, I _D = 15A	\supset	19	26	
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 15 A	17	34	_	S
Input capacitance		C _{iss}		_	1950	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		140	1/	pF
Output capacitance		Coss	, v	- (230	<u>></u>	
Switching time	Rise time	t _r	10 V VGS	_(4	<u> </u>	
	Turn-on time	t _{on}	0 V 2 2.0 Ω	7	16	_	no
	Fall time	t _f			8	_	ns
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{W} = 10 \mu s$	/ _	48	_	
Total gate charge (Gate-source plus gate-drain)		Q _g (36		
Gate source charge		Qgs	$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$	_	26	_	nC
Gate-drain ("miller") charge		Qgd		_	10	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

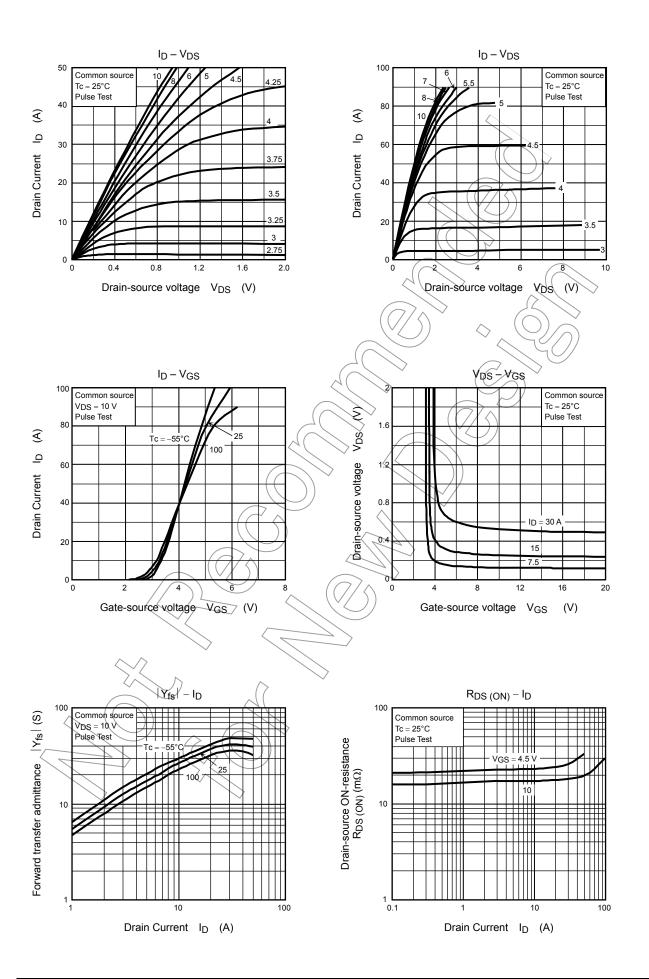
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	⟩ I _{DR} ⟨	-	-	_	30	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	-	-	90	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 30 A, V _{GS} = 0 V	_	_	-1.5	V
Reverse recovery time	t _{rr}	Y _{DR} = 30 A, V _{GS} = 0 V	_	40	_	ns
Reverse recovered charge	Qrr	df _{DR} / dt = 50 A / μs	_	32	_	nC

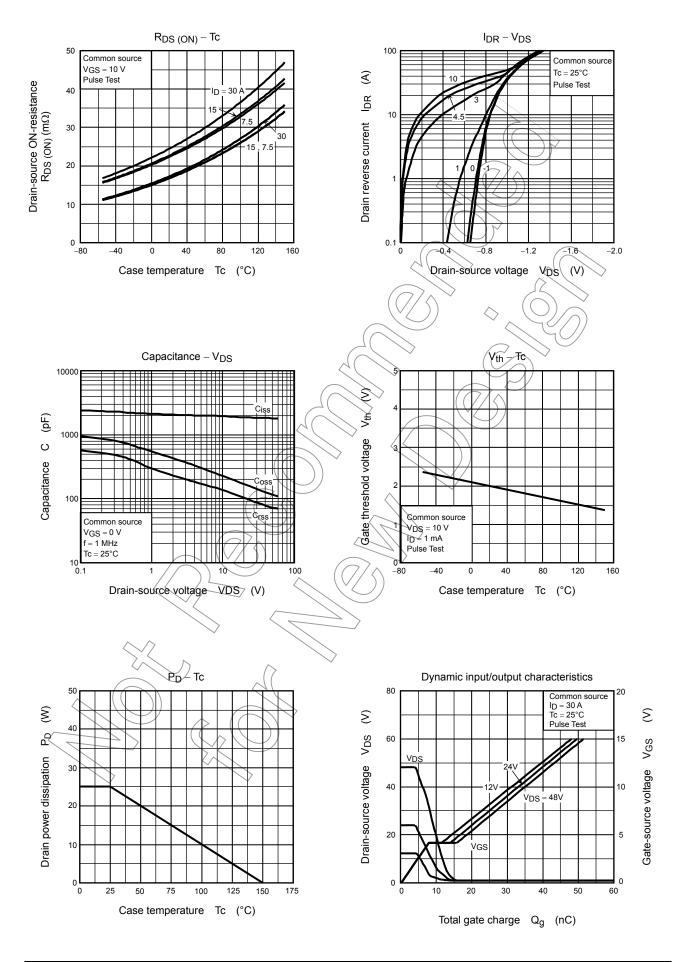


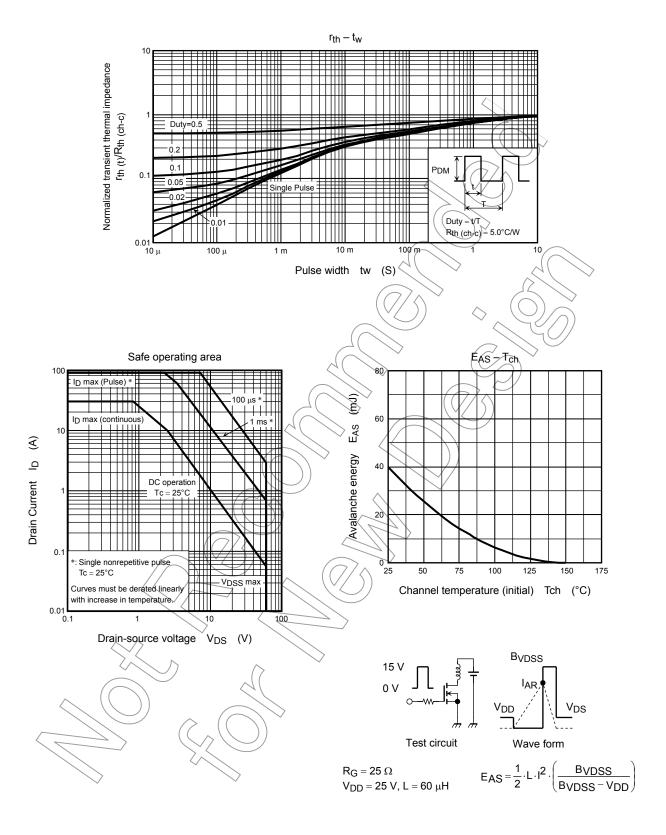
Note 4: A line under a Lot No. identifies the indication of product Labels.

[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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