

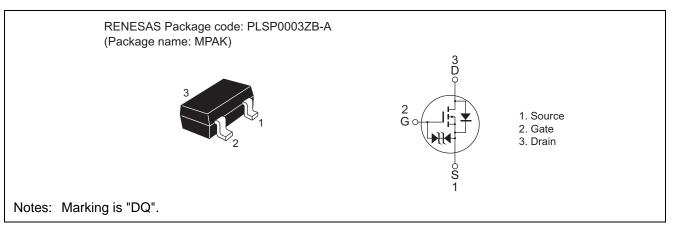
RQJ0304DQDQA

Silicon P Channel MOS FET Power Switching

Features

- Low gate drive V_{DSS} : -30 V and 2.5 V gate drive
- Low drive current
- High speed switching
- Small traditional package (MPAK)

Outline



Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	-30	V
Gate to source voltage	V _{GSS}	+8 / -12	V
Drain current	I _D	-1.8	A
Drain peak current	I _{D(pulse)} Note1	-8	A
Body - drain diode reverse drain current	I _{DR}	1.8	A
Channel dissipation	Pch Note2	0.8	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. $PW \le 10 \ \mu s$, Duty cycle $\le 1\%$

2. When using the glass epoxy board (FR-4 40 \times 40 \times 1 mm)

R07DS0296EJ0300 Rev.3.00 Jan 10, 2014

Datasheet



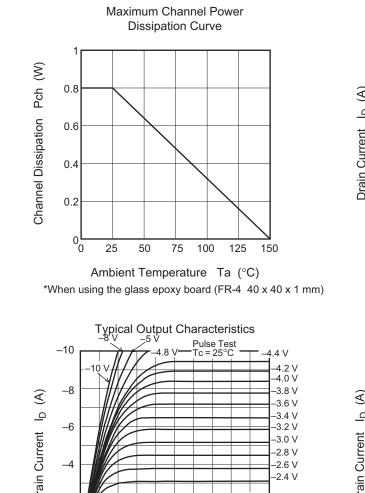
Electrical Characteristics

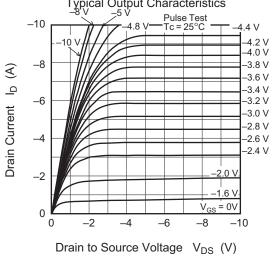
ltem	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	-30			V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V _{(BR)GSS}	+8			V	$I_{G} = +100 \ \mu A, \ V_{DS} = 0$
Gate to source breakdown voltage	V _{(BR)GSS}	-12			V	$I_{G} = -100 \ \mu A, V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	+10	μA	$V_{GS} = +6 V, V_{DS} = 0$
Gate to source leak current	I _{GSS}		_	-10	μA	$V_{GS} = -10 V, V_{DS} = 0$
Drain to source leak current	I _{DSS}			-1	μA	$V_{DS} = -30 V, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	-0.4		-1.4	V	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$
Drain to source on state resistance	R _{DS(on)}		195	245	mΩ	$I_D = -1.0 \text{ A}, V_{GS} = -4.5 \text{ V}^{\text{Note3}}$
Drain to source on state resistance	R _{DS(on)}		300	420	mΩ	$I_D = -1.0 \text{ A}, V_{GS} = -2.5 \text{ V}^{\text{Note3}}$
Forward transfer admittance	y _{fs}	1.8	2.5	—	S	$I_D = -1.0 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	185	—	pF	$V_{DS} = -10 V, V_{GS} = 0,$
Output capacitance	Coss	—	45	—	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	25	—	pF	
Turn - on delay time	t _{d(on)}	—	18	—	ns	$I_{D} = -1.0 \text{ A}$
Rise time	tr	—	33	—	ns	$V_{GS} = -4.5 V$
Turn - off delay time	t _{d(off)}	—	22	—	ns	$R_L = 10 \ \Omega$
Fall time	t _f	_	5	—	ns	$R_g = 4.7 \Omega$
Total gate charge	Qg	_	1.9	—	nC	$V_{DD} = -10 \text{ V}$
Gate to Source charge	Qgs		0.4		nC	$V_{GS} = -4.5 V$
Gate to drain charge	Qgd		0.7		nC	$I_{\rm D} = -2.0 \text{ A}$
Body - drain diode forward voltage	V _{DF}		-0.9	-1.3	V	$I_F = -2.0 \text{ A}, V_{GS} = 0^{\text{Note3}}$

Notes: 3. Pulse test

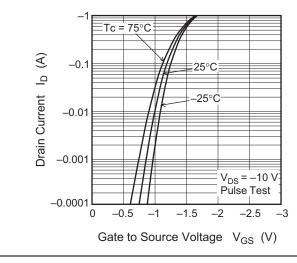


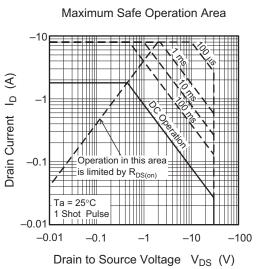
Main Characteristics



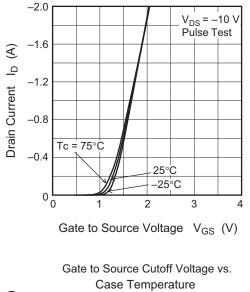


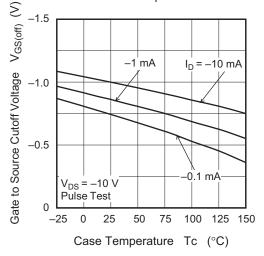




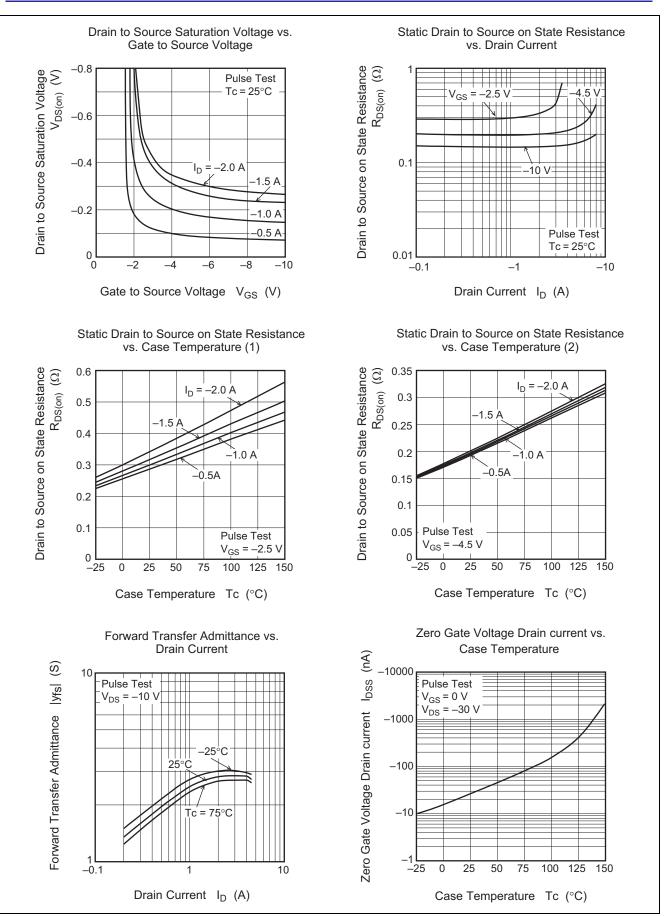


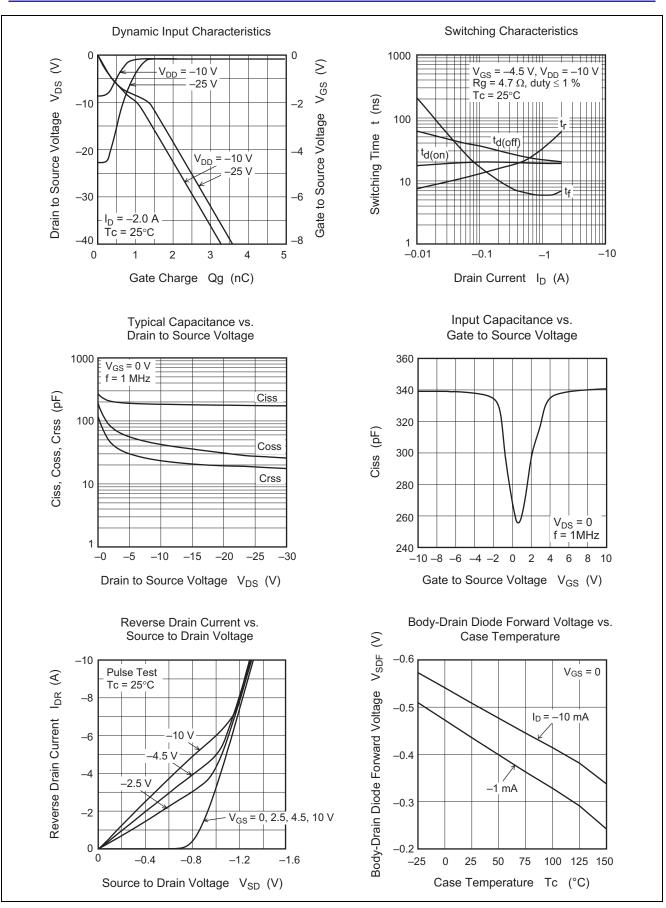
Typical Transfer Characteristics (1)

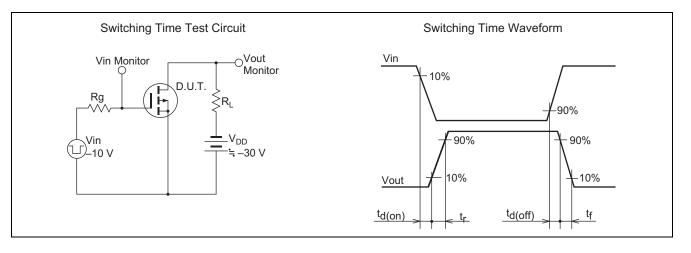








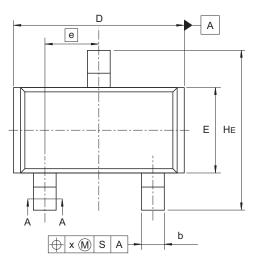


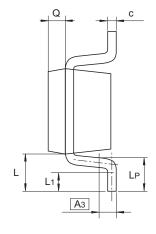


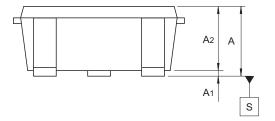


Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
SC-59A	PLSP0003ZB-A	MPAK(T) / MPAK(T)V	0.011











Reference	Dimensions in millimeters		
Symbol	Min	Nom	Max
Α	1.0		1.3
A ₁	0		0.1
A ₂	1.0	1.1	1.2
A ₃		0.25	
b	0.35	0.4	0.5
С	0.1	0.16	0.26
D	2.7	—	3.1
E	1.35	1.5	1.65
е		0.95	
HE	2.2	2.8	3.0
L	0.35		0.75
L ₁	0.15	—	0.55
LP	0.25		0.65
Х			0.05
Q	_	0.3	

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Ordering Information

Orderable Part Number	Quantity	Shipping Container
RQJ0304DQDQATL-H	3000 pcs.	φ178 mm reel, 8 mm Emboss taping



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