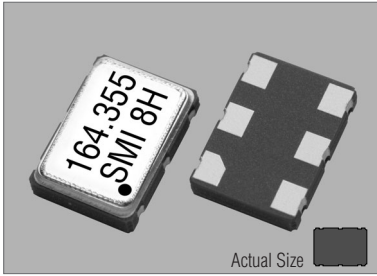
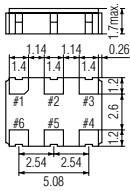
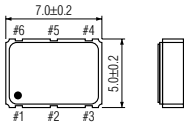


67SMO



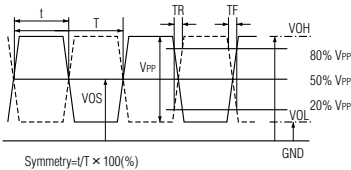
67SMO



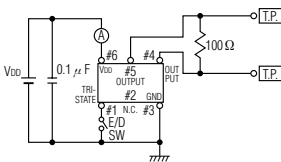
PIN	CONNECTION
1	"L" OPEN or "H"
2	N.C.
3	GND
4	Z OUTPUT
5	Z C-OUTPUT
6	V _{DD}

Z : high impedance

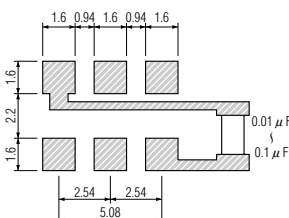
OUTPUT WAVEFORM



TEST CIRCUIT



SOLDERING PATTERN



STANDARD SPECIFICATIONS

LVDS

Item	Specifications
Generic part number	67SMO*1
Frequency range	80.000 MHz to 300.000 MHz
Frequency stability (0°C to +70°C)	67SMO(A) : ±100 ppm 67SMO(B) : ±50 ppm 67SMO(C) : ±30 ppm 67SMO(D) : ±25 ppm
over all conditions	
Operating Conditions	
Operating temperature	0°C to +70°C
Input voltage (VDD)	+2.5V DC ±5% or +3.3V DC ±5%
Stand-by control voltage (Pin#1)	V _{IH} : 70%VDD min. V _{IL} : 30%VDD max.*2
Absolute Max. Ratings	
Supply voltage	-0.5V to +5.0V DC
Storage temperature	-50°C to +125°C
Input current (Pin#1 = Open or V _{IH})	70 mA max.
Stand-by current (Pin#1=V _{IL})	30 µA max.
Output (0°C to +70°C)	
Symmetry	45% to 55% (at crossing point)
Rise and fall times	1 ns max. (20% to 80% of amplitude)
"0" level	V _{OL} : +1.10V, Typical
"1" level	V _{OH} : +1.43V, Typical
Load	100 Ω (OUT-OUTN)
Differential output voltage	0.33V, Typical
Offset voltage	1.25V, Typical
Disable delay time	200 ns max.
Enable delay time	10 ms max.
Startup time	10 ms max.
RMS jitter (12 kHz to 20 MHz band)	1 ps max.
Agging	±5 ppm max. at +25°C ±3°C for first year
Reflow condition	+250°C ±10°C for 10 seconds +170°C ±10°C for 1 to 2 minutes (preheating)

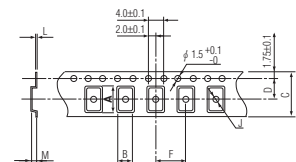
(※1) Final exact part number to be determined with frequency, frequency stability and input voltage.
e.g. 67SMO(3.3VC) 164.355 MHz.

(※2) Internal crystal oscillation to be halted (Pin #1=V_{IL}).

PACKAGE DATA

Item	Package	67SMO
Lid		Metal
Base		Ceramic
Sealing		Seam
Terminal		Tungsten (metalized)
Terminal plating		Gold / Nickel (surface) / (under)
RoHS		Compliant (Pb-free)

TAPE SPECIFICATIONS



A	B	C	D	F	J	L	M	Reel Dia.	Qty/Reel
7.5	5.5	16.0	7.5	8.0	2.0	0.3	2.2	245	1000pcs