Photo Interrupter

KIT3018H

Description

The KIT3018H is a high performance standard type photo interrupter, combines high-output GaAs IRED with high sensitive phototransistor.

Features

- High speed response.
- 3.0mm gap.
- Bottom surface sealed from dust.
- Protected from dust and contamination.
- RoHS compliant.





Applications

- Media detection sensor.
- Timing sensor.
- Edge sensor.

Absolute Maximum Ratings (T_a=25°C, Unless otherwise specified)

Parameter		Symbol	Rating	Unit
Input	Reverse Voltage	V_R	5	V
	Forward Current	I _F	60	mA
	Peak Forward Current *1	I _{FP}	1	А
	Power Dissipation	P_{D}	100	mW
Output	Collector-Emitter Voltage	V_{CEO}	30	V
	Emitter-Collector Voltage	V_{ECO}	5	V
	DC Collector Current	Ic	40	mA
	Collector Power Dissipation	Pc	100	mW
Soldering Temperature for 5 Seconds *2		Tsol	260	${\mathbb C}$
Operating Temperature		Topr	-20 ~ +85	င
Storage Temperature Range		Tstg	-30 ~ +85	င

Notes: $\times 1$. tw = 100 μ s, T=10ms.

※2. Distance from end of the package = 2.0mm, time = 5sec, MAX.

The contents of this data sheet are subject to change without advance notice for the purpose of improvement. When using this product, would you please refer to the latest specifications.

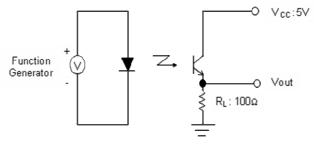
Electrical-Optical Characteristics (T_a=25°C)

Parameter		Symbol	Conditions	Min.	Тур.	Max.	Unit
Input	Forward Voltage	V_{F}	I _F =30mA	ı	1.2	1.5	V
	Wavelength of Emission	λ		1	940	-	nm
	Reverse Current	I _R	V _R =4V	1	-	10	μΑ
Output	Dark Current	I _{CEO}	V_{CE} =10 V , I_F =0 mA	-	1	0.1	μΑ
			E _V =0 lux				
Transfer Charac- teristics	Collector - Emitter	V	I _C =0.1 mA, I _F =30 mA	1	1	0.4	V
	Saturation Voltage	$V_{CE(sat)}$					
	Collector Current *3	Ic	V _{CE} =5.0V, I _F =20mA	0.1	-	-	mA
Response Time	Rise Time *4	t _r	Below Schematic	-	5	-	μS
	Fall Time *4	t _f	Delow Schematic	-	5	-	μS

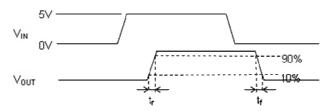
Note 3. Light beam uninterrupted condition: Unblocked is normal condition.

Note 4. Adjust amplitude and offset of square wave so that Vout transitions from 10% to 90% of Vout range of the Device Under Test(DUT)

* Circuit for measuring response time



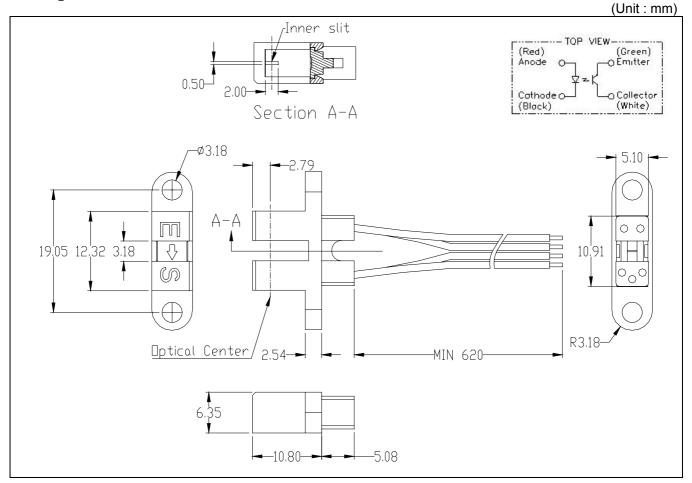
Test Circuit for Rise and Fall Time



Definitions for Response Times

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Package Outline Dimensions



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