UNISONIC TECHNOLOGIES CO., LTD

BTA12A Preliminary TRIAC

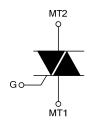
12A TRIACS

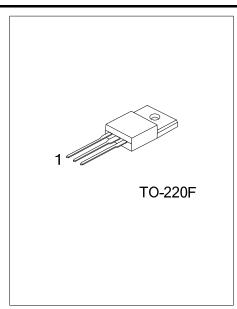
■ DESCRIPTION

The UTC **BTA12A** is a 12A triacs which can be operated in 3 quadrants only, it uses UTC's advanced technology to provide customers with high commutation performances, etc.

The UTC **BTA12A** is suitable for inductive load switching operations, also can be used in ON/OFF function applications such as induction motor starting circuits, heating regulation, static relays etc.

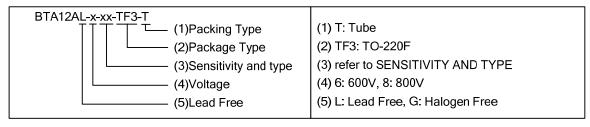
■ SYMBOL





ORDERING INFORMATION

Ordering	Dookogo	Pin <i>i</i>	Assignr	Dooking			
Lead Free	Halogen Free	Package	1	2	3	Packing	
BTA12AL-x-xx-TF3-T	BTA12AG-x-xx-TF3-T	TO-220F	MT1	MT2	G	Tube	



■ SENSITIVITY AND TYPE

	VOLT	AGE	OFNOITIVITY	TVDE		
PART NUMBER	PART NUMBER 600V 800V SENSITIV		SENSITIVITY	TYPE		
BW	0	0	50mA	SNUBBERLESS		
CW	0	0	35mA	SNUBBERLESS		
SW	0	0	10mA	LOGIC LEVEL		
TW	0	0	5mA	LOGIC LEVEL		

⊚: Available

■ MARKING INFORMATION

PACKAGE	MARKING						
TO-220F	UTC BTA12A ☐ L: Lead Free → G: Halogen Free → Data Code 1						

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■ ABSOLUTE MAXIMUM RATINGS

PARAME	TER		SYMBOL	RATINGS	UNIT
RMS On-State Current (Full S	On-State Current (Full Sine Wave) T _C =90°C		I _{T(RMS)}	12	Α
Non Repetitive Surge Peak On-State Current (Full Cycle,	F=50 Hz	t=20ms	1	120	Α
T _J initial=25°C)	F=60 Hz	t=16.7ms	I _{TSM}	126	Α
I ² t Value for Fusing	t _P =10ms		l ² t	78	A^2s
Critical Rate of Rise of On-State Current I _G =2xI _{GT} , tr≤100ns	F=120 Hz	T _J =125°C	dl/dt	50	A/µs
Non Repetitive Surge Peak Off-State Voltage	t _P =10ms	T _J =25°C	V _{DSM} /V _{RSM}	V _{DRM} /V _{RRM} +100	V
Peak Gate Current	t _P =20µs	T _J =125°C	I _{GM}	4	Α
Average Gate Power Dissipation T _J =125°C		$P_{G(AV)}$	1	W	
Operating Junction Temperature			T_J	-40~+125	°C
Storage Junction Temperature			T _{STG}	-40~+150	°C
Note: Absolute maximum ra	atings are	those values	s hevond whic	h the device could be permanen	tly damaged

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL RESISTANCES

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	60	°C/W
Junction to Case (AC)	θ_{JC}	2.3	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J =25°C unless otherwise specified)

FOR SNUBBERLESS TYPE and LOGIC LEVEL TYPE (3 QUADRANTS

PARAMETER SYMB		TEST		TW		SW		CW			BW			UNIT		
PARAMETER	TANAMILTEN STIMBOL		CONDITIONS		TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNIT
Gate Trigger Current (Note 1)	I _{GT}	V _D =12V,	1-11-111			5			10			35			50	mA
Gate Trigger Voltage	V_{GT}	R _L =30Ω	1-11-111			1.3			1.3			1.3			1.3	٧
Gate Non-Trigger Voltage	V_{GD}	$V_D=V_{DRM}$, $R_L=3.3k\Omega$, $T_J=125^{\circ}C$	1-11-111	0.2			0.2			0.2			0.2			٧
Holding Current (Note 2)	I _H	I _T =100mA				10			15			35			50	mA
Latching Current	ΙL	I _G =1.2I _{GT}	I-III II			10 15			25 30			50 60			70 80	mA mA
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V _D =67%V _{DI} Gate Open T _J =125°C		20			40			500			1000			V/µs
Critical Rate of Rise of		(dV/dt)c=0. T _J =125°C	1V/μs,	3.5			6.5									
Off-State Voltage at	(dl/dt)c	(dV/dt)c=10 T _J =125°C)V/μs,	1			2.9									A/ms
Commutation (Note 2)		Without Sn T _J =125°C	ubber							6.5			12			

Note: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.

■ STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Peak On-State Voltage(Note)	V_{T}	I_{TM} =17A, t_p =380 μ s	T _J =25°C			1.55	V
Threshold Voltage(Note)	V_{TO}		T _J =125°C			0.85	V
Dynamic Resistance(Note)	R_D		T _J =125°C			35	mΩ
Repetitive Peak Off-State Current	I _{DRM}	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	T _J =25°C			5	μΑ
	I _{RRM}	V _{DRM} =V _{RRM}	T _J =125°C			1	mA

Note: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.

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