

## Description

The MIK386 is a power amplifier designed for use in low voltage consumer applications. The gain is internally set to 20 to keep external part count low, but the addition of an external resistor and capacitor between pins 1 and 8 will increase the gain to any value up to 200. The inputs are ground referenced while the output is automatically biased to one half the supply voltage. The quiescent power drain is only 24 milliwatts when operating from a 6 volt supply, making the MIK386 ideal for battery operation.

## Features

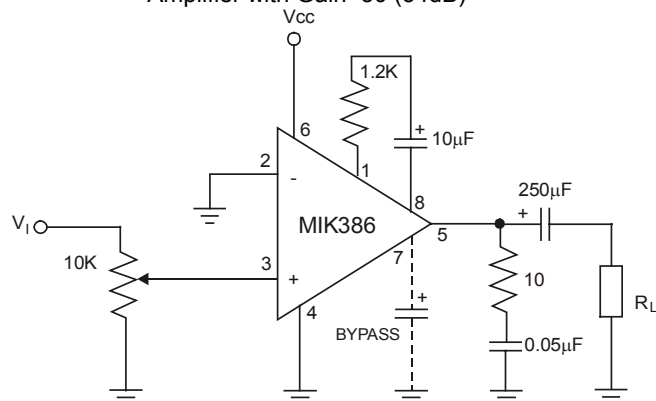
- Battery operation
- Minimum external parts
- Wide supply voltage range 4V to 12V
- Low quiescent current drain 4mA (Typ)
- Voltage gains: 20 - 200
- Ground referenced input
- Self-centering output quiescent voltage
- Low distortion

## Applications

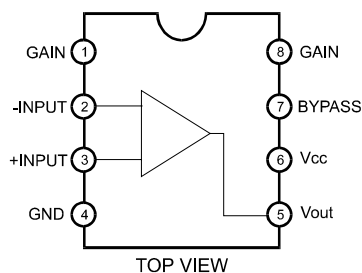
- AM-FM radio amplifiers
- Portable tape player amplifiers
- Intercoms
- TV sound systems
- Line drivers
- Ultrasonic drivers
- Small servo drivers
- Power converters

## Typical Applications

Amplifier with Gain=50 (34dB)



## Pin connection



## Absolute Maximum Ratings

Symbol	Parameter	Maximum	Units
$V_{CC}$	Supply voltage	15	V
$P_D$	Power dissipation	660	mW
$V_{IN}$	Input voltage	$\pm 0.4$	V
$T_J$	Junction temperature	+150	$^{\circ}C$
$T_{OPR}$	Operating temperature	-20 ... +70	$^{\circ}C$
$T_{STG}$	Storage temperature	-40 ... +125	$^{\circ}C$

## Electrical Characteristics

( $T_A=25^{\circ}C$ ,  $V_{CC}=6V$ ,  $R_L=8\Omega$ ,  $f=1KHz$ , unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Quiescent Current	$I_{CCQ}$	$V_{IN}=0$		4	8	mA
Output Power	$P_{OUT}$	$V_{CC}=6V$ , THD=10%	250	325		mW
		$V_{CC}=9V$ , THD=10%	500	700		
Voltage Gain	$A_V$	Pins 1 and 8 Open		26		dB
		10 $\mu F$ from Pin 1 to 8		46		
Bandwidth	BW	Pins 1 and 8 Open		300		KHz
		10 $\mu F$ from Pin 1 to 8		60		
Total Harmonic Distortion	THD	$P_{OUT}=125mW$ , Pins 1 and 8 Open		0.2		%
Power Supply Rejection Ratio	PSRR	$C_{bypass}=10 \mu F$ , Pins 1 and 8 Open, Referred to output		50		dB
Input Resistance	$R_{IN}$			50		$k\Omega$
Input Bias Current	$I_{BIAS}$	Pins 1 and 8 Open		250		nA