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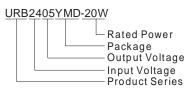
URB YMD-20W Series

20W,ULTRA WIDE INPUT,ISOLATED & REGULATED SINGLE OUTPUT DIP PACKAGING, DC-DC CONVERTER



Patent Protected RoHS

PART NUMBER SYSTEM



FEATURES

- •4:1 Ultra wide input voltage range
- •Small footprint,20W,1×1inch
- Efficiency up to 90%
- •1.5KVDC isolation
- •Short circuit protection (automatic recovery)
- Over load protection
- Over temperature protection
- Input under voltage protection
- Output over voltage protection
- Metal shielding package
- ●Meet CISPR22/EN55022 CLASS A
- •Low temperature rise, Industrial level specifications
- Inverse polarity protection for A2S (chassis mounting) and A4S (DIN-Rail mounting)

APPLICATION

URB_YMD-20W series are applied to the data transmission device, battery-powered equipment, communications equipment, distributed power systems, mixed analog / digital systems, remote control system, industrial robot system and wide input voltage occasions.

SELECTION GUID	ÞΕ									
	Input Volta	age(VDC)	Output	Output Cu	ırrent (mA)	Input ((mA)	Current (Typ.)	Reflected	Max.	Efficiency (%, Typ.)
Model ^①	Nominal (Range)	Max ^②	Voltage (VDC)	Max.	Min.	@Max. Load	@No Load	Ripple Current (Ma,Typ.)	Capacitive Load(µF)	@Max. Load
URB2403YMD-20W	- A		3.3	5000	250	780	50		10300	87
URB2405YMD-20W	24	40	5	4000	200	925	60		5400	90
URB2412YMD-20W	(9-36)	40	12	1667	84	925	60		1200	90
URB2415YMD-20W			15	1333	67	925	60	30	750	90
URB4803YMD-20W			3.3	5000	250	390	25	30	10300	88
URB4805YMD-20W	48	80	5	4000	200	463	30		5400	90
URB4812YMD-20W	(18-75)	30	12	1667	84	463	30		1200	90
URB4815YMD-20W			15	1333	67	463	30		750	90

Note: ① Series with suffix "H" are heat sink mounting, series with suffix "A2S" are chassis mounting, with suffix "A4S" are DIN-Rail mounting, for example URB2405YMD-20WHA2S is chassis mounting with heat sink, URB2405YMD-20WA4S is DIN-Rail mounting without heat sink. If the application has a higher requirement for heat dissipation, you can choose modules with heat sink.

② Absolute maximum rating without damage on the converter, but it isn't recommended.
③ The efficiency of "A2S" and "A4S" is approx. 2% lower for the protection of inverse polarity.

INPUT SPECIFICATIONS					
Item	Test Conditions	Min.	Тур.	Max.	Unit
Input Curso Voltage (1000 may)	24VDC input	-0.7		50	
Input Surge Voltage (1sec.max.)	48 VDC input	-0.7		100	
Ctort up Voltage	24VDC input			9	VDC
Start-up Voltage	48 VDC input			18	VDC
Under Voltage Shutdown	24VDC input	6			
	48 VDC input	15			

Start-up Time	Nominal input& constant resistance load	10			ms		
	Models ON	Ctrl open or connect TTL high level (2.5-12VD)			5-12VDC)		
Ctrl*	Models OFF	els OFF Ctrl connect GND or low level (0-			2VDC)		
	Input current (Models OFF)	5 mA			mA		
Input Filter Pi Filter							
Note:*The Ctrl control pin voltage is re	Note:*The Ctrl control pin voltage is refer to GND.						

Item	Test Conditions	Min.	Тур.	Max.	Unit		
Output voltage Accuracy			±1	±3			
Line Regulation	Full load, Input voltage from low to high		±0.2	±0.5	%		
Load Regulation	5% to 100% load		±0.5	±1			
Transient Recovery Time	25% load star shares 0.4A/IIa 0.5mC		300	500	us		
Transient Response Deviation	25% load step change,0.1A/Us,0.5mS		±3	±5	%		
Temperature Drift Coefficient	100% load		±0.02		%/°C		
Ripple & Noise*	20MHz bandwidth		50	120	mV p-p		
Trim			±10%	-			
	3.3V output		3.9	\			
Output Over Veltere Protection	5V output		6.2	- \	VDC		
Output Over Voltage Protection	12V output		15				
	15V output	-	18	/ -			
Output Over Current Protection			150		%		
Over Temperature Protection	Input voltage range	-	110		°C		
Output Short Circuit Protection	t Short Circuit Protection Continuous, automatic recovery						

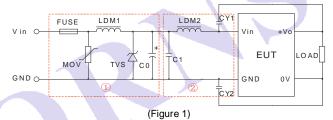
COMMON SPEC	IFICATIONS				
Item	Test Conditions	Min.	Тур.	Max.	Unit
Isolation Voltage	Input-Output, tested for 1 minute , leakage current less than 1 mA	1500			VDC
Isolation Resistance	Input-Output, test at 500VDC	1000			МΩ
Isolation Capacitance	Input-Output, 100KHz/0.1V		1000		pF
Switching Frequency	PWM mode		320		KHz
MTBF	MIL-HDBK-217F@25℃	1000			K hours
Case Material		Aluminum Alloy			
	PCB mounting (without heat sink)	25.4x25.4x11.7			
	PCB mounting (with heat sink)		25.4x25.4x16.	4	
Cina	A2S chassis mounting (without heat sink)		2	- mm	
Size	A2S chassis mounting (with heat sink)		2		
	A4S DIN-Rail mounting (without heat sink)	76.0x31.5x25.8			1
	A4S DIN-Rail mounting (with heat sink)	76.0x31.5x29.8			
	PCB mounting (without heat sink)		15		
	PCB mounting (with heat sink)		20		
Weight	A2S chassis mounting (without heat sink)		35		
	A2S chassis mounting (with heat sink)		40		- g
	A4S DIN-Rail mounting (without heat sink)		54		1
	A4S DIN-Rail mounting (with heat sink)		59]

ENVIRONMENTAL SPE	CIFICATIONS				
Item	Test Conditions	Min.	Тур.	Max.	Unit

Storage Humidity	Non condensing	5		95	%
Operating Temperature	Refer to temperature derating curve (see Figure3)	-40		85	
Storage Temperature		-55		125	
	Operating Temperature curve range (No wind)			105	°C
The Max. Case Temperature	25°C,rated input,100%load output (No wind)		58		
	60°C,rated input,100%load output (No wind)		93		
Lead Temperature	1.5mm from case for 10 seconds			300	
Cooling		Free air convection			
Vibration		10-55Hz, 10G, 30 Min. along X, Y and Z			

EMC SI	PECIFICATIONS			
ENAL	CE	CISPR22/EN55022	CLASS A(Without External Circuit) / CLASS B (Extern	nal Circuit Refer to Figure1-②)
EMI	RE	CISPR22/EN55022	CLASS A(Without External Circuit) / CLASS B (Extern	nal Circuit Refer to Figure1-②)
	ESD	IEC/EN61000-4-2	Contact ±4KV	perf. Criteria B
	RS	IEC/EN61000-4-3	10V/m	perf. Criteria A
	EFT	IEC/EN61000-4-4	±2KV (External Circuit Refer to Figure1-①)	perf. Criteria B
EMS	Surge	IEC/EN61000-4-5	±2KV (External Circuit Refer to Figure1-①)	perf. Criteria B
	CS	IEC/EN61000-4-6	3 Vr.m.s	perf. Criteria A
	Voltage dips, short and interruptions immunity	IEC/EN61000-4-29	0%-70%	perf. Criteria B

EMC RECOMMENDED CIRCUIT

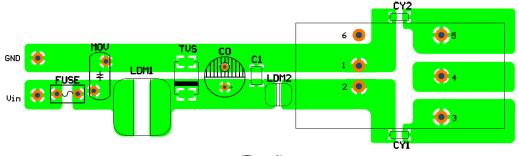


Recommended external circuit parameters:

Model	Vin:24 V	Vin:48 V		
FUSE	Choose according to practical input cur			
MOV	S14K35	S14K60		
LDM1	5	6μΗ		
TVS	SMCJ48A	SMCJ90A		
C0	330µF/50V	330µF/100V		
C1	1μF/50V	1μF /100V		
LDM2	4.7μH 1nF/2KV			
CY1,CY2				

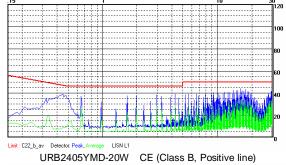
Note: In Figure 1, part ① is EMS recommended external circuit, part ② is EMI recommended external circuit. Choose according to requirements.

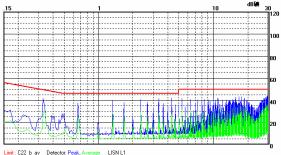
EMC MODULE RECOMMENDED CIRCUIT PCB LAYOUT



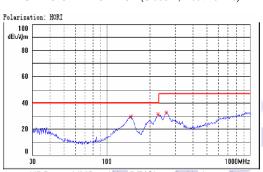
(Figure 2)
Note: The pad space between input and output (CY1/CY2) must ≥2mm.

EMI TEST WAVEFORM (RECOMMENDED CIRCUIT FINGURE 1-2)

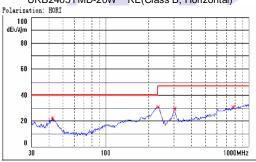




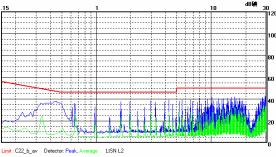
URB4815YMD-20W CE (Class B, Positive line)



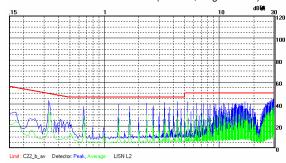
URB2405YMD-20W RE(Class B, Horizontal)



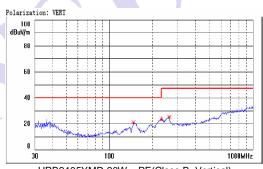
URB4815YMD-20W RE(Class B, Horizontal)



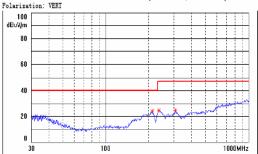
URB2405YMD-20W CE (Class B, Negative line)



URB4815YMD-20W CE (Class B, Negative line)

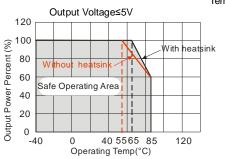


URB2405YMD-20W RE(Class B, Vertical)

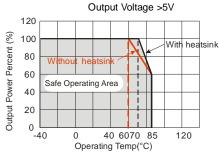


URB4815YMD-20W RE(Class B, Vertical)

PRODUCT TYPICAL CURVE

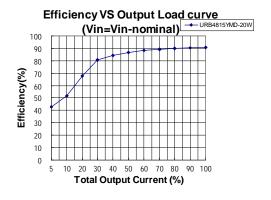


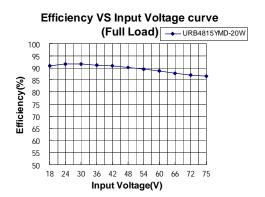
Temperature Derating Curve

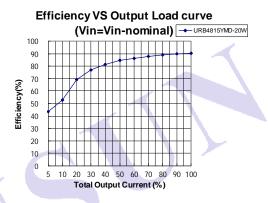


(Figure3)

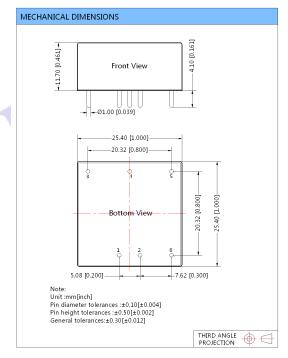
Efficiency VS Input Voltage curve (Full Load) → URB2405YMD-20W 100 95 90 Efficiency(%) 85 80 75 70 65 60 55 50 12 18 21 24 27 30 33 36 9 15 Input Voltage(V)

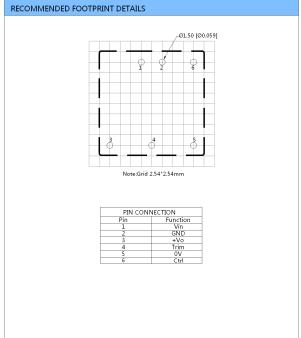




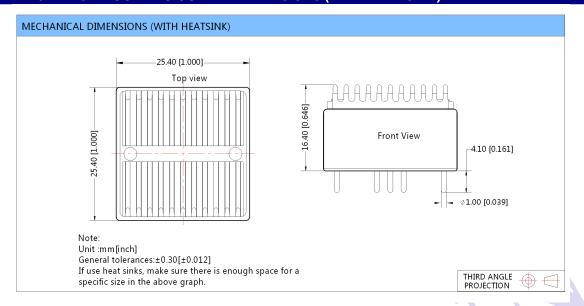


URB_YMD-20W PCB MOUNTING OUTLINE DIMENSIONS,RECOMMENDED FOOTPRINT (WITHOUT HEAT SINK)





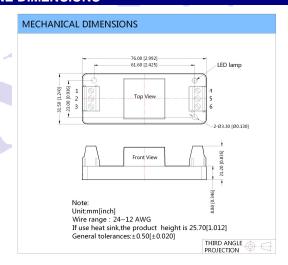
URB_YMD-20WH PCB MOUNTING OUTLINE DIMENSIONS (WITH HEAT SINK)



URB YMD-20WA2S CHASSIS MOUNTING WITH OUTLINE DIMENSIONS



Footprint Details						
Pin	1	2	3	4	5	6
Function	Ctrl	GND	Vin	0V	Trim	+Vo

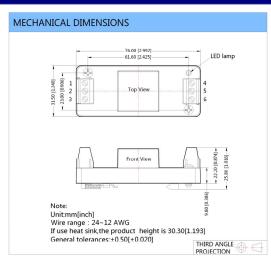


URB YMD-20WA4S DIN-RAIL MOUNTING OUTLINE DIMENSIONS



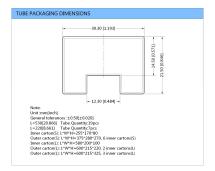
DIN-rail modules are fitting to TS35 rails

		Footpri	nt Details	3		
Pin	1	2	3	4	5	6
Function	Ctrl	GND	Vin	0V	Trim	+Vo

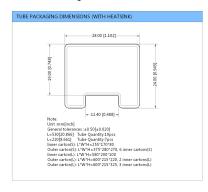


PACKAGE DIAGRAM

PCB mounting Series (Without heat sink)



PCB mounting Series (With heat sink)



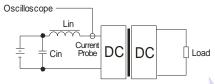
Special Package Series (A2S/A4S)



TEST CONFIGURATIONS

Input Reflected-Ripple Current Test Setup

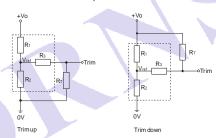
Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate source impedance.



Lin(4.7μH) Cin(220μF, ESR < 1.0Ω at 100 KHz)

TRIM APPLICATION & TRIM RESISTANCE

Application circuit for TRIM (Part in broken line is the interior of models)



Formula for resistance of TRIM

up:
$$R_{T} = \frac{aR_2}{R_2 - a} - R_3$$
 $a = \frac{Vref}{Vo' - Vref} \cdot R_1$
down: $R_{T} = \frac{aR_1}{R_1 - a} - R_3$ $a = \frac{Vo' - Vref}{Vref} \cdot R_2$

Note: Leave open if not used. Value for R1, R2, R3, and V_{ref} refer to the above table 1. R_T : Resistance of Trim. a: User-defined parameter, no actual meanings. Vo': The trim up/down voltage.

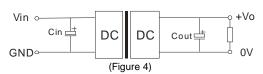
(Table 1) Vo 3.3(VDC) 5(VDC) 12(VDC) 15(VDC) Param eter R1(KΩ) 4.841 15 10 15 R2(KΩ) 2.87 15 2.609 3 $R3(K\Omega)$ 12.4 15 15 20 Vref(V) 1.24 2.5 2.5 2.5

DESIGN CONSIDERATIONS

Recommended circuit

All the URB_YMD-20W Series have been tested according to the following recommended testing circuit before leaving factory (see Figure 4) The product must be tested with load.

If you want to further decrease the input surge voltage and the output ripple, you can increase a capacitance properly or choose capacitors with low ESR. It should also be noted that the capacitance of filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the recommended capacitance of its filter capacitor sees (Table 2).



EXTERNAL CAPACITOR TABLE (Table 2)

Capacitance Vout	Cout(µF)	Cin(µF)
3.3V/5V	470	100
12V/15V	220	100

②It is not recommended to increase the output power capability by connecting two or more converters in parallel. The product is not hot-swappable.

Note:

- 1. Min. load shouldn't be less than 5%, otherwise ripple maybe increased dramatically, If the product operates under min. load, it may not be guaranteed to meet all specifications listed. Operation under minimum load will not damage the converter.
- 2. Recommended Dual output models unbalanced load is ≤±5%, If the product operates >±5%, it may not be guaranteed to meet all specifications listed. Please contact our technical support for more details.
- 3. Max. Capacitive Load is tested at input voltage range and full load.
- 4. All specifications measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.
- 5. In this datasheet, all test methods are based on our corporate standards.
- 6. All characteristics are for listed models, and non-standard models may perform differently. Please contact our technical support for more details.
- 7. Please contact our technical support for any specific requirement.
- 8. Specifications of this product are subject to changes without prior notice.

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