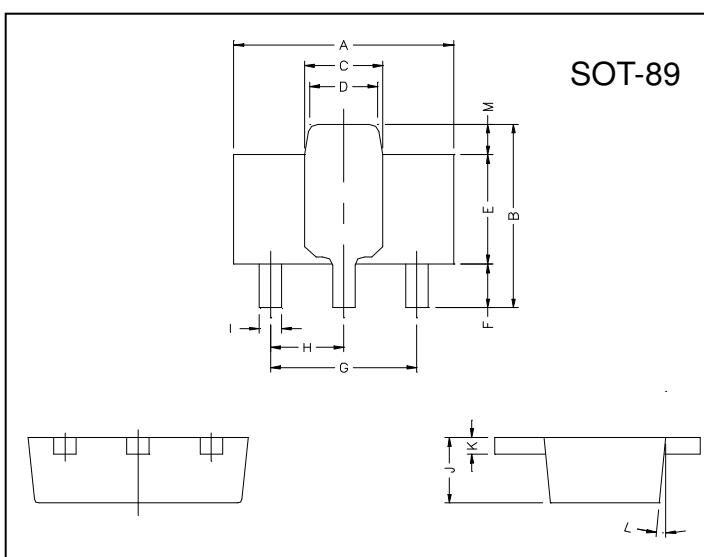
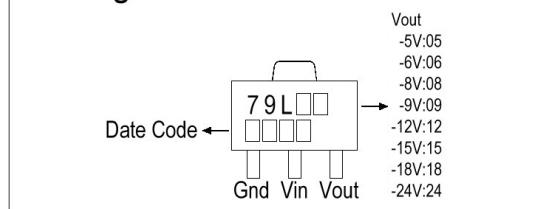


**GM79LXX****NEGATIVE VOLTAGE REGULATOR****Description**

The GM79LXX series of fixed-voltage monolithic integrated circuit voltage regulators are designed for a wide range of applications. These applications include on-card regulation for elimination of noise and distribution problems associated with single-point regulation. In addition, they can be used with power-pass elements to make high current voltage regulators. Each of these regulators can deliver up to 100mA of output current. The internal limiting and thermal shutdown features of these regulators make them essentially immune to overload. When used as a replacement for a Zener diode-resistor combination, an effective improvement in output impedance can be obtained together with lower-bias current.

**Features**

- Fixed output voltage of -5V, -6V, -8V, -9V, -12V, -15V, -18V, -24V
- Internal Short-Circuit Current Limiting
- Internal Thermal Overload Protection
- No External Components Required

**Package Dimensions****Marking :**

REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.40	4.60	G	3.00	REF.
B	4.05	4.25	H	1.50	REF.
C	1.50	1.70	I	0.40	0.52
D	1.30	1.50	J	1.40	1.60
E	2.40	2.60	K	0.35	0.41
F	0.89	1.20	L	5° TYP.	
			M	0.70 REF.	

**Absolute Maximum Ratings**

Parameter	Ratings	Unit
Input voltage	GM79L05 ~ 09	V
	GM79L12 ~ 18	V
	GM79L24	V
Output current	100	mA
Operating junction temperature range	0 ~ 125	°C
Storage temperature range	-65 ~ 150	°C
Power Dissipation	350*	mW

\*When tested in free air condition, without heat sinking.

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ISSUED DATE :2006/07/12  
REVISED DATE :

## Electrical Characteristics

**GM79L05** (Refer to the test circuits,  $T_j=0\sim125^\circ C$ ,  $I_o=40mA$ ,  $V_{in}=-10V$ ,  $C_{in}=0.33\mu F$ ,  $C_o=0.1\mu F$  unless otherwise specified) (Note1)

Symbol		Min.	Typ.	Max.	Unit	Test Conditions
VO	A-Rank (3%)	-4.85	-5.0	-5.15	V	Vin= -10V, $I_o=40mA$ , $T_j=25^\circ C$ $-7V \leq V_{in} \leq -20V$ , $1mA \leq I_o \leq 40mA$ Vin= -10V, $1mA \leq I_o \leq 70mA$ (Note2)
	B-Rank (5%)	-4.75	-	-5.25		
$\Delta V_O$ (Line Regulation)		-	15	150	mV	$-7V \leq V_{in} \leq -20V$ , $I_o=40mA$ , $T_j=25^\circ C$
$\Delta V_O$ (Load Regulation)		-	20	60	mV	Vin= -10V, $1mA \leq I_o \leq 100mA$ , $T_j=25^\circ C$
IQ		-	-	6.0	mA	Vin= -10V, $I_o=40mA$ , $T_j=25^\circ C$
$\Delta IQ$	-	-	0.1	mA	Vin= -10V, $1mA \leq I_o \leq 40mA$	
	-	-	1.5		$-8V \leq V_{in} \leq -20V$ , $I_o=40mA$	
Vn		-	40	-	$\mu V$	$10Hz \leq f \leq 100KHz$
RR		41	49	-	dB	$-8V \leq V_{in} \leq -18V$ , $I_o=40mA$ , $f=120Hz$ , $T_j=25^\circ C$
VD		-	1.7	-	V	$I_o=100mA$ , $T_j=25^\circ C$

**GM79L06** (Refer to the test circuits,  $T_j=0\sim125^\circ C$ ,  $I_o=40mA$ ,  $V_{in}=-11V$ ,  $C_{in}=0.33\mu F$ ,  $C_o=0.1\mu F$  unless otherwise specified) (Note1)

Symbol		Min.	Typ.	Max.	Unit	Test Conditions
VO	A-Rank (3%)	-5.82	-6.0	-6.18	V	Vin= -11V, $I_o=40mA$ , $T_j=25^\circ C$ $-8V \leq V_{in} \leq -20V$ , $1mA \leq I_o \leq 40mA$ Vin= -11V, $1mA \leq I_o \leq 70mA$ (Note2)
	B-Rank (5%)	-5.70	-	-6.30		
$\Delta V_O$ (Line Regulation)		-	20	150	mV	$-8V \leq V_{in} \leq -20V$ , $I_o=40mA$ , $T_j=25^\circ C$
$\Delta V_O$ (Load Regulation)		-	21	60	mV	Vin= -11V, $1mA \leq I_o \leq 100mA$ , $T_j=25^\circ C$
IQ		-	-	6.0	mA	Vin= -11V, $I_o=40mA$ , $T_j=25^\circ C$
$\Delta IQ$	-	-	0.1	mA	Vin= -11V, $1mA \leq I_o \leq 40mA$	
	-	-	1.5		$-9V \leq V_{in} \leq -20V$ , $I_o=40mA$	
Vn		-	44	-	$\mu V$	$10Hz \leq f \leq 100KHz$
RR		40	48	-	dB	$-9V \leq V_{in} \leq 19V$ , $I_o=40mA$ , $f=120Hz$ , $T_j=25^\circ C$
VD		-	1.7	-	V	$I_o=100mA$ , $T_j=25^\circ C$

**GM79L08** (Refer to the test circuits,  $T_j=0\sim125^\circ C$ ,  $I_o=40mA$ ,  $V_{in}=-14V$ ,  $C_{in}=0.33\mu F$ ,  $C_o=0.1\mu F$  unless otherwise specified) (Note1)

Symbol		Min.	Typ.	Max.	Unit	Test Conditions
VO	A-Rank (3%)	-7.76	-8.0	-8.24	V	Vin= -14V, $I_o=40mA$ , $T_j=25^\circ C$ $-10.5V \leq V_{in} \leq -23V$ , $1mA \leq I_o \leq 40mA$ Vin= -14V, $1mA \leq I_o \leq 70mA$ (Note2)
	B-Rank (5%)	-7.60	-	-8.40		
$\Delta V_O$ (Line Regulation)		-	42	175	mV	$-10.5V \leq V_{in} \leq -23V$ , $I_o=40mA$ , $T_j=25^\circ C$
$\Delta V_O$ (Load Regulation)		-	30	80	mV	Vin= -14V, $1mA \leq I_o \leq 100mA$ , $T_j=25^\circ C$
IQ		-	-	6.0	mA	Vin= -14V, $I_o=40mA$ , $T_j=25^\circ C$
$\Delta IQ$	-	-	0.1	mA	Vin= -14V, $1mA \leq I_o \leq 40mA$	
	-	-	1.5		$-11V \leq V_{in} \leq -23V$ , $I_o=40mA$	
Vn		-	54	-	$\mu V$	$10Hz \leq f \leq 100KHz$
RR		37	46	-	dB	$-11V \leq V_{in} \leq -21V$ , $I_o=40mA$ , $f=120Hz$ , $T_j=25^\circ C$
VD		-	1.7	-	V	$I_o=100mA$ , $T_j=25^\circ C$

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**GM79L09** (Refer to the test circuits,  $T_j=0\sim125^\circ C$ ,  $I_o=40mA$ ,  $V_{in}=-15V$ ,  $C_{in}=0.33\mu F$ ,  $C_{o}=0.1\mu F$  unless otherwise specified) (Note1)

Symbol		Min.	Typ.	Max.	Unit	Test Conditions
VO	A-Rank (3%)	-8.73	-9.0	-9.27	V	$V_{in} = -15V$ , $I_o = 40mA$ , $T_j = 25^\circ C$ $-11.5V \leq V_{in} \leq -24V$ , $1mA \leq I_o \leq 40mA$ $V_{in} = -15V$ , $1mA \leq I_o \leq 70mA$ (Note2)
	B-Rank (5%)	-8.55	-	-9.45		
$\Delta V_{O}$ (Line Regulation)	-	42	200	mV		$-11.5V \leq V_{in} \leq -24V$ , $I_o = 40mA$ , $T_j = 25^\circ C$
$\Delta V_{O}$ (Load Regulation)	-	30	90	mV		$V_{in} = -15V$ , $1mA \leq I_o \leq 100mA$ , $T_j = 25^\circ C$
$I_{Q}$	-	-	6.0	mA		$V_{in} = -15V$ , $I_o = 40mA$ , $T_j = 25^\circ C$
$\Delta I_{Q}$	-	-	0.1	mA	$V_{in} = -15V$ , $1mA \leq I_o \leq 40mA$	
	-	-	1.5		$-12V \leq V_{in} \leq -24V$ , $I_o = 40mA$	
$V_n$	-	54	-	$\mu V$		$10Hz \leq f \leq 100KHz$
RR	37	46	-	dB		$-12V \leq V_{in} \leq -22V$ , $I_o = 40mA$ , $f = 120Hz$ , $T_j = 25^\circ C$
VD	-	1.7	-	V		$I_o = 100mA$ , $T_j = 25^\circ C$

**GM79L12** (Refer to the test circuits,  $T_j=0\sim125^\circ C$ ,  $I_o=40mA$ ,  $V_{in}=-19V$ ,  $C_{in}=0.33\mu F$ ,  $C_{o}=0.1\mu F$  unless otherwise specified) (Note1)

Symbol		Min.	Typ.	Max.	Unit	Test Conditions
VO	A-Rank (3%)	-11.64	-12.0	-12.36	V	$V_{in} = -19V$ , $I_o = 40mA$ , $T_j = 25^\circ C$ $-14.5V \leq V_{in} \leq -27V$ , $1mA \leq I_o \leq 40mA$ $V_{in} = -19V$ , $1mA \leq I_o \leq 70mA$ (Note2)
	B-Rank (5%)	-11.40	-	-12.60		
$\Delta V_{O}$ (Line Regulation)	-	50	250	mV		$-14.5V \leq V_{in} \leq -27V$ , $I_o = 40mA$ , $T_j = 25^\circ C$
$\Delta V_{O}$ (Load Regulation)	-	24	100	mV		$V_{in} = -19V$ , $1mA \leq I_o \leq 100mA$ , $T_j = 25^\circ C$
$I_{Q}$	-	-	6.5	mA		$V_{in} = -19V$ , $I_o = 40mA$ , $T_j = 25^\circ C$
$\Delta I_{Q}$	-	-	0.1	mA	$V_{in} = -19V$ , $1mA \leq I_o \leq 40mA$	
	-	-	1.5		$-16V \leq V_{in} \leq -27V$ , $I_o = 40mA$	
$V_n$	-	80	-	$\mu V$		$10Hz \leq f \leq 100KHz$
RR	37	42	-	dB		$-15V \leq V_{in} \leq -25V$ , $I_o = 40mA$ , $f = 120Hz$ , $T_j = 25^\circ C$
VD	-	1.7	-	V		$I_o = 100mA$ , $T_j = 25^\circ C$

**GM79L15** (Refer to the test circuits,  $T_j=0\sim125^\circ C$ ,  $I_o=40mA$ ,  $V_{in}=-23V$ ,  $C_{in}=0.33\mu F$ ,  $C_{o}=0.1\mu F$  unless otherwise specified) (Note1)

Symbol		Min.	Typ.	Max.	Unit	Test Conditions
VO	A-Rank (3%)	-14.55	-15.0	-15.45	V	$V_{in} = -23V$ , $I_o = 40mA$ , $T_j = 25^\circ C$ $-17.5V \leq V_{in} \leq -30V$ , $1mA \leq I_o \leq 40mA$ $V_{in} = -23V$ , $1mA \leq I_o \leq 70mA$ (Note2)
	B-Rank (5%)	-14.25	-	-15.75		
$\Delta V_{O}$ (Line Regulation)	-	65	300	mV		$-17.5V \leq V_{in} \leq -30V$ , $I_o = 40mA$ , $T_j = 25^\circ C$
$\Delta V_{O}$ (Load Regulation)	-	25	150	mV		$V_{in} = -23V$ , $1mA \leq I_o \leq 100mA$ , $T_j = 25^\circ C$
$I_{Q}$	-	-	6.5	mA		$V_{in} = -23V$ , $I_o = 40mA$ , $T_j = 25^\circ C$
$\Delta I_{Q}$	-	-	0.1	mA	$V_{in} = -23V$ , $1mA \leq I_o \leq 40mA$	
	-	-	1.5		$-20V \leq V_{in} \leq -30V$ , $I_o = 40mA$	
$V_n$	-	90	-	$\mu V$		$10Hz \leq f \leq 100KHz$
RR	34	39	-	dB		$-18.5V \leq V_{in} \leq -28.5V$ , $I_o = 40mA$ , $f = 120Hz$ , $T_j = 25^\circ C$
VD	-	1.7	-	V		$I_o = 100mA$ , $T_j = 25^\circ C$

**GM79L18** (Refer to the test circuits,  $T_j=0\sim125^\circ C$ ,  $I_o=40mA$ ,  $V_{in}=-27V$ ,  $C_{in}=0.33\mu F$ ,  $C_{o}=0.1\mu F$  unless otherwise specified) (Note1)

Symbol		Min.	Typ.	Max.	Unit	Test Conditions
VO	A-Rank (3%)	-17.46	-18.0	-18.54	V	$V_{in}=-27V$ , $I_o=40mA$ , $T_j=25^\circ C$
	B-Rank (5%)	-17.10	-	-18.9		$-20.5V \leq V_{in} \leq -33V$ , $1mA \leq I_o \leq 40mA$ $V_{in}=-27V$ , $1mA \leq I_o \leq 70mA$ (Note2)
$\Delta VO$ (Line Regulation)		-	70	300	mV	$-20.5V \leq V_{in} \leq -33V$ , $I_o=40mA$ , $T_j=25^\circ C$
$\Delta VO$ (Load Regulation)		-	27	170	mV	$V_{in}=-27V$ , $1mA \leq I_o \leq 100mA$ , $T_j=25^\circ C$
IQ		-	-	6.5	mA	$V_{in}=-27V$ , $I_o=40mA$ , $T_j=25^\circ C$
$\Delta IQ$	-	-	0.1	mA	$V_{in}=-27V$ , $1mA \leq I_o \leq 40mA$	
	-	-	1.5		$-21V \leq V_{in} \leq -33V$ , $I_o=40mA$	
Vn		-	150	-	$\mu V$	$10Hz \leq f \leq 100KHz$
RR		33	48	-	dB	$-23V \leq V_{in} \leq -33V$ , $I_o=40mA$ , $f=120Hz$ , $T_j=25^\circ C$
VD		-	1.7	-	V	$I_o=100mA$ , $T_j=25^\circ C$

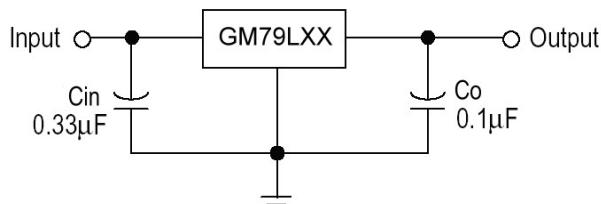
**GM79L24** (Refer to the test circuits,  $T_j=0\sim125^\circ C$ ,  $I_o=40mA$ ,  $V_{in}=-33V$ ,  $C_{in}=0.33\mu F$ ,  $C_{o}=0.1\mu F$  unless otherwise specified) (Note1)

Symbol		Min.	Typ.	Max.	Unit	Test Conditions
VO	A-Rank (3%)	-23.28	-24.0	-24.72	V	$V_{in}=-33V$ , $I_o=40mA$ , $T_j=25^\circ C$
	B-Rank (5%)	-22.80	-	-25.20		$-27V \leq V_{in} \leq -38V$ , $1mA \leq I_o \leq 40mA$ $V_{in}=-33V$ , $1mA \leq I_o \leq 70mA$ (Note2)
$\Delta VO$ (Line Regulation)		-	90	350	mV	$-27V \leq V_{in} \leq -38V$ , $I_o=40mA$ , $T_j=25^\circ C$
$\Delta VO$ (Load Regulation)		-	40	200	mV	$V_{in}=-33V$ , $1mA \leq I_o \leq 100mA$ , $T_j=25^\circ C$
IQ		-	-	6.5	mA	$V_{in}=-33V$ , $I_o=40mA$ , $T_j=25^\circ C$
$\Delta IQ$	-	-	0.1	mA	$V_{in}=-33V$ , $1mA \leq I_o \leq 40mA$	
	-	-	1.5		$-28V \leq V_{in} \leq -38V$ , $I_o=40mA$	
Vn		-	200	-	$\mu V$	$10Hz \leq f \leq 100KHz$
RR		31	47	-	dB	$-29V \leq V_{in} \leq -35V$ , $I_o=40mA$ , $f=120Hz$ , $T_j=25^\circ C$
VD		-	1.7	-	V	$I_o=100mA$ , $T_j=25^\circ C$

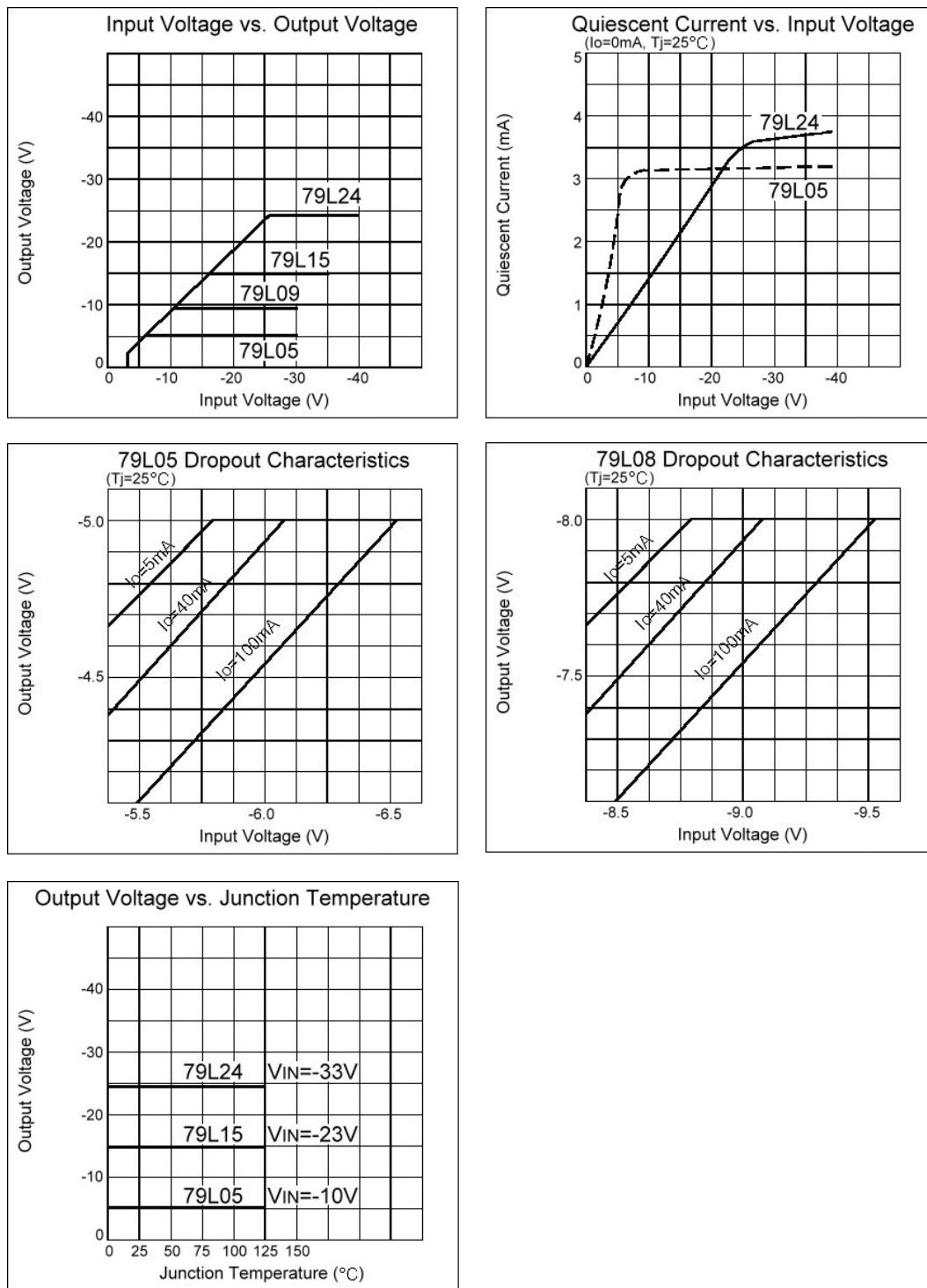
Note1: The Maximum steady state usable output current is dependent on input voltage, heat sinking, lead length of the package and copper of PCB. The data above represent pulse test conditions with junction temperatures specified at the initiation of test.

Note2: Power dissipation<0.5W

## Typical Application



## Characteristics Curve



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