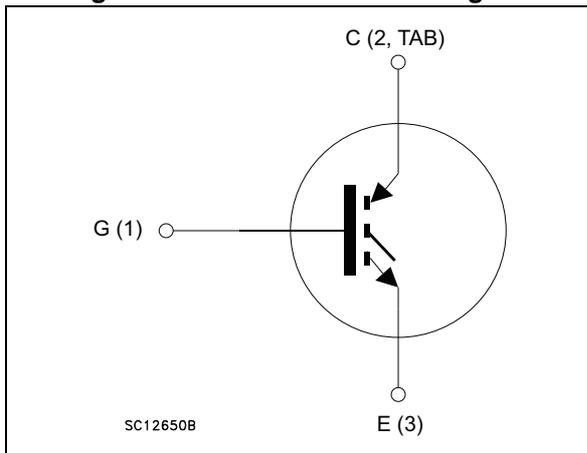


Figure 1. Internal schematic diagram



Features

- Maximum junction temperature: $T_J = 175\text{ }^\circ\text{C}$
- High speed switching series
- Minimized tail current
- $V_{CE(sat)} = 1.55\text{ V (typ.) @ } I_C = 30\text{ A}$
- Tight parameters distribution
- Safe paralleling
- Low thermal resistance
- Lead free package

Applications

- Photovoltaic inverters
- High frequency converters

Description

These devices are IGBTs developed using an advanced proprietary trench gate and field stop structure. The device is part of the new "HB" series of IGBTs, which represent an optimum compromise between conduction and switching losses to maximize the efficiency of any frequency converter. Furthermore, a slightly positive $V_{CE(sat)}$ temperature coefficient and very tight parameter distribution result in safer paralleling operation.

Table 1. Device summary

Order code	Marking	Package	Packaging
STGFW30H65FB	GFW30H65FB	TO-3PF	Tube
STGW30H65FB	GW30H65FB	TO-247	Tube
STGWT30H65FB	GWT30H65FB	TO-3P	Tube

Contents

1	Electrical ratings	3
2	Electrical characteristics	4
	2.1 Electrical characteristics (curve)	6
3	Test circuits	12
4	Package mechanical data	13
	4.1 TO-3PF, STGFW30H65FB	13
	4.2 TO-247, STGW30H65FB	15
	4.3 TO-3P, STGWT30H65FB	17
5	Revision history	19

1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value		Unit
		TO-247 TO-3P	TO-3PFP	
V_{CES}	Collector-emitter voltage ($V_{GE} = 0$)	650		V
I_C	Continuous collector current at $T_C = 25\text{ °C}$	60		A
I_C	Continuous collector current at $T_C = 100\text{ °C}$	30		A
$I_{CP}^{(1)}$	Pulsed collector current	120		A
V_{GE}	Gate-emitter voltage	± 20		V
P_{TOT}	Total dissipation at $T_C = 25\text{ °C}$	260	58	W
T_{STG}	Storage temperature range	- 55 to 150		$^{\circ}\text{C}$
T_J	Operating junction temperature	- 55 to 175		$^{\circ}\text{C}$

1. Pulse width limited by maximum junction temperature.

Table 3. Thermal data

Symbol	Parameter	Value		Unit
		TO-3PF	TO-247 TO-3P	
R_{thJC}	Thermal resistance junction-case	2.60	0.58	$^{\circ}\text{C/W}$
R_{thJA}	Thermal resistance junction-ambient	50		$^{\circ}\text{C/W}$

2 Electrical characteristics

$T_J = 25\text{ °C}$ unless otherwise specified.

Table 4. Static characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(BR)CES}$	Collector-emitter breakdown voltage ($V_{GE} = 0$)	$I_C = 2\text{ mA}$	650			V
$V_{CE(sat)}$	Collector-emitter saturation voltage	$V_{GE} = 15\text{ V}, I_C = 30\text{ A}$		1.55	2	V
		$V_{GE} = 15\text{ V}, I_C = 30\text{ A}$ $T_J = 125\text{ °C}$		1.65		
		$V_{GE} = 15\text{ V}, I_C = 30\text{ A}$ $T_J = 175\text{ °C}$		1.75		
$V_{GE(th)}$	Gate threshold voltage	$V_{CE} = V_{GE}, I_C = 1\text{ mA}$	5	6	7	V
I_{CES}	Collector cut-off current ($V_{GE} = 0$)	$V_{CE} = 650\text{ V}$			25	μA
I_{GES}	Gate-emitter leakage current ($V_{CE} = 0$)	$V_{GE} = \pm 20\text{ V}$			250	nA

Table 5. Dynamic characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
C_{ies}	Input capacitance	$V_{CE} = 25\text{ V}, f = 1\text{ MHz},$ $V_{GE} = 0$	-	3659	-	pF
C_{oes}	Output capacitance		-	101	-	pF
C_{res}	Reverse transfer capacitance		-	76	-	pF
Q_g	Total gate charge	$V_{CC} = 520\text{ V}, I_C = 30\text{ A},$ $V_{GE} = 15\text{ V},$ see Figure 28	-	149	-	nC
Q_{ge}	Gate-emitter charge		-	25	-	nC
Q_{gc}	Gate-collector charge		-	62	-	nC

Table 6. Switching characteristics (inductive load)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on delay time	$V_{CE} = 400\text{ V}$, $I_C = 30\text{ A}$, $R_G = 10\ \Omega$, $V_{GE} = 15\text{ V}$, see Figure 27	-	37	-	ns
t_r	Current rise time		-	14.6	-	ns
$(di/dt)_{on}$	Turn-on current slope		-	1643	-	A/ μ s
$t_{d(off)}$	Turn-off delay time		-	146	-	ns
t_f	Current fall time		-	23	-	ns
$E_{on}^{(1)}$	Turn-on switching losses		-	151	-	μ J
$E_{off}^{(2)}$	Turn-off switching losses		-	293	-	μ J
E_{ts}	Total switching losses		-	444	-	μ J
$t_{d(on)}$	Turn-on delay time	$V_{CE} = 400\text{ V}$, $I_C = 30\text{ A}$, $R_G = 10\ \Omega$, $V_{GE} = 15\text{ V}$, $T_J = 175\text{ }^\circ\text{C}$, see Figure 27	-	35	-	ns
t_r	Current rise time		-	16.1	-	ns
$(di/dt)_{on}$	Turn-on current slope		-	1496	-	A/ μ s
$t_{d(off)}$	Turn-off delay time		-	158	-	ns
t_f	Current fall time		-	65	-	ns
$E_{on}^{(1)}$	Turn-on switching losses		-	175	-	μ J
$E_{off}^{(2)}$	Turn-off switching losses		-	572	-	μ J
E_{ts}	Total switching losses		-	747	-	μ J

1. Energy losses include reverse recovery of the external diode. Turn-on times and energy have been measured applying as freewheeling an external SiC diode STPSC206W
2. Turn-off losses include also the tail of the collector current.

2.1 Electrical characteristics (curve)

Figure 2. Output characteristics ($T_J = 25^\circ\text{C}$)

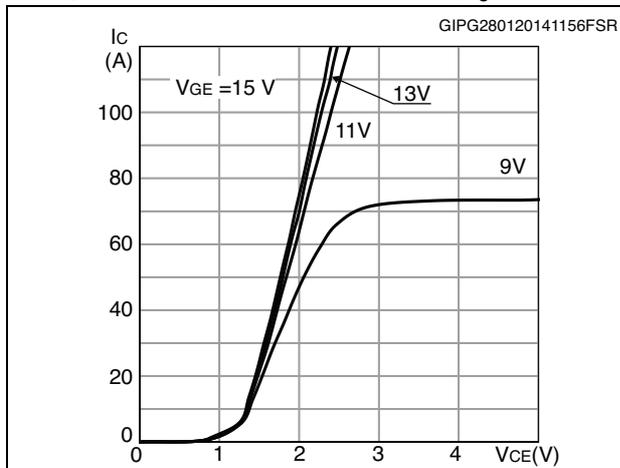


Figure 3. Output characteristics ($T_J = 175^\circ\text{C}$)

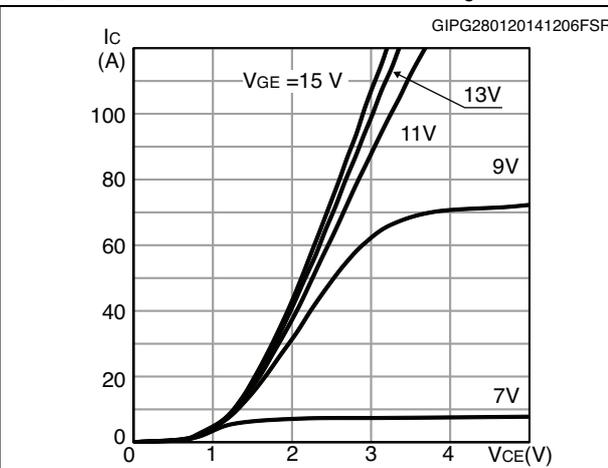


Figure 4. Transfer characteristics

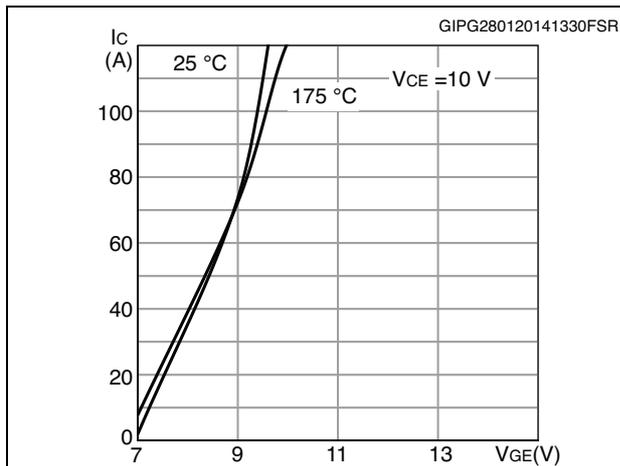


Figure 5. Collector current vs. case temperature for TO-247 and TO-3P

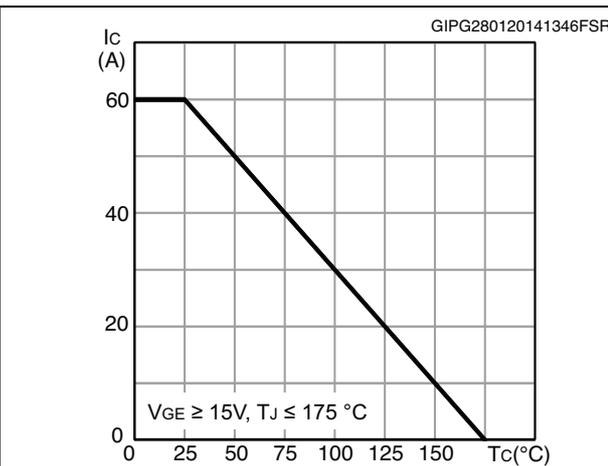


Figure 6. Collector current vs. case temperature for TO-3PF

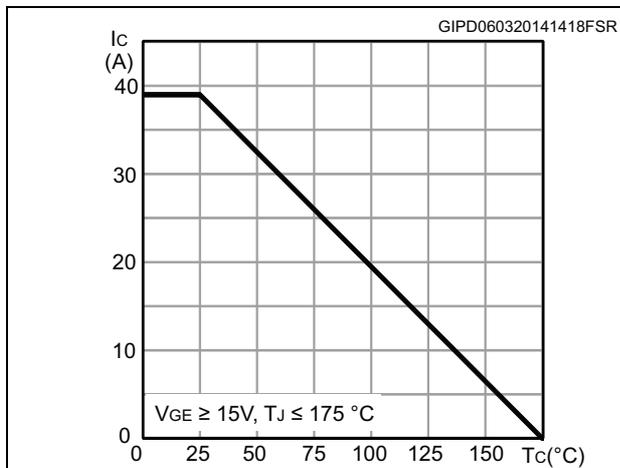


Figure 7. $V_{CE(sat)}$ vs. junction temperature

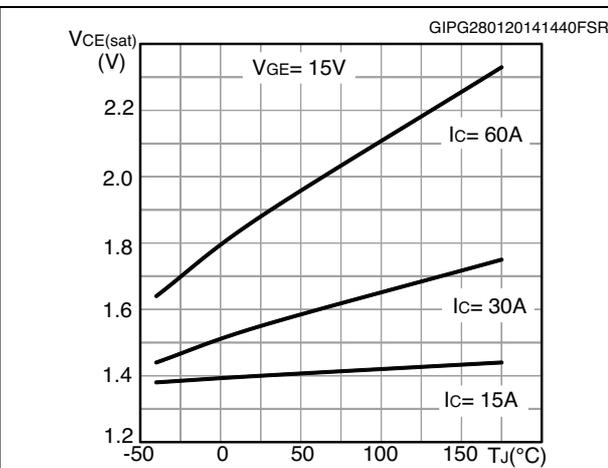


Figure 8. Power dissipation vs. case temperature for TO-247 and TO-3P

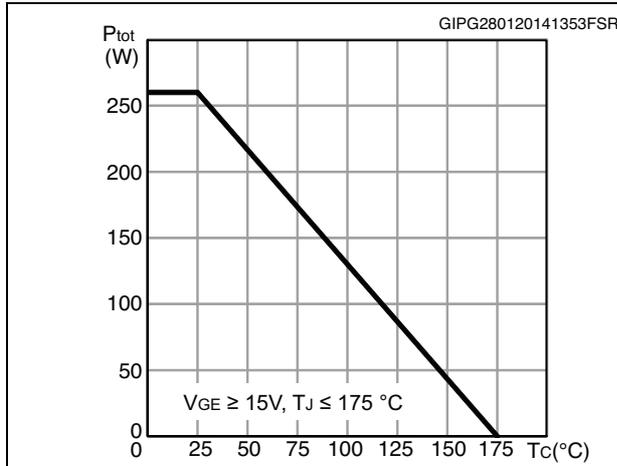


Figure 9. Power dissipation vs. case temperature for TO-3PF

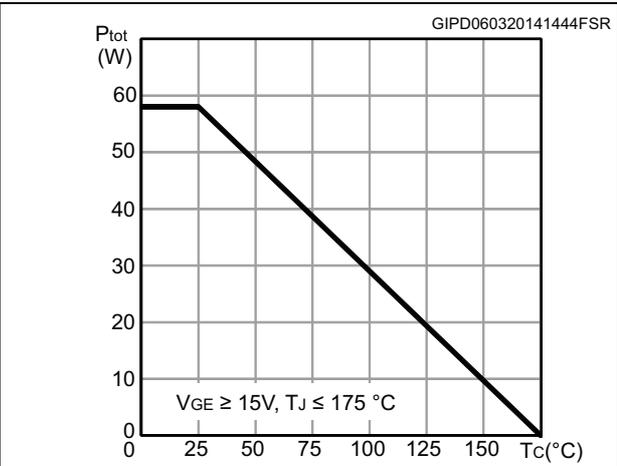


Figure 10. Forward bias safe operating area for TO-247 and TO-3P

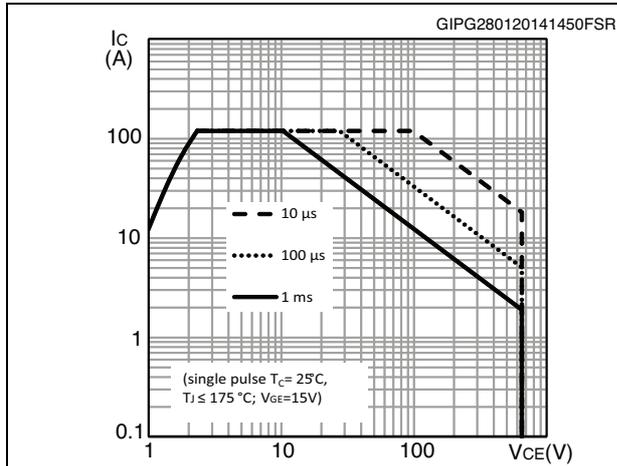


Figure 11. Forward bias safe operating area for TO-3PF

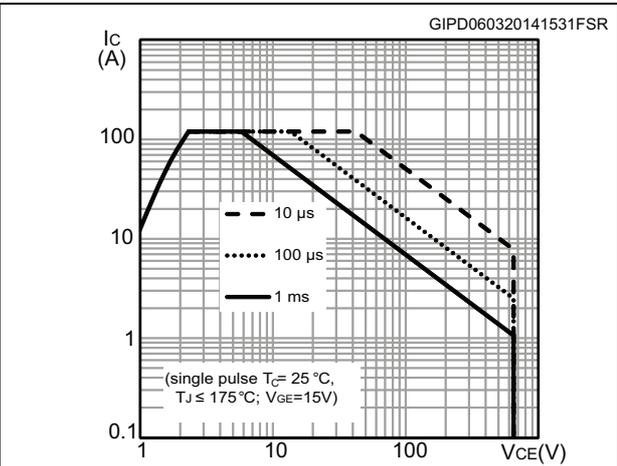


Figure 12. Collector current vs. switching frequency for TO-247 and TO-3P

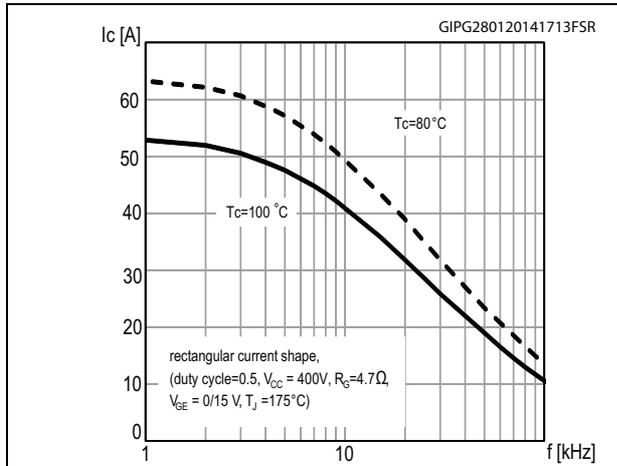


Figure 13. Collector current vs. switching frequency for TO-3PF

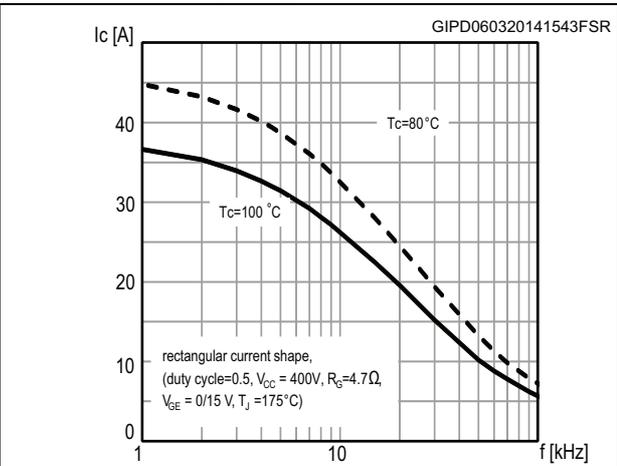


Figure 14. Normalized $V_{GE(th)}$ vs. junction temperature

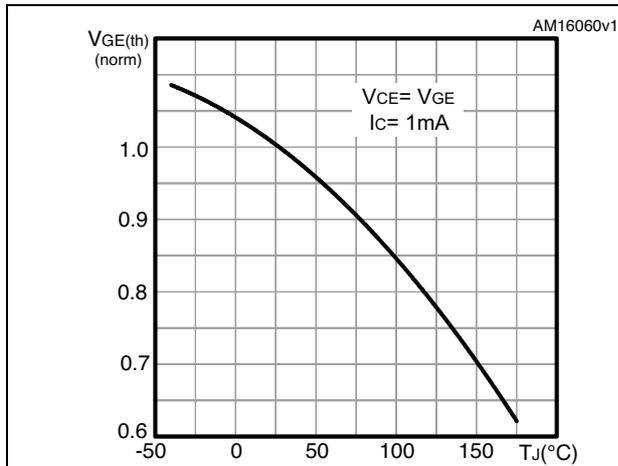


Figure 15. Normalized $V_{(BR)CES}$ vs. junction temperature

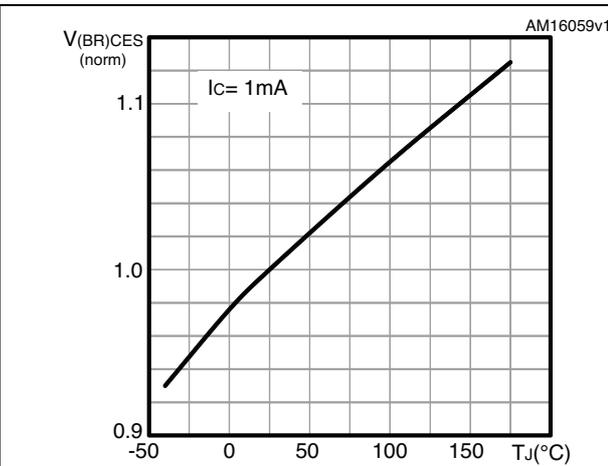


Figure 16. Switching losses vs temperature

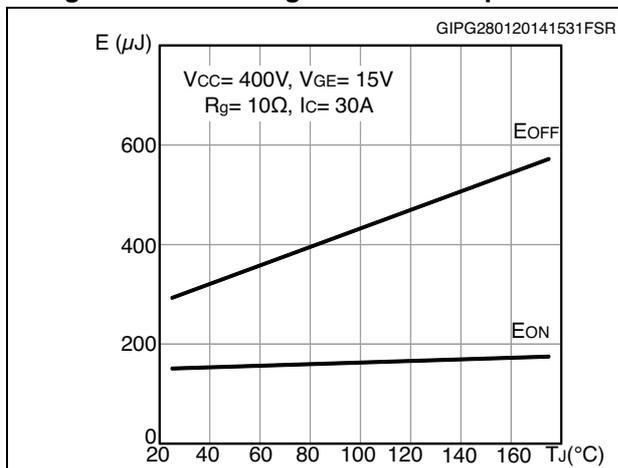


Figure 17. Switching losses vs gate resistance

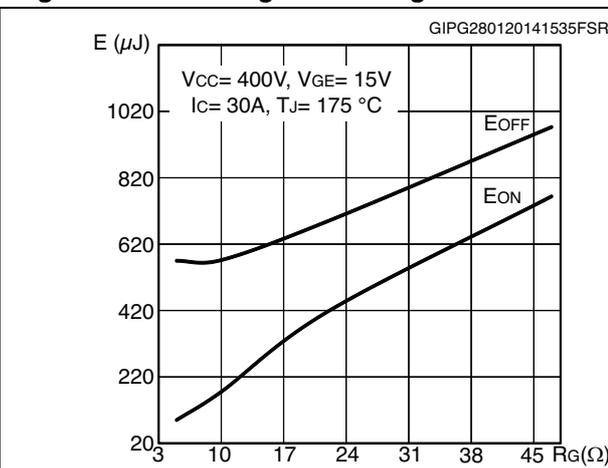


Figure 18. Switching losses vs collector current

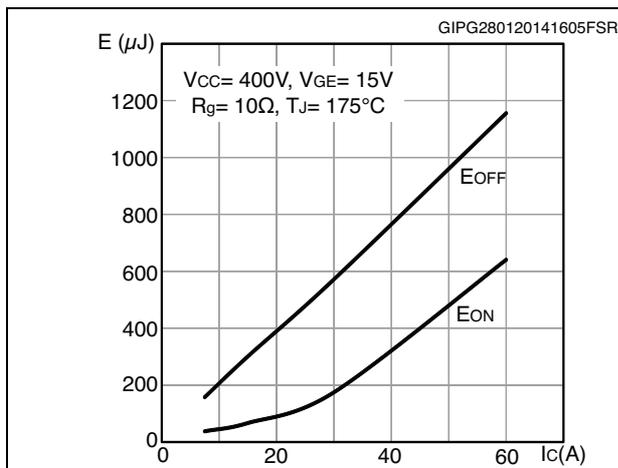


Figure 19. Switching losses vs collector emitter voltage

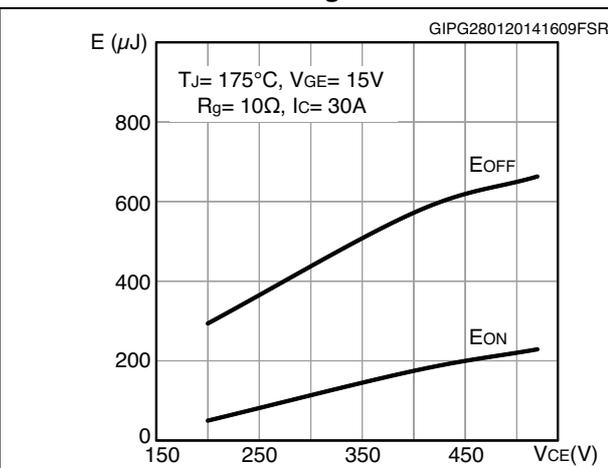


Figure 20. Switching times vs collector current

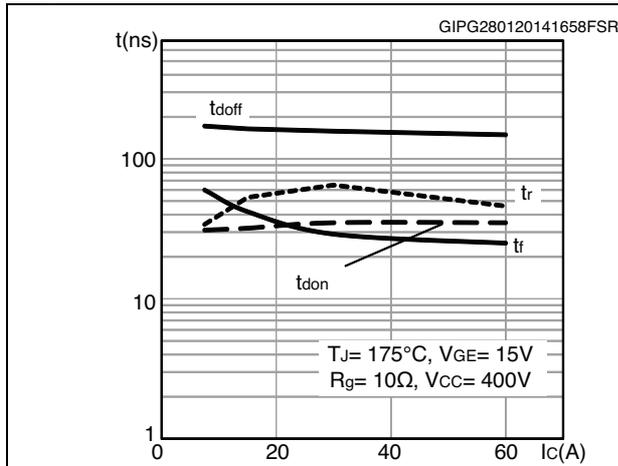


Figure 21. Switching times vs gate resistance

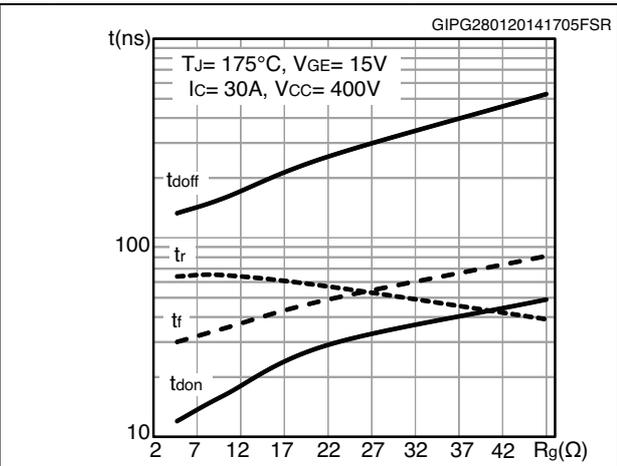


Figure 22. Capacitance variations

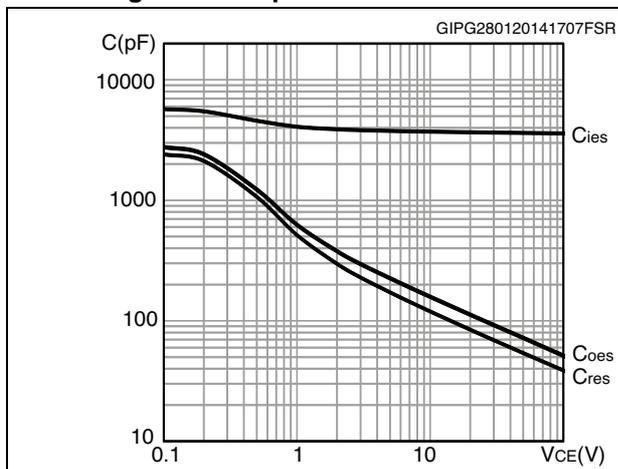


Figure 23. V_{CE(sat)} vs. collector current

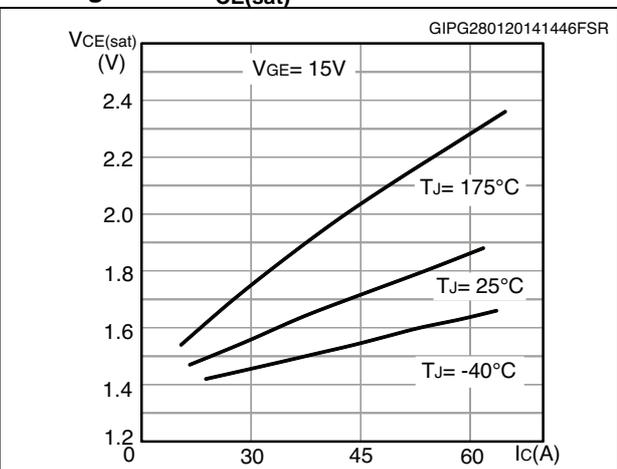


Figure 24. Gate charge vs. gate-emitter voltage

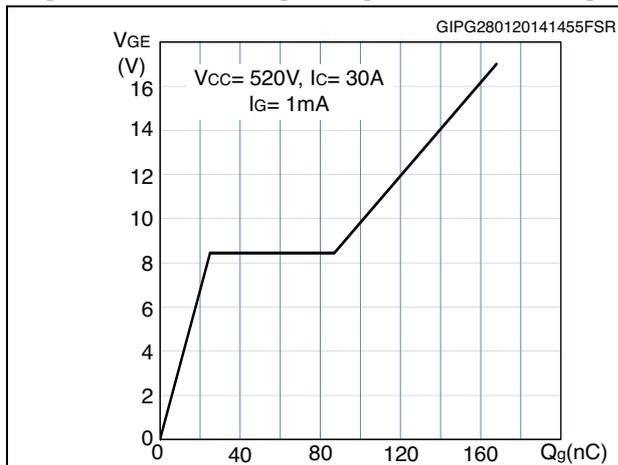


Figure 25. Thermal impedance for TO-247 and TO-3P

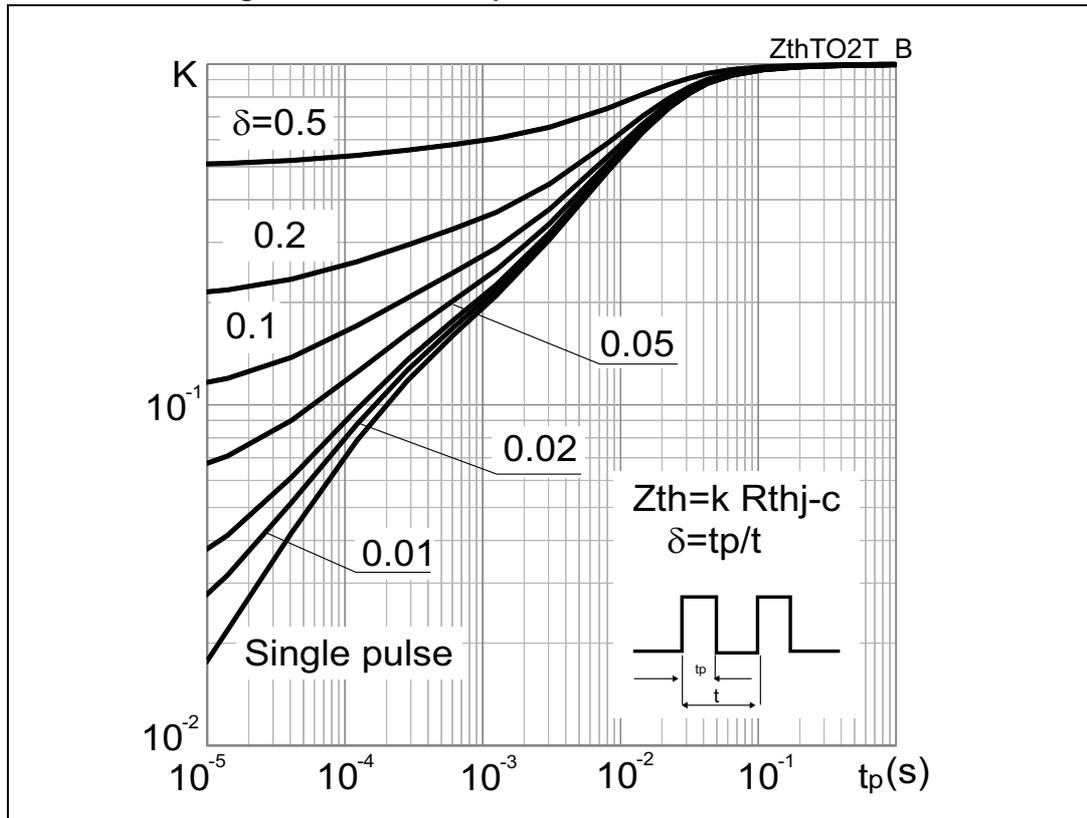
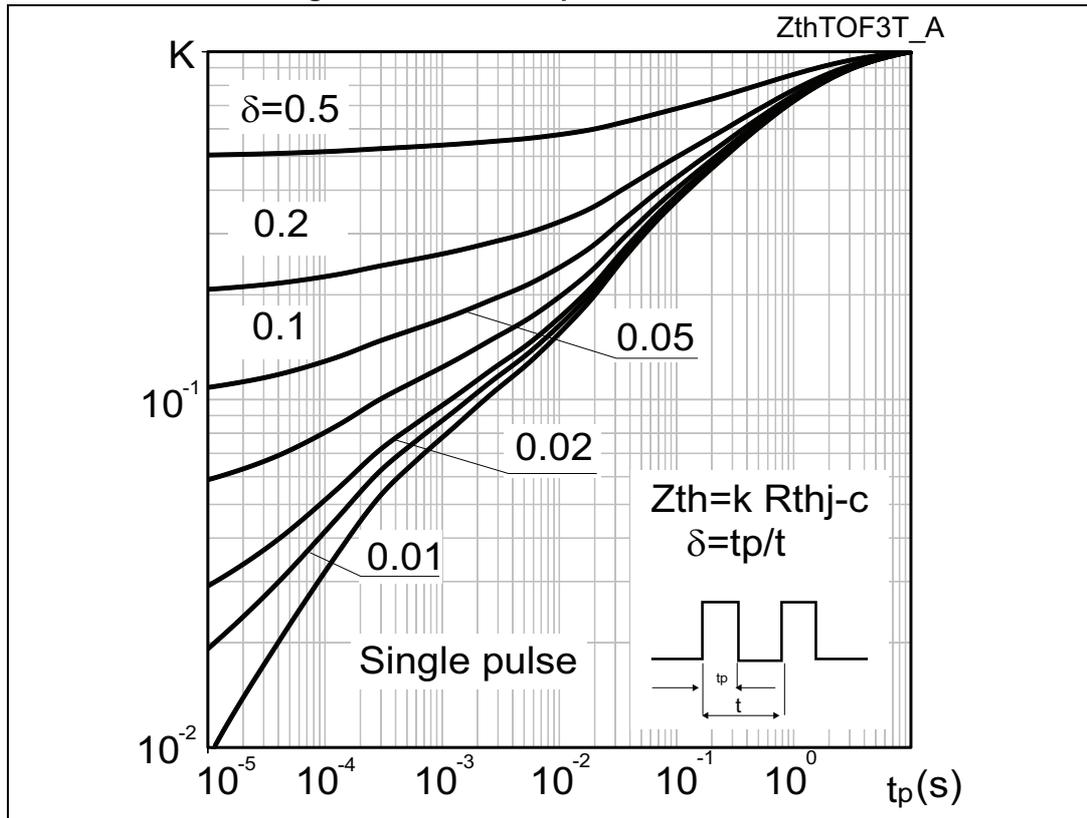


Figure 26. Thermal impedance for TO-3PF



4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

4.1 TO-3PF, STGFW30H65FB

Figure 30. TO-3PF drawing

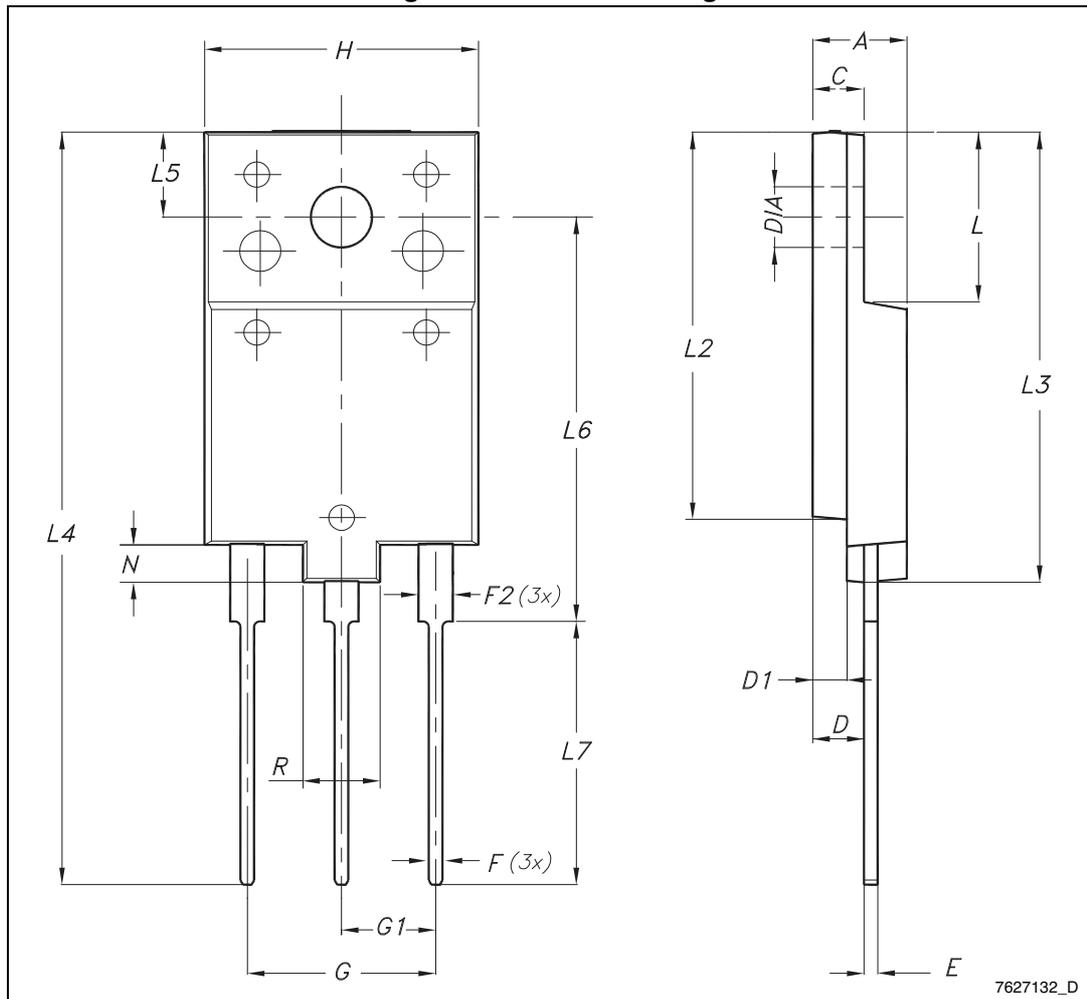
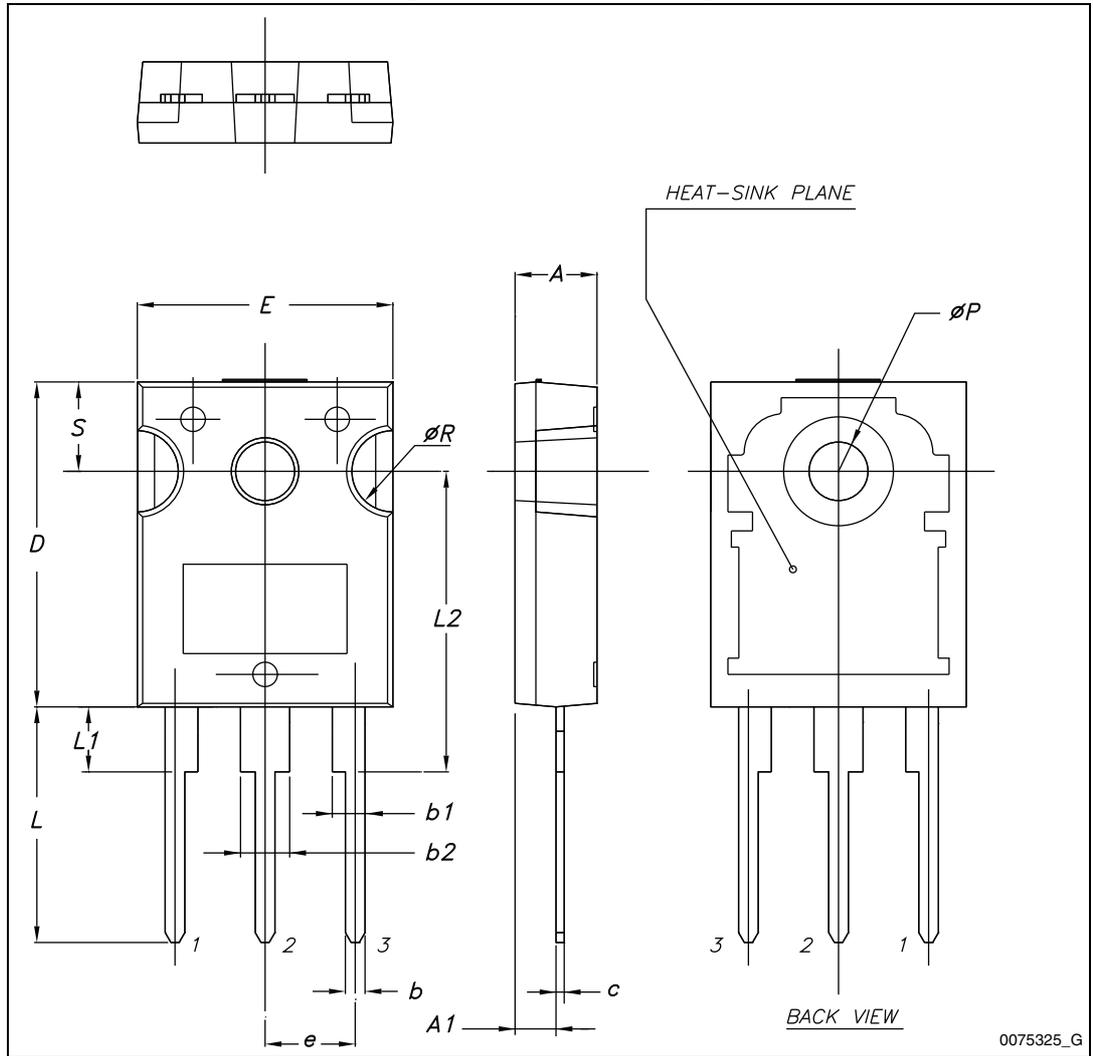


Table 7. TO-3PF mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A	5.30		5.70
C	2.80		3.20
D	3.10		3.50
D1	1.80		2.20
E	0.80		1.10
F	0.65		0.95
F2	1.80		2.20
G	10.30		11.50
G1		5.45	
H	15.30		15.70
L	9.80	10	10.20
L2	22.80		23.20
L3	26.30		26.70
L4	43.20		44.40
L5	4.30		4.70
L6	24.30		24.70
L7	14.60		15
N	1.80		2.20
R	3.80		4.20
Dia	3.40		3.80

4.2 TO-247, STGW30H65FB

Figure 31. TO-247 drawing



0075325_G

Table 8. TO-247 mechanical data

Dim.	mm.		
	Min.	Typ.	Max.
A	4.85		5.15
A1	2.20		2.60
b	1.0		1.40
b1	2.0		2.40
b2	3.0		3.40
c	0.40		0.80
D	19.85		20.15
E	15.45		15.75
e	5.30	5.45	5.60
L	14.20		14.80
L1	3.70		4.30
L2		18.50	
ØP	3.55		3.65
ØR	4.50		5.50
S	5.30	5.50	5.70

4.3 TO-3P, STGWT30H65FB

Figure 32. TO-3P drawing

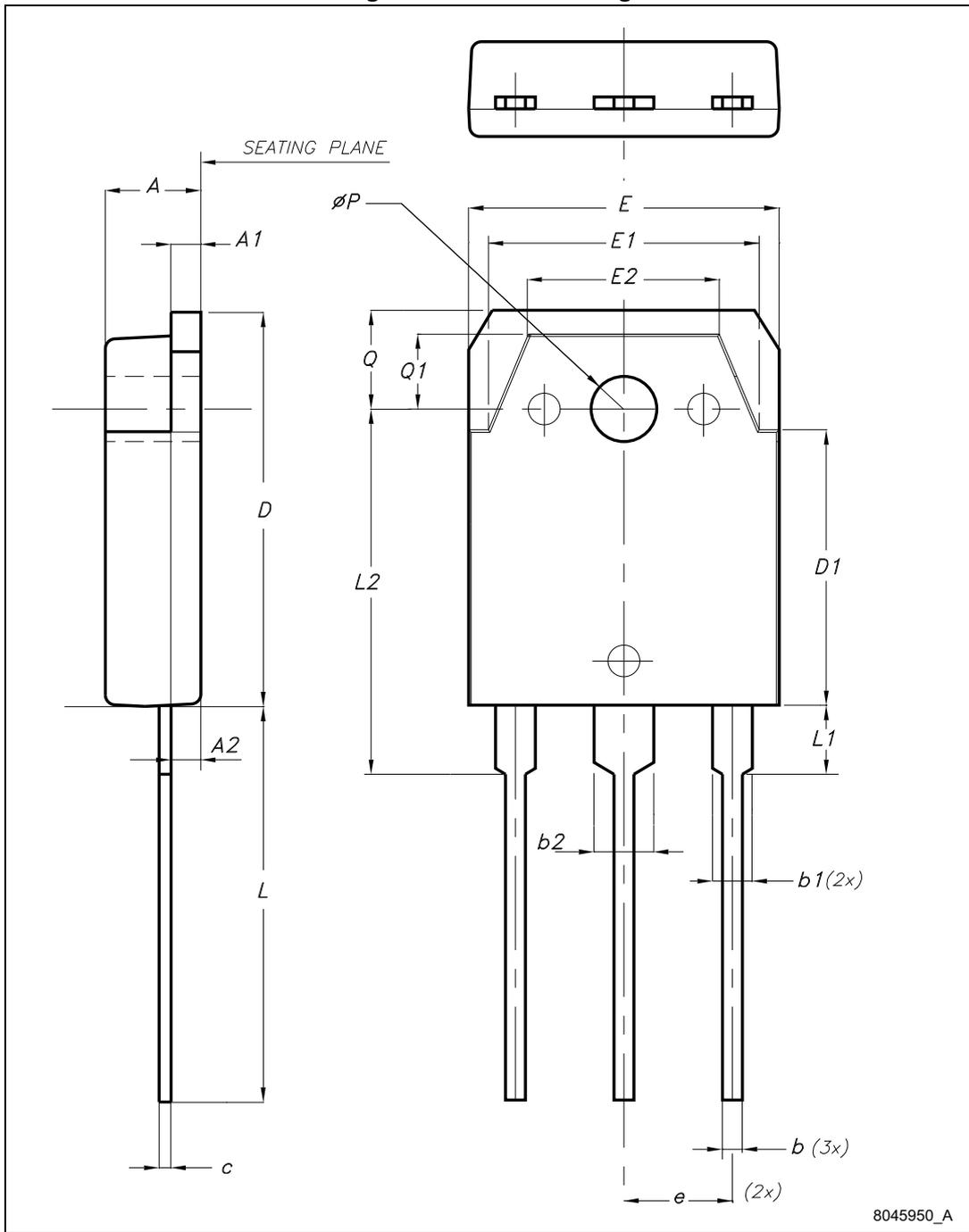


Table 9. TO-3P mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A	4.60		5
A1	1.45	1.50	1.65
A2	1.20	1.40	1.60
b	0.80	1	1.20
b1	1.80		2.20
b2	2.80		3.20
c	0.55	0.60	0.75
D	19.70	19.90	20.10
D1		13.90	
E	15.40		15.80
E1		13.60	
E2		9.60	
e	5.15	5.45	5.75
L	19.50	20	20.50
L1		3.50	
L2	18.20	18.40	18.60
øP	3.10		3.30
Q		5	
Q1		3.80	

5 Revision history

Table 10. Document revision history

Date	Revision	Changes
28-Jan-2014	1	Initial release.
24-Feb-2014	2	Updated units in Table 6: Switching characteristics (inductive load) for E_{ts} , and updated note 1. Update Figure 16: Switching losses vs temperature , Figure 17: Switching losses vs gate resistance and Figure 18: Switching losses vs collector current . Updated title and features in cover page. Minor text changes.
10-Mar-2014	3	Added device in TO-3PF. Updated Table 1: Device summary , Table 2: Absolute maximum ratings , Table 3: Thermal data . Added Figure 6: Collector current vs. case temperature for TO-3PF , Figure 9: Power dissipation vs. case temperature for TO-3PF , Figure 11: Forward bias safe operating area for TO-3PF and Figure 26: Thermal impedance for TO-3PF . Updated Section 4: Package mechanical data .
20-May-2014	4	Updated Table 2: Absolute maximum ratings .

Please Read Carefully:

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

ST PRODUCTS ARE NOT DESIGNED OR AUTHORIZED FOR USE IN: (A) SAFETY CRITICAL APPLICATIONS SUCH AS LIFE SUPPORTING, ACTIVE IMPLANTED DEVICES OR SYSTEMS WITH PRODUCT FUNCTIONAL SAFETY REQUIREMENTS; (B) AERONAUTIC APPLICATIONS; (C) AUTOMOTIVE APPLICATIONS OR ENVIRONMENTS, AND/OR (D) AEROSPACE APPLICATIONS OR ENVIRONMENTS. WHERE ST PRODUCTS ARE NOT DESIGNED FOR SUCH USE, THE PURCHASER SHALL USE PRODUCTS AT PURCHASER'S SOLE RISK, EVEN IF ST HAS BEEN INFORMED IN WRITING OF SUCH USAGE, UNLESS A PRODUCT IS EXPRESSLY DESIGNATED BY ST AS BEING INTENDED FOR "AUTOMOTIVE, AUTOMOTIVE SAFETY OR MEDICAL" INDUSTRY DOMAINS ACCORDING TO ST PRODUCT DESIGN SPECIFICATIONS. PRODUCTS FORMALLY ESCC, QML OR JAN QUALIFIED ARE DEEMED SUITABLE FOR USE IN AEROSPACE BY THE CORRESPONDING GOVERNMENTAL AGENCY.

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2014 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Philippines - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com