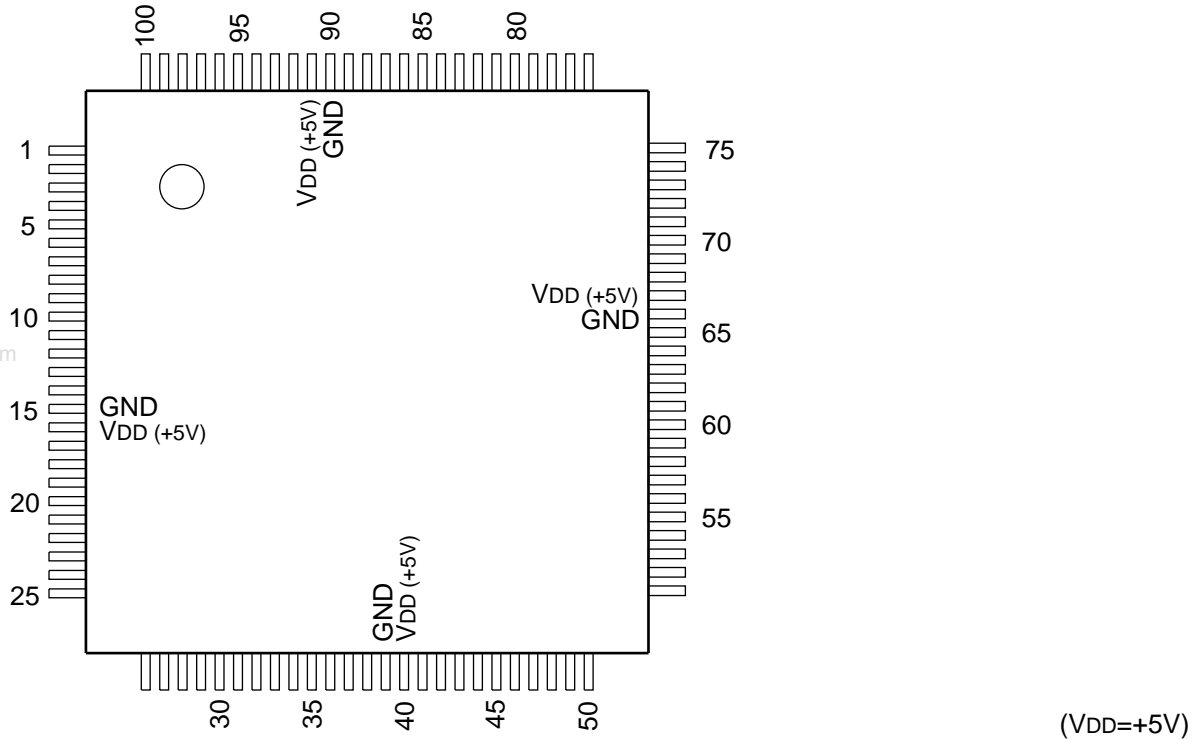


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C-MOS ADDRESS BUS SW  
-TOP VIEW-



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(VDD=+5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	CK1	26	I	V0	51	O	B16	76	I	WBANK0
2	I	CK1D	27	I	VM1	52	I	VD	77	I	WBANK1
3	I	HIN6	28	I	VM2	53	I	PSHD	78	I	WBANK2
4	I	HIN5	29	I	VDLY	54	I	FLOE	79	I	WBANK3
5	I	HIN4	30	I	TEST	55	I	FLDCH	80	O	A16
6	I	HIN3	31	O	BCE	56	I	FLDMASK	81	O	A15
7	I	HIN2	32	O	BWE	57	I	RBANK0	82	O	A14
8	I	HIN1	33	O	B0	58	I	RBANK1	83	O	A13
9	I	HIN0	34	O	B1	59	I	RBANK2	84	O	A12
10	I	MOD3	35	O	B2	60	I	RBANK3	85	O	A11
11	I	MOD2	36	O	B3	61	I	CSTOP	86	O	A10
12	I	MOD1	37	O	B4	62	O	CEN	87	O	A9
13	I	MOD0	38	O	B5	63	I	CCTL	88	O	A8
14	—	NC	39	—	GND	64	I	MUL0	89	O	A7
15	—	GND	40	—	VDD	65	I	MUL1	90	—	GND
16	—	VDD	41	O	B6	66	—	GND	91	—	VDD
17	I	V9	42	O	B7	67	—	VDD	92	O	A6
18	I	V8	43	O	B8	68	I	MUL2	93	O	A5
19	I	V7	44	O	B9	69	I	MUL3	94	O	A4
20	I	V6	45	O	B10	70	I	MUL4	95	O	A3
21	I	V5	46	O	B11	71	I	MUL5	96	O	A2
22	I	V4	47	O	B12	72	I	MUL6	97	O	A1
23	I	V3	48	O	B13	73	I	MUL7	98	O	A0
24	I	V2	49	O	B14	74	I	MUL8	99	O	AWE
25	I	V1	50	O	B15	75	I	MUL9	100	O	ACE

3			80
4	HIN6	A16	81
5	HIN5	A15	82
6	HIN4	A14	83
7	HIN3	A13	84
8	HIN2	A12	85
9	HIN1	A11	86
	HIN0	A10	87
		A9	88
10	MOD3	A8	89
11	MOD2	A7	92
12	MOD1	A6	93
13	MOD0	A5	94
		A4	95
64	MUL0	A3	96
65	MUL1	A2	97
68	MUL2	A1	98
69	MUL3	A0	
70	MUL4		
71	MUL5	B0	33
72	MUL6	B1	34
73	MUL7	B2	35
74	MUL8	B3	36
75	MUL9	B4	37
		B5	38
57	RBANK0	B6	41
58	RBANK1	B7	42
59	RBANK2	B8	43
60	RBANK3	B9	44
		B10	45
17	V9	B11	46
18	V8	B12	47
19	V7	B13	48
20	V6	B14	49
21	V5	B15	50
22	V4	B16	51
23	V3		
24	V2	ACE	100
25	V1	AWE	99
26	V0	BCE	31
		BWE	32
27	VM1	CEN	62
28	VM2		
76	WBANK0		
77	WBANK1		
78	WBANK2		
79	WBANK3		
63	CCTL		
1	CK1		
2	CK1D		
61	CSTOP		
30	TEST		
55	FLDCH		
56	FLDMASK		
54	FLOE		
53	PSHD		
52	VD		
29	VDLY		

