# ES1A thru ES1D

## Vishay General Semiconductor



## **Surface Mount Ultrafast Plastic Rectifier**



### DO-214AC (SMA)

1.0 A

50 V to 200 V

30 A

15 ns

0.92 V 150 °C

**PRIMARY CHARACTERISTICS** 

I<sub>F(AV)</sub>

V<sub>RRM</sub>

I<sub>FSM</sub>

t<sub>rr</sub>

VF

T<sub>J</sub> max.

### FEATURES

- Low profile package
- Ideal for automated placement
- Glass passivated chip junction
- Ultrafast recovery times for high efficiency
- Low forward voltage, low power losses
- High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Solder dip 260 °C, 40 s
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

### **TYPICAL APPLICATIONS**

For use in high frequency rectification and freewheeling application in switching mode converters and inverters for consumer, computer, automotive and telecommunication.

### **MECHANICAL DATA**

Case: DO-214AC (SMA)

Epoxy meets UL 94V-0 flammability rating

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD22-B102

E3 suffix for consumer grade, meets JESD 201 class 1A whisker test, HE3 suffix for high reliability grade (AEC-Q101 qualified), meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	SYMBOL	ES1A	ES1B	ES1C	ES1D	UNIT	
Device marking code		EA	EB	EC	ED		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	50	100	150	200	V	
Maximum RMS voltage	V <sub>RMS</sub>	35	70	105	140	V	
Maximum DC blocking voltage	V <sub>DC</sub>	50	100	150	200	V	
Maximum average forward rectified current (Fig. 1)	I <sub>F(AV)</sub>		А				
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	30				А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	- 55 to + 150				°C	



RoHS

COMPLIANT



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ELECTRICAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIO	SYMBOL	VALUE	UNIT			
Maximum instantaneous forward voltage	I <sub>F</sub> = 0.6 A <sup>(1)</sup> I <sub>F</sub> = 1.0 A		V <sub>F</sub>	0.865 0.920	V		
Maximum DC reverse current at rated DC blocking voltage		T <sub>A</sub> = 25 °C T <sub>A</sub> = 100 °C	I <sub>R</sub>	5.0 100	μΑ		
Maximum reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1.0 A, I <sub>rr</sub> = 0.25 A		t <sub>rr</sub>	15	ns		
Maximum reverse recovery time	$I_F = 0.6 \text{ A}, V_R = 30 \text{ V},$ dI/dt = 50 A/ $\mu$ s, $I_{rr} = 10 \% I_{RM}$	T <sub>J</sub> = 25 °C T <sub>J</sub> = 100 °C	t <sub>rr</sub>	25 35	ns		
Maximum stored charge	$I_F = 0.6 \text{ A}, V_R = 30 \text{ V},$ dI/dt = 50 A/ $\mu$ s, $I_{rr} = 10 \% I_{RM}$	T <sub>J</sub> = 25 °C T <sub>J</sub> = 100 °C	Q <sub>rr</sub>	10 25	nC		
Typical junction capacitance	4.0 V, 1 MHz		CJ	10	pF		

#### Note:

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \degree C$ unless otherwise noted)							
PARAMETER	SYMBOL	ES1A	ES1B	ES1C	ES1D	UNIT	
Typical thermal resistance <sup>(1)</sup>	$R_{ extsf{ heta}JA}\ R_{ hetaJL}$	85 35			°C/W		

#### Note:

(1) Units mounted on P.C.B. 5.0 x 5.0 mm (0.013 mm thick) land areas

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
ES1D-E3/61T	0.064	61T	1800	7" diameter plastic tape and reel		
ES1D-E3/5AT	0.064	5AT	7500	13" diameter plastic tape and reel		
ES1DHE3/61T <sup>(1)</sup>	0.064	61T	1800	7" diameter plastic tape and reel		
ES1DHE3/5AT <sup>(1)</sup>	0.064	5AT	7500	13" diameter plastic tape and reel		

#### Note:

(1) Automotive grade AEC-Q101 qualified

### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

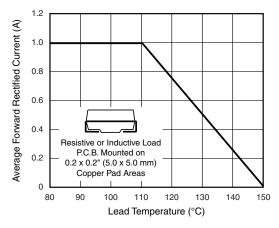


Figure 1. Maximum Forward Current Derating Curve

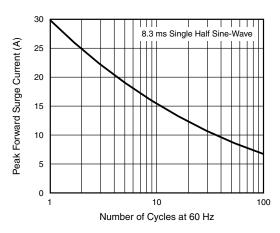


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current

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T<sub>J</sub> = 25 °C

f = 1.0 MHz

 $V_{sig} = 50 \text{ mVp-p}$ 

100

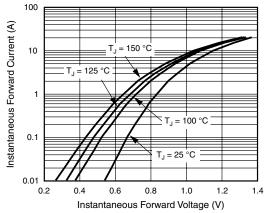


Figure 3. Typical Instantaneous Forward Characteristics

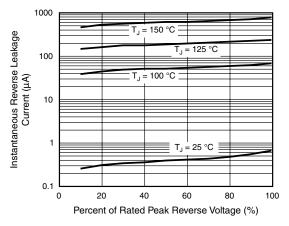
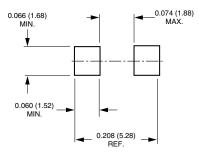


Figure 4. Typical Reverse Leakage Characteristics



DO-214AC (SMA) Cathode Band 0.065 (1.65) 0.049 (1.25) 0.110 (2.79) 0.100 (2.54) 0.177 (4.50) 0.157 (3.99) 0.012 (0.305)
0.006 (0.152) • 0.090 (2.29) 0.078 (1.98) 0.060 (1.52) 0.008 (0.203) 0 (0) 0.208 (5.28) 0.194 (4.93)

**Mounting Pad Layout** 



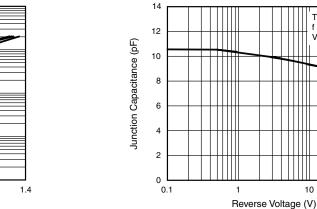


Figure 5. Typical Junction Capacitance

10

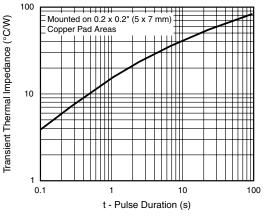


Figure 6. Typical Thermal Impedance

For technical questions within your region, please contact one of the following: PDD-Americas@vishay.com, PDD-Asia@vishay.com, PDD-Europe@vishay.com



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