

# R1200F THRU R2000F



CO., LTD

## HIGH VOLTAGE FAST RECOVERY RECTIFIER

**REVERSE VOLTAGE:** 1200 to 2000 VOLTS

**FORWARD CURRENT:** 0.2 to 0.5 AMPERE

### FEATURES

- Fast switching
- Low leakage
- Low forward voltage drop
- High current capability
- High current surge
- High reliability

### MECHANICAL DATA

Case: Molded plastic, DO-41

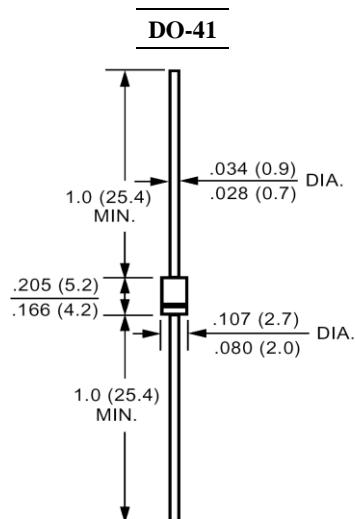
Epoxy: UL 94V-O rate flame retardant

Terminals: Axial leads, solderable per MIL-STD-202, method 208 guaranteed

Polarity: Band denotes cathode

Mounting position: Any

Weight: 0.013ounce, 0.3gram



Dimensions in inches and (millimeters)

### Maximum Ratings and Electrical Characteristics

Ratings at 25° ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

	Symbols	R1200F	R1500F	R1800F	R2000F	Units
Maximum Recurrent Peak Reverse Voltage	V <sub>RRM</sub>	1200	1500	1800	2000	Volts
Maximum RMS Voltage	V <sub>RMS</sub>	840	1050	1260	1400	Volts
Maximum DC Blocking Voltage	V <sub>DC</sub>	1200	1500	1800	2000	Volts
Maximum Average Forward Rectified Current .375"(9.5mm) Lead Length at T <sub>A</sub> =50	I <sub>(AV)</sub>		0.5		0.2	Amp
Peak Forward Surge Current, 8.3ms single half-sine-wave superimposed on rated load (JEDEC method)	I <sub>FSM</sub>			30		Amp
Maximum Forward Voltage at 0.5/0.2A	V <sub>F</sub>		2.5		4	Volts
Maximum Reverse Current at Rated DC Blocking Voltage T <sub>A</sub> =25	I <sub>R</sub>			5.0		uAmp
Maximum Full Load Reverse Current Average, Full Cycle .375", (9.5mm) lead length at T <sub>L</sub> = 55				100		uAmp
Maximum Reverse Recovery Time (Note 1)	T <sub>RR</sub>			300		nS
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>Stg</sub>			-55 to +150		

### NOTES:

1- Reverse Recovery Test Conditions : I<sub>F</sub>=.5A , I<sub>R</sub>=1A , I<sub>RR</sub>=.25A.



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### RATINGS AND CHARACTERISTIC CURVES

FIG. 1 - TYPICAL FORWARD CURRENT DERATING CURVE

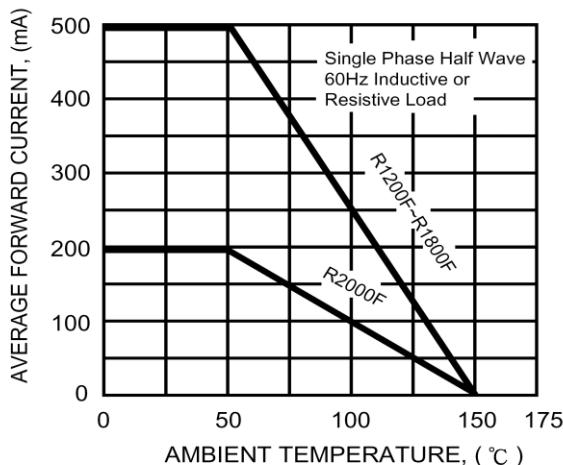


FIG. 2 - MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

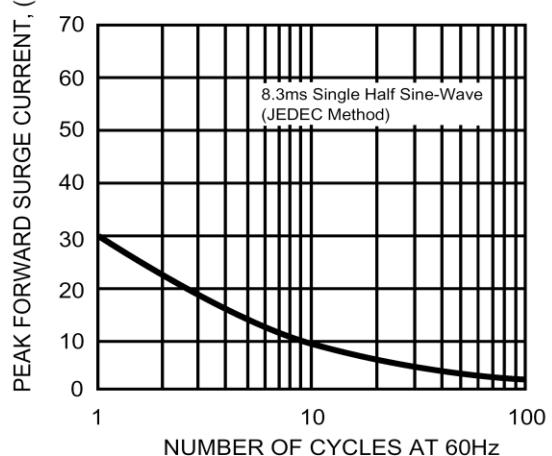
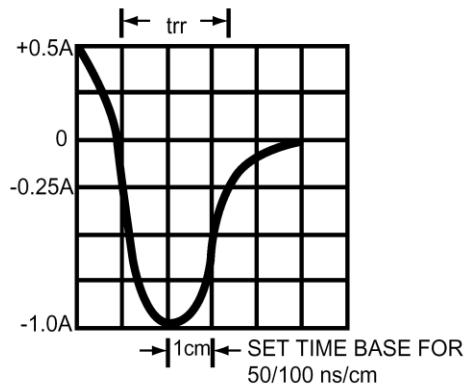
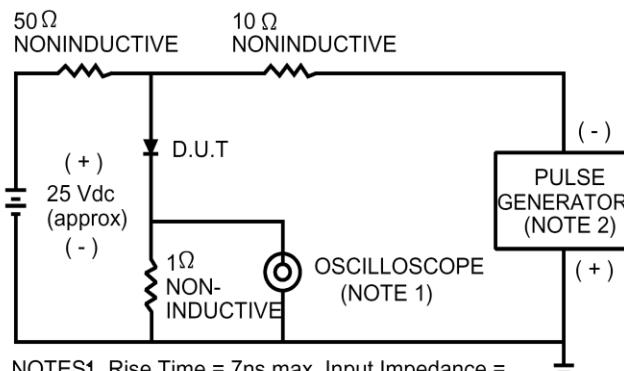


FIG. 3 - TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC



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