TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ86FE

2-Input EXCLUSIVE OR Gate

Features

High output current : ±24mA (min) at V_{CC} = 3V

• Super high speed operation : $t_{pd} = 2.9$ ns (typ.)

at $V_{CC} = 5V$, 50pF

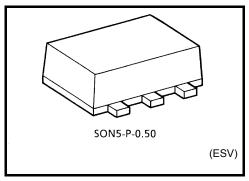
Operating voltage range : V_{CC} = 1.65 to 5.5V

• 5.5-V tolerant inputs

• 5.5-V power down protection output

Matches the performance of TC74LCX series when operated at

3.3-V V_{CC}

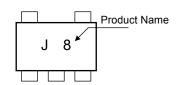


Weight: 0.003 g (typ.)

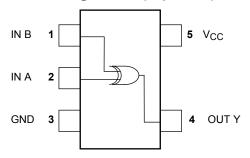
Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	−0.5 to 6	V	
DC Input voltage	V _{IN}	−0.5 to 6	V	
DC output voltage	Vout	-0.5 to 6 (Note1)	V	
	V _{OUT}	-0.5 to V _{CC} +0.5 (Note 2)		
Input diode current	I _{IK}	-20	mA	
Output diode current	lok	-20 (Note3)	mA	
DC output current	lout	±50	mA	
DC VCC/ground current	Icc	±50	mA	
Power dissipation	PD	150	mW	
Storage temperature	T _{stg}	-65 to 150	°C	

Marking



Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

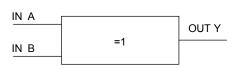
Note 1: $V_{CC} = 0V$

Note 2: High or Low state. Do not exceed I_{OUT} of absolute maximum ratings.

Note 3: V_{OUT} < GND

IEC Logic Symbol

Truth Table



Α	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	1.65 to 5.5	V	
		1.5 to 5.5 (Note 4)	V	
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	V _{OUT}	0 to 5.5 (Note 5)	V	
		0 to V _{CC} (Note 6)		
Operating temperature	T _{opr}	-40 to 85	°C	
Input rise and fall time		0 to 20 (V _{CC} = 1.80 V \pm 0.15V, 2.5 V \pm 0.2 V)		
	dt/dv	0 to 10 (V _{CC} = 3.3 V \pm 0.3 V)	ns/V	
		0 to 5 (V _{CC} = 5.0 V \pm 0.5 V)		

Note 4: Data retention only

Note 5: $V_{CC} = 0 V$

Note 6: High or Low State

Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit		
			V _{CC} (V)	Min	Тур.	Max	Min	Max		
High-level input voltage VIH —			1.65 to 1.95	V _{CC} × 0.75		_	V _{CC} × 0.75		. V	
			2.3 to 5.5	V _{CC} × 0.7		l	V _{CC} × 0.7			
Low-level input	put V _I ,		1.65 to 1.95			V _{CC} × 0.25		V _{CC} × 0.25		
voltage			_				$\begin{array}{c} V_{CC} \\ \times 0.3 \end{array}$			$\begin{array}{c} V_{CC} \\ \times \ 0.3 \end{array}$
				1.65	1.55	1.65		1.55		
			I _{OH} = -100 μA	2.3	2.2	2.3		2.2		
			ΙΟΗ = -100 μΑ	3.0	2.9	3.0		2.9		
				4.5	4.4	4.5	_	4.4	_	
High-level output voltage	V_{OH}	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -4 \text{ mA}$	1.65	1.29	1.52		1.29		
			$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15		1.9		
			$I_{OH} = -16 \text{ mA}$	3.0	2.4	2.8	_	2.4	_	V
			I _{OH} = -24 mA	3.0	2.3	2.68	_	2.3	_	
			$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8	_	
			I _{OL} = 100 μA	1.65		0	0.1		0.1	
		V _{IN} = V _{IH} or V _{IL}		2.3		0	0.1		0.1	
				3.0	_	0	0.1	_	0.1	
				4.5	_	0	0.1	_	0.1	
Low-level output voltage VoL	V_{OL}		I _{OL} = 4 mA	1.65	_	0.08	0.24	_	0.24	
			$I_{OL} = 8 \text{ mA}$	2.3	_	0.1	0.3	_	0.3	
			$I_{OL} = 16 \text{ mA}$	3.0	_	0.15	0.4	_	0.4	
			I _{OL} = 24 mA	3.0	_	0.22	0.55		0.55	
			I _{OL} = 32 mA	4.5		0.22	0.55		0.55	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_	_	±1		±10	μΑ
Power OFF leakage current	l _{OFF}	V _{IN} or V _{OUT} = 5.5 V		0.0	_		1	-	10	μА
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		1.65 to 5.5	_	_	2	_	20	μА

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AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
			V _{CC} (V)	Min	Тур.	Max	Min	Max	5
Propagation delay time	^t pLH ^t pHL	C_L = 15 pF, R_L = 1 $M\Omega$	1.8 ± 0.15	2.0	4.9	11.5	2.0	12.0	- ns
			2.5 ± 0.2	0.8	3.8	8.0	0.8	8.5	
			3.3 ± 0.3	0.5	3.0	5.7	0.5	6.0	
			5.0 ± 0.5	0.5	2.4	5.0	0.5	5.4	
		$C_L = 50 \text{ pF},$ $R_L = 500 \Omega$	3.3 ± 0.3	1.5	3.5	6.2	1.5	6.5	
			5.0 ± 0.5	0.8	2.9	5.4	0.8	5.8	
Input capacitance	C _{IN}	_	0 to 5.5	_	4	_	_	_	pF
Power dissipation capacitance	C _{PD}	(Note 7)	3.3	_	17	_	_	_	- pF
			5.5	_	21		_	_	

Note 7: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

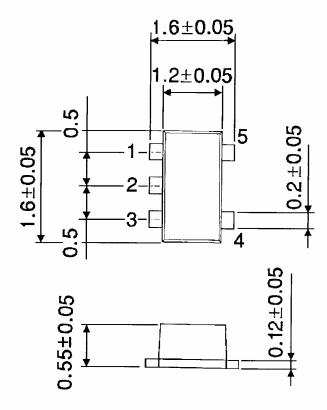
Average operating current can be obtained by the equation.

$$I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$



Package Dimensions

SON5-P-0.50 Unit: mm



Weight: 0.003 g (typ.)

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