

Super Low Distortion Multilayer Ceramic Capacitors (CFCAP(TM))

UMK212SD562KD-T



■ Features

- Item Summary
50V, 0.0056 μ F, \pm 10%, 0805
- Lifecycle Stage
Mass Production
- Standard packaging quantity (minimum)
Taping paper 4000pcs

■ Products characteristics table

CaseSize (EIA/JIS)	0805/2012
Rated Voltage (max)	50V
Capacitance	0.0056 μ F
Capacitance Tolerance	\pm 10%
Resistance	10,000 M Ω min.
Dielectric Loss Tangent	0.1%MAX
Temperature Range (EIA/JIS)	-55 to +125 $^{\circ}$ C/-55 to +125 $^{\circ}$ C
RoHS Compliance	Yes
Halogen Free	Yes
Soldering Method	Reflow

■ External Dimensions

L	2.0mm \pm 0.10
W	1.25mm \pm 0.10
T	0.85mm \pm 0.10
e	0.5mm \pm 0.25

2015.01.05

The data is reference only. Electrical characteristics vary depending on environment or measurement condition.
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 Before making final selection, please check product specification.

Multi Layer Ceramic Capacitor

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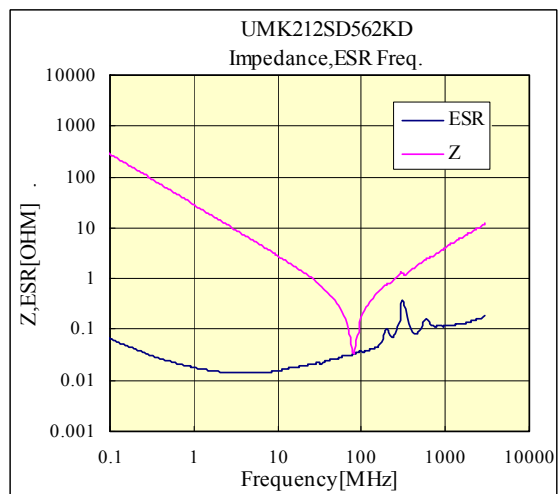
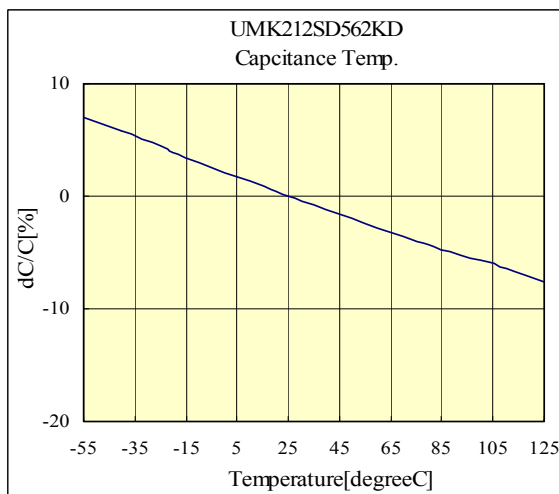
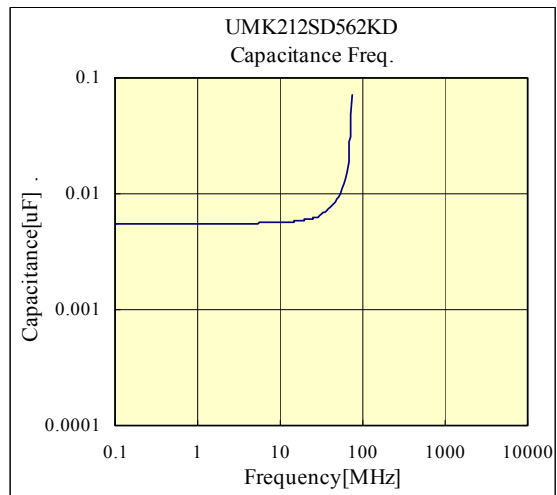
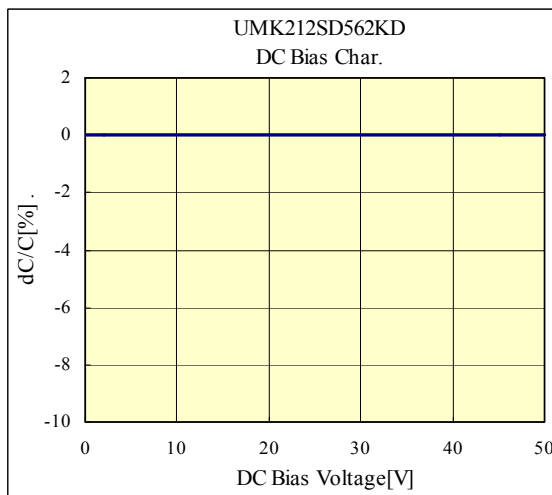
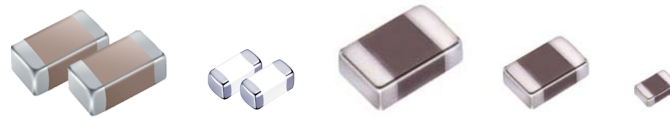
-Electrical Characteristics Data- Jun-2011

RoHS compliance product

○UMK212SD562KD

50V 2012 0.0056uF +/-10% t=0.95mmMAX

50V 0805 0.0056uF +/-10% t=0.037inchMAX



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