TOSHIBA MULTI CHIP DISCRETE DEVICE

HN2E04F

Super High Speed Switching Application Audio Frequency Amplifier Application Audio Low Noise Amplifier Application **Q1**

High Voltage : $V_{CEO} = 120V$ High DC Current Gain : $h_{FE} = 200$ to 700

Good here Linearity : $h_{FE}(I_C = 0.1 \text{mA})/h_{FE}(I_C = 2 \text{mA}) = 0.95$

Q2

Low Forward Voltage Drop : $V_{F(3)} = 0.98V$ (typ.) Fast Reverse Recovery Time : $t_{rr} = 1.6$ ns (typ.) Low Total Capacitance : $C_T = 0.5$ pF (typ.)

Q1 (Transistor) : 2SA1587 equivalent Q2 (Diode) : 1SS352 equivalent

Q1 (Transistor) Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	-120	V
Collector-emitter voltage	V _{CEO}	-120	V
Emitter-base voltage	V _{EBO}	-5	V
Collector current	IC	-100	mA
Base current	ΙΒ	-20	mA

Q2 (Diode) Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Maximum (peak) reverse voltage	V_{RM}	85	V
Reverse voltage	V _R	80	V
Maximum (peak) forward current	I _{FM}	300	mA
Average forward current	Io	100	mA
Surge current (10ms)	I _{FSM}	1	Α

Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 Common)

Characteristic	Symbol	Rating	Unit
Collector power dissipation	P _C *	300	mW
Junction temperature	Tj	125	°C
Storage temperature range	T _{stg}	-55 to 125	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Weight: 0.015g (typ.)

Start of commercial production 2000-02

^{*}Total rating: Power dissipation per element should not exceed 200mW per element.

Q1 (Transistor) Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	_	$V_{CB} = -120V, I_{E} = 0$	_	_	-100	nA
Emitter cut-off current	I _{EBO}	_	$V_{EB} = -5V, I_{C} = 0$	_	_	-100	nA
DC current gain	h _{FE} *	_	$V_{CE} = -6V, I_{C} = -2mA$	200	_	700	
Collector-emitter saturation voltage	V _{CE(sat)}	_	I _C =-10mA, I _B =-1mA	_	_	-0.3	V
Transition Frequency	f _T	_	$V_{CE} = -6V, I_{C} = -1mA$	_	100	_	MHz
Collector Output Capacitance	C _{ob}	_	V _{CB} =–10V, I _E = 0, f=1MHz	_	4	_	pF
Noise figure	NF	_	$V_{CE} = -6 \text{ V}, I_{C} = -0.1 \text{ mA}$ $f = 1 \text{ kHz}, R_g = 10 \text{ k}\Omega$	_	1.0	_	dB

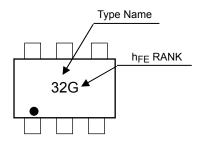
^{*:} h_{FE} Classifications GR(G): 200 to 400 , BL(L): 350 to 700

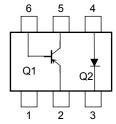
Q2 (Diode) Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V _{F (1)}	_	I _F = 1mA	_	0.62		٧
	V _{F (2)}	_	I _F = 10mA	_	0.75	1	
	V _{F (3)}	_	I _F = 100mA	_	0.98	1.20	
Reverse current	I _{R (1)}	_	V _R = 30V	-		0.1	μА
	I _{R (2)}	_	V _R = 80V	_	-	0.5	
Total capacitance	C _T	_	V _R = 0, f = 1MHz	_	0.5	-	pF
Reverse recovery time	t _{rr}	_	I _F = 10mA (fig.1)	_	1.6	-	ns

Marking

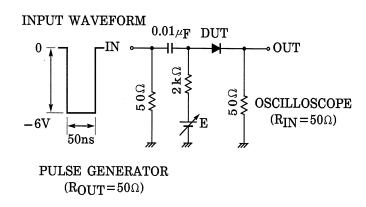
Equivalent Circuit (Top View)

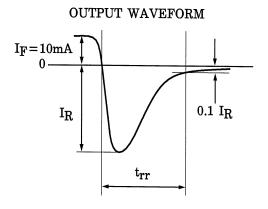




^()Marking Symbol

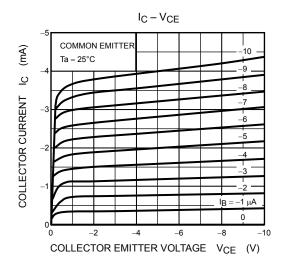
Fig. 1: Reverse Recovery Time (t_{rr}) Test Circuit

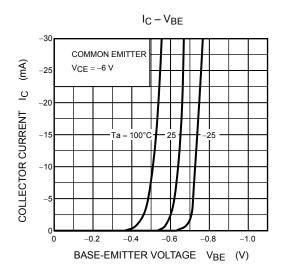


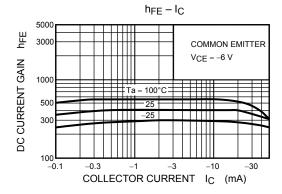


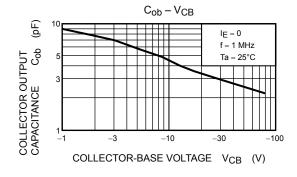
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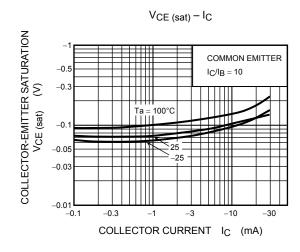
Q1

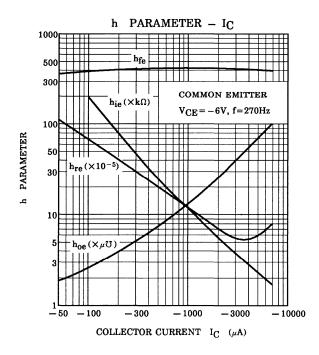


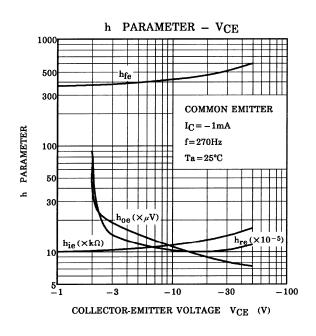


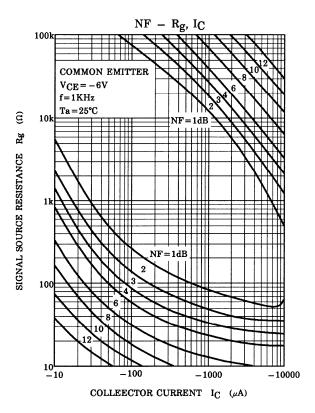


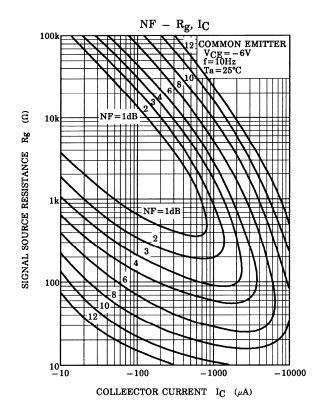




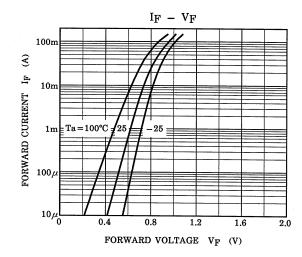


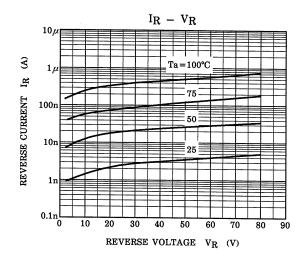


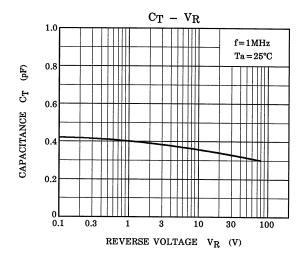




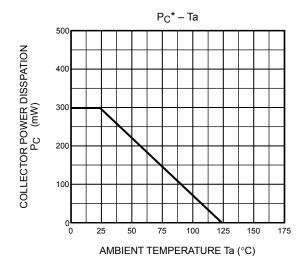
Q2







Q1, Q2 Common



*Total Rating.

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8