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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# 2SK3446

# Silicon N Channel Power MOS FET Power Switching

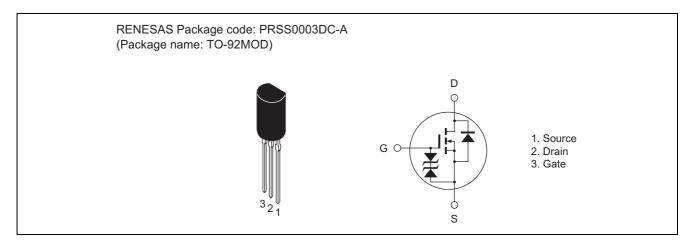
REJ03G1100-0800 (Previous: ADE-208-1566F)

> Rev.8.00 Sep 07, 2005

#### **Features**

- Capable of 2.5 V gate drive
- Low drive current
- Low on-resistance  $R_{DS \, (on)} = 1.5 \; \Omega \; typ. \; (at \; V_{GS} = 4 \; V)$

#### **Outline**



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	150	V
Gate to source voltage	V <sub>GSS</sub>	±10	V
Drain current	I <sub>D</sub>	1	A
Drain peak current	I <sub>D (pulse)</sub> Note 1	4	A
Body-drain diode reverse drain current	I <sub>DR</sub>	1	Α
Channel dissipation	Pch Note 2	0.9	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at Ta = 25°C

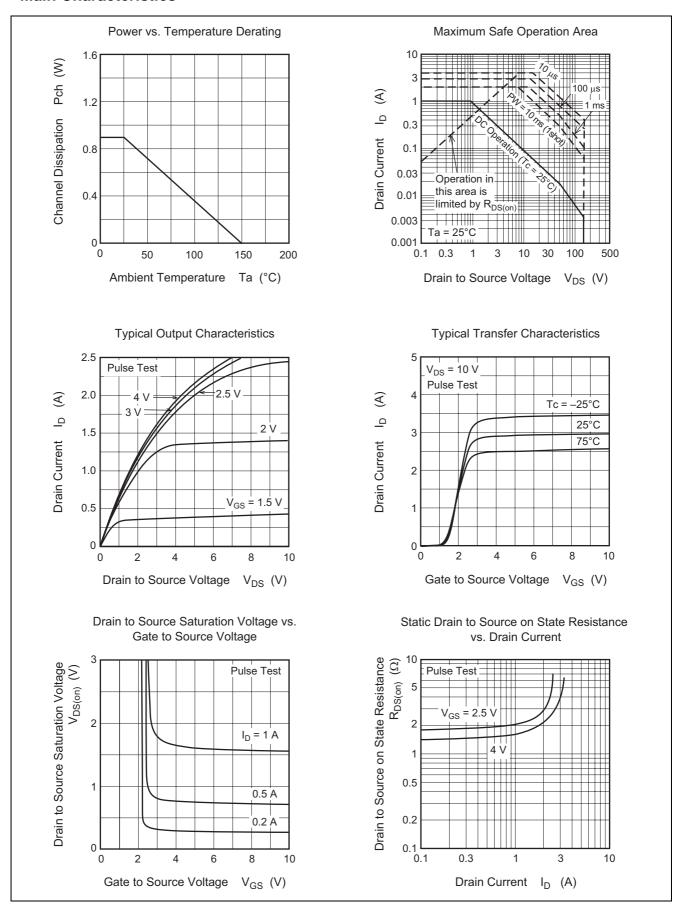
## **Electrical Characteristics**

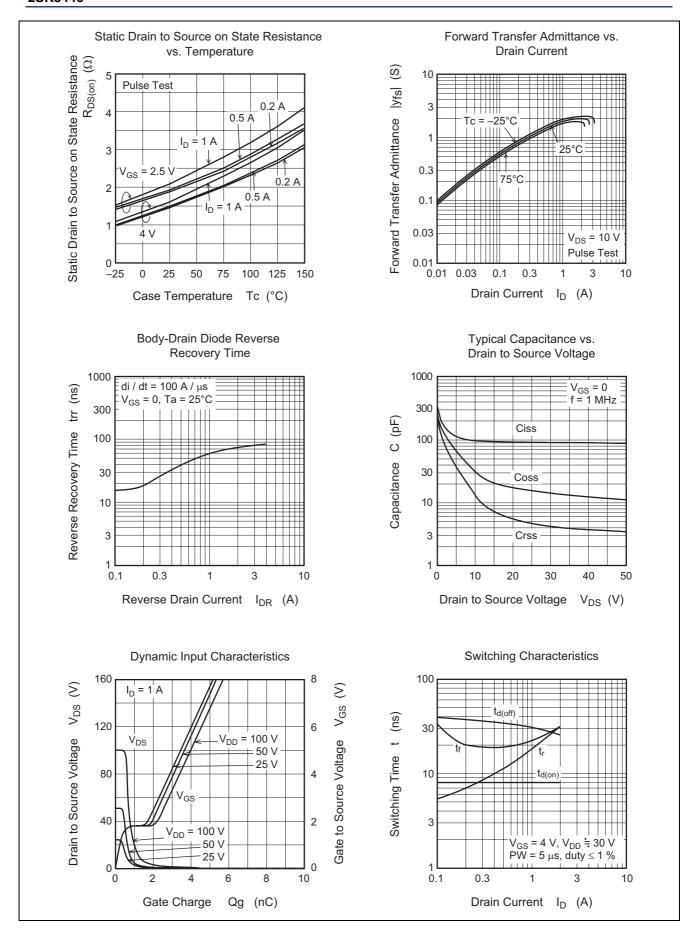
 $(Ta = 25^{\circ}C)$ 

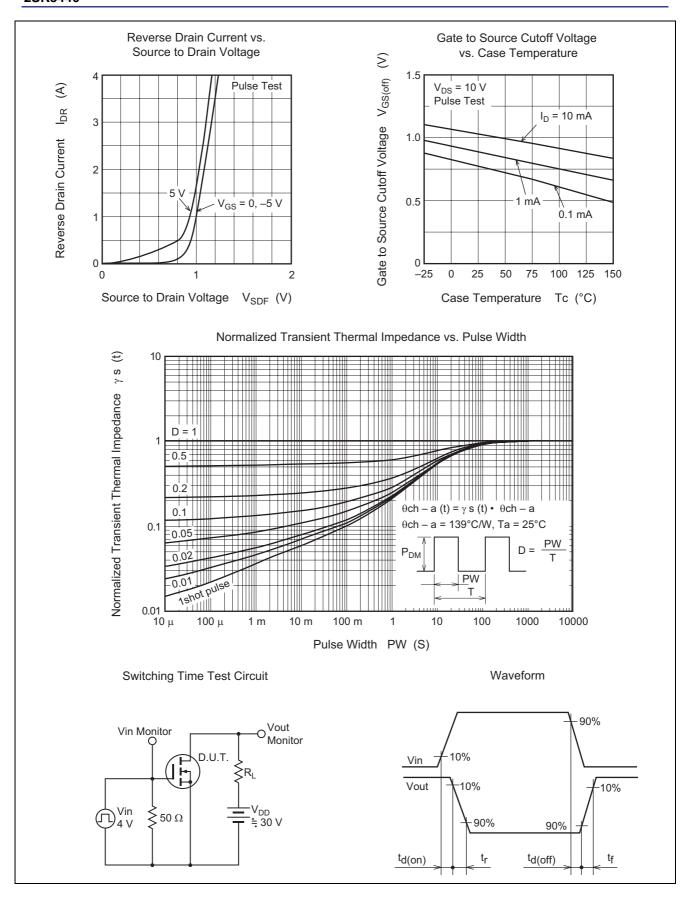
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR) DSS</sub>	150	_		V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V <sub>(BR) GSS</sub>	±10	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 8 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 150 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	0.5	_	1.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	_	1.5	1.95	Ω	$I_D = 0.5 \text{ A}, V_{GS} = 4 \text{ V}^{\text{Note 3}}$
	R <sub>DS (on)</sub>	_	1.9	2.5	Ω	$I_D = 0.5 \text{ A}, V_{GS} = 2.5 \text{ V}^{\text{Note 3}}$
Forward transfer admittance	y <sub>fs</sub>	0.8	1.4	_	S	$I_D = 0.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note 3}}$
Input capacitance	Ciss	_	98	_	рF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	_	31	_	рF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	14	_	рF	f = 1 MHz
Total gate charge	Qg	_	3.5	_	nC	V <sub>DD</sub> = 100 V
Gate to source charge	Qgs	_	0.5	_	nC	$V_{GS} = 4 V$
Gate to drain charge	Qgd	_	1.8	_	nC	I <sub>D</sub> = 1 A
Turn-on delay time	t <sub>d (on)</sub>	_	8	_	ns	V <sub>GS</sub> = 4 V
Rise time	t <sub>r</sub>	_	12	_	ns	I <sub>D</sub> = 0.5 A
Turn-off delay time	t <sub>d (off)</sub>	_	34	_	ns	$R_L = 60 \Omega$
Fall time	t <sub>f</sub>	_	19	_	ns	
Body-drain diode forward voltage	$V_{DF}$	_	1.0	1.5	V	I <sub>F</sub> = 1 A, V <sub>GS</sub> = 0
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	60	_	ns	I <sub>F</sub> = 1 A, V <sub>GS</sub> = 0
						di <sub>F</sub> /dt = 100 A/μs

Note: 3. Pulse test

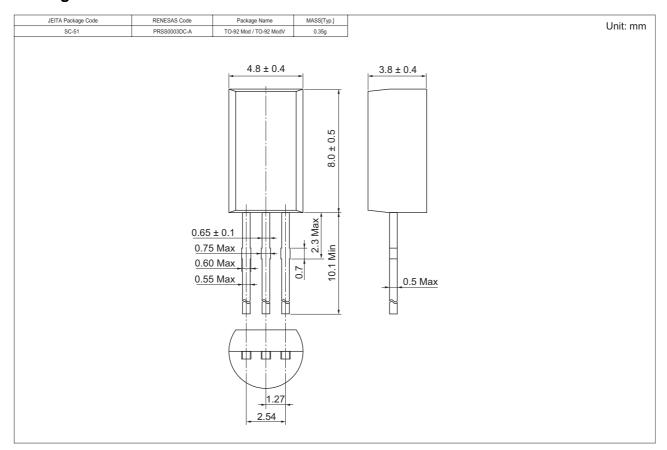
### **Main Characteristics**







## **Package Dimensions**



## **Ordering Information**

Part Name	Quantity	Shipping Container
2SK3446TZ-E	2500 pcs	Hold box, Radial taping

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