TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SZ07FE

#### NON-Inverter (Open Drain)

#### **Features**

High output current : ±24mA (min) at V<sub>CC</sub> = 3V

• Super high speed operation : t<sub>pZL</sub> 2.3 ns (typ.)

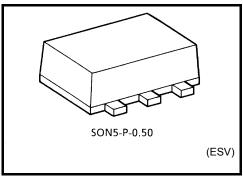
at  $V_{CC} = 5 \text{ V}$ , 50 pF

Operation voltage range : V<sub>CC (opr.)</sub> = 1.65 to 5.5V

5.5-V tolerant input

• 5.5-V power down protection output

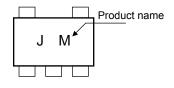
- Matches the performance of TC74LCX series when operated at 3.3-V  $\mbox{V}_{\mbox{CC}}$ 

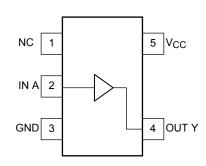


Weight: 0.003 g (typ.)

### Marking

### Pin Assignment (top view)





### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	–0.5 to 6	V
DC input voltage	V <sub>IN</sub>	–0.5 to 6	V
DC output voltage	Vout	-0.5 to 6 (Note 1)	V
Input diode current	lıĸ	-20	mA
Output diode current	lok	-20 (Note 2)	mA
DC output current	lout	50	mA
DC V <sub>CC</sub> /ground current	Icc	±50	mA
Power dissipation	PD	150	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

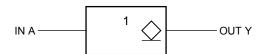
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Do not exceed I<sub>OUT</sub> of absolute maximum ratings.

Note 2:  $V_{OUT} \le GND$ 

Start of commercial production 2008-12

# **IEC Logic Symbol**



## **Truth Table**

Α	Υ
L	L
Н	Z

Z: High impedance

# **Operating Ranges**

Characteristics	Symbol	Rating	Unit
Supply voltage	Vac	1.65 to 5.5	V
Supply voltage	V <sub>CC</sub>	1.5 to 5.5 (Note 3)	V
Input voltage	V <sub>IN</sub>	0 to 5.5	V
Output voltage	V <sub>OUT</sub>	0 to 5.5	V
Operating temperature	T <sub>opr</sub>	-40 to 85	°C
Input rise and fall time		0 to 20 (V <sub>CC</sub> = 1.8 V $\pm$ 0.15 V, 2.5 V $\pm$ 0.2 V)	
	dt/dv	0 to 10 (V <sub>CC</sub> = 3.3 V $\pm$ 0.3 V)	ns/V
	-	0 to 5 (V <sub>CC</sub> = 5.0 V $\pm$ 0.5 V)	

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Note 3: Data retention only

## **Electrical Characteristics**

### **DC Characteristics**

Characteristics Symbol		Symbol Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit	
		rest Condition		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic	
Input voltage  Low level	High level	V <sub>IH</sub>			1.65 to 1.95	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75	_	
	VIH	_		2.3 to 5.5	V <sub>CC</sub> × 0.7		_	V <sub>CC</sub> × 0.7	_	V	
	.,	_		1.65 to 1.95			V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25		
	V <sub>IL</sub>			2.3 to 5.5			V <sub>CC</sub> × 0.3	_	V <sub>CC</sub> × 0.3		
Z-state output lea	akage	lLKG	V <sub>IN</sub> = V <sub>IH</sub> V <sub>OUT</sub> = 0 to 5.5 V		1.65 to 5.5	_	_	±5	_	±10	μΑ
		_ow level V <sub>OL</sub>	V <sub>IN</sub> = V <sub>II</sub>	I <sub>OL</sub> = 100 μA	1.65	_	0	0.1	_	0.1	- - - - V
					2.3	_	0	0.1	_	0.1	
Output voltage Low level					3.0		0	0.1	_	0.1	
	Low level				4.5		0	0.1	_	0.1	
	VOL	VIN - VIL	$I_{OL} = 8 \text{ mA}$	2.3		0.1	0.3	_	0.3	V	
				I <sub>OL</sub> = 16 mA	3.0	_	0.15	0.4	_	0.4	
				I <sub>OL</sub> = 24 mA	3.0	_	0.22	0.55	_	0.55	
				$I_{OL} = 32 \text{ mA}$	4.5		0.22	0.55	_	0.55	
Input leakage current $I_{IN}$ $V_{IN} = 5.5 \text{ V or GND}$		0 to 5.5		_	±1	_	±10	μА			
Power off leakag	Power off leakage current I <sub>OFF</sub> V <sub>IN</sub> or V <sub>OUT</sub> = 5.5 V		0.0			1	_	10	μА		
Quiescent supply current $I_{CC}$ $V_{IN} = 5.5 \text{ V or GND}$		5.5	_	_	2	_	20	μА			

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## AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
Characteristics Syl		rest Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	t <sub>p</sub> zL	$C_L$ = 50 pF, $R_L$ = 500 $\Omega$	$1.8\pm0.15$	1.8	5.5	9.5	1.8	10.5	- ns
			$2.5\pm0.2$	1.2	3.7	5.8	1.2	6.4	
			$3.3 \pm 0.3$	0.8	2.9	4.4	0.8	4.8	
			$5.0 \pm 0.5$	0.5	2.3	3.5	0.5	3.9	
	<sup>t</sup> pLZ	$C_L$ = 50 pF, $R_L$ = 500 $\Omega$	$1.8\pm0.15$	1.8	4.3	9.5	1.8	10.5	
			$2.5 \pm 0.2$	1.2	2.8	5.8	1.2	6.4	
			$3.3 \pm 0.3$	0.8	2.1	4.4	0.8	4.8	
			$5.0 \pm 0.5$	0.5	1.4	3.5	0.5	3.9	
Input capacitance	C <sub>IN</sub>		0 to 5.5		4		_	_	pF
Output capacitance	C <sub>OUT</sub>		0 to 5.5		8		_	_	pF
Power dissipation capacitance	C	(Note 4)	3.3		20		_	_	pF
	C <sub>PD</sub>		5.5	_	26	_	_	_	ρı

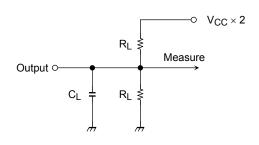
Note4: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

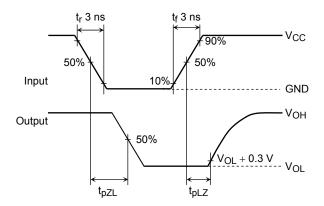
Average operating current can be obtained by the equation.

$$I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

#### **AC Characteristics Measurement Circuit**

#### **AC Waveforms**

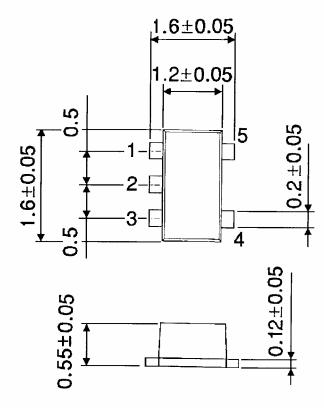






# **Package Dimensions**

SON5-P-0.50 Unit: mm



Weight: 0.003 g (typ.)

2014-03-01

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