2SC3211, 2SC3211A

Silicon NPN Triple-Diffused Junction Mesa Type

High Breakdown Voltage, High Speed Switching

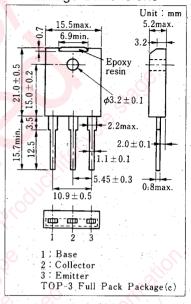
■ Features

- High speed switching
- High collector-base voltage (V_{CBO})
- Low collector-emitter saturation voltage (V_{CE (sat)})
- "Full Pack" package for simplified mounting on a heat sink with one screw

■ Absolute Maximum Ratings (Tc=25°C)

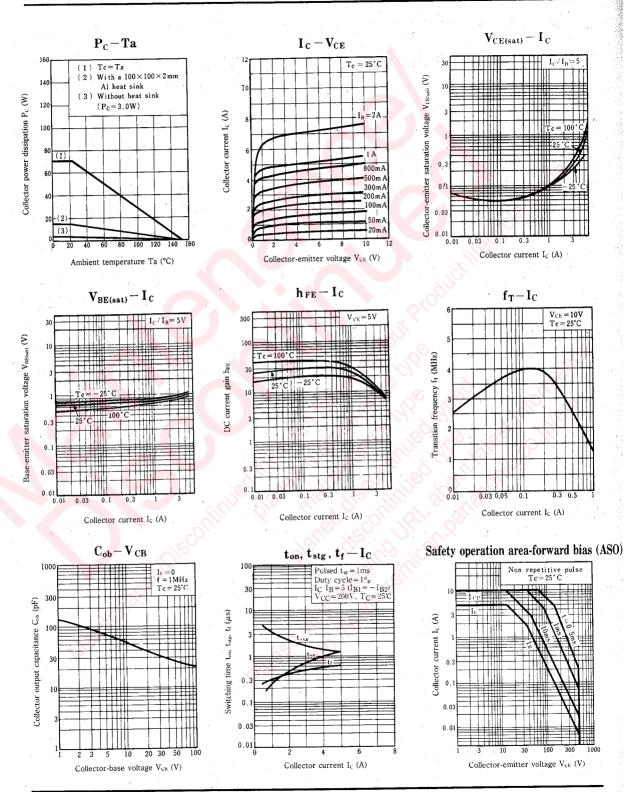
Symbol	Value	Unit	
V	800		
У СВО	900	V	
V _{CEO}	500	V	
V_{EBO}	8	V	
ICP	10	A	
I _C	5	A	
I_B	3	A	
D	70		
Pe	3	W	
T_i	150	O CO	
Tstg	$-55 \sim +150$	CC C	
	VCBO VCEO VEBO ICP IC IB PC	VCBO 800 900 900 VCEO 500 VEBO 8 ICP 10 IC 5 IB 3 PC 70 3 7 T _i 150	

■ Package Dimensions

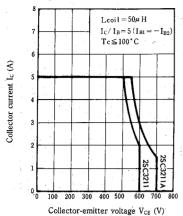


■ Electrical Characteristics (Tc=25°C)

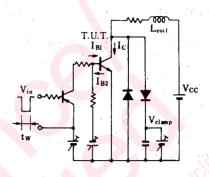
Symbol	Condition	min.	typ.	max.	Unit
Collector cutoff 2S3211 Long	$V_{CB} = 800 \text{ V}, I_E = 0$	5		100	μА
1CBO	$V_{CB} = 900 \text{ V}, I_E = 0$			100	
I _{EBO}	$V_{EB}=5 \text{ V}, I_C=0$		1.0	100	μΑ
V _{CEO(sus)}	$I_C = 0.2 \text{ A}, L = 25 \text{ mH}$	500		2.	· V
current gain h_{FE1} $V_{CE}=5 \text{ V}, I_{C}=0.1 \text{ A}$	$V_{CE} = 5 \text{ V}, I_{C} = 0.1 \text{ A}$	15			
h _{FE2}	$V_{CE} = 5 \text{ V}, I_{C} = 3 \text{ A}$	8			
V _{CE(sat)}	$I_C = 3 \text{ A}, I_B = 0.6 \text{ A}$			1	V
V _{BE(sat)}	$I_{\rm C} = 3 {\rm A}, \ I_{\rm B} = 0.6 {\rm A}$		· · · ·	1.5	V
f _T	$V_{CE} = 10V, I_C = 0.5A, f = 1MHz$		3	· · · · · · · · · · · · · · · · · · ·	MHz
+				1	
Lon	$I_C = 3 A$			1.2	μs
tstg	$I_{B1} = 0.6 \text{ A}, \ I_{B2} = -0.6 \text{ A}$			3	μs
Collector current 2SC3211	V _{CC} =200 V			1	
] Lf				1.2	μS
	I _{CBO} I _{EBO} VCEO(sus) h _{FE1} h _{FE2} VCE(sat) VBE(sat) f _T t _{on}	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$I_{CBO} = \begin{array}{c} V_{CB} = 800 \text{ V}, \ I_{E} = 0 \\ V_{CB} = 900 \text{ V}, \ I_{E} = 0 \\ \end{array}$ $I_{EBO} = \begin{array}{c} V_{EB} = 5 \text{ V}, \ I_{C} = 0 \\ V_{CEO(sus)} = \begin{array}{c} I_{C} = 0.2 \text{ A}, \ L = 25 \text{ mH} \\ \end{array}$ $I_{C} = \begin{array}{c} 5 \text{ V}, \ I_{C} = 0.1 \text{ A} \\ \end{array}$ $I_{C} = \begin{array}{c} 5 \text{ V}, \ I_{C} = 0.1 \text{ A} \\ \end{array}$ $V_{CE(sat)} = \begin{array}{c} I_{C} = 3 \text{ A}, \ I_{B} = 0.6 \text{ A} \\ \end{array}$ $V_{CE(sat)} = \begin{array}{c} I_{C} = 3 \text{ A}, \ I_{B} = 0.6 \text{ A} \\ \end{array}$ $V_{CE} = \begin{array}{c} 10 \text{ V}, \ I_{C} = 0.5 \text{ A}, \ I_{C} = 10 \text{ MHz} \\ \end{array}$ $I_{C} = \begin{array}{c} 3 \text{ A} \\ \end{array}$ $I_{C} = 3 \text{ A} \\ I_{B1} = 0.6 \text{ A}, \ I_{B2} = -0.6 \text{ A} \\ \end{array}$ $V_{CC} = 200 \text{ V}$	$I_{CBO} = \begin{cases} V_{CB} = 800 \text{ V}, \ I_{E} = 0 \\ V_{CB} = 900 \text{ V}, \ I_{E} = 0 \end{cases}$ $I_{EBO} = \begin{cases} V_{EB} = 5 \text{ V}, \ I_{C} = 0 \\ V_{CEO(sus)} \end{cases} = \begin{cases} I_{C} = 0.2 \text{ A}, \ L = 25 \text{ mH} \end{cases}$ $I_{C} = 5 \text{ V}, \ I_{C} = 0.1 \text{ A} \end{cases} = \begin{cases} I_{D} = 0.1 \text{ A} \end{cases}$ $V_{CE(sus)} = \begin{cases} I_{C} = 3 \text{ A}, \ I_{D} = 0.6 \text{ A} \\ V_{DE(sus)} \end{cases} = \begin{cases} I_{C} = 3 \text{ A}, \ I_{D} = 0.6 \text{ A} \\ I_{C} = 3 \text{ A}, \ I_{D} = 0.6 \text{ A} \end{cases}$ $I_{C} = 3 \text{ A} = \begin{cases} I_{D} = 0.6 \text{ A}, \ I_{D} = 0.6 \text{ A} \\ V_{CC} = 200 \text{ V} \end{cases}$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$



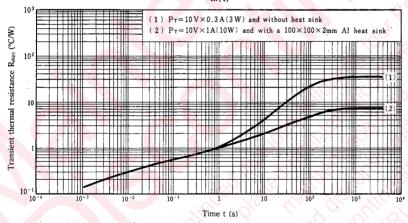
Safety operation area-reverse bias (ASO)



Measurement circuit of reverse bias ASO



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