

FEATURES

- InGaP HBT Technology
- -47 dBc ACPR @ ±10 MHz, +27 dBm
- 29 dB Gain
- High Efficiency
- Low Transistor Junction Temperature
- Matched for a 50 Ω System
- Low Profile Miniature Surface Mount Package; RoHS Compliant
- Multi-Carrier Capability

APPLICATIONS

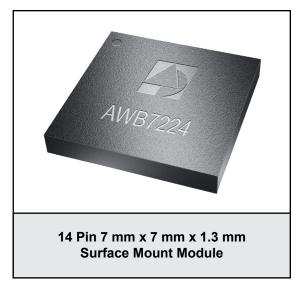
- LTE, WCDMA, and HSDPA Air Interfaces
- Picocell, Femtocell, Home Nodes
- Customer Premises Equipment (CPE)

PRODUCT DESCRIPTION

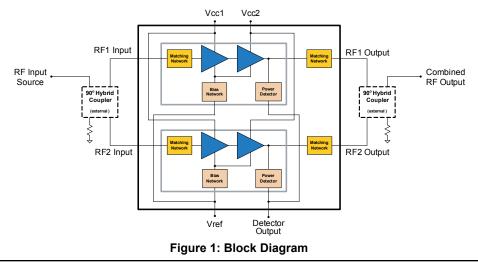
The AWB7224 is a fully matched, Multi-Chip-Module (MCM) designed for picocell, femtocell, and customer premises equipment (CPE) applications. Consisting of two parallel path high linearity, high efficiency power amplifiers, the device meets the extremely demanding needs of small cell infrastructure architectures. Designed for LTE, WCDMA and HSDPA air interfaces operating in the 728 MHz to 768 MHz bands, the AWB7224 delivers up to +27 dBm of LTE (E-TM1.1)

728 - 768 MHz Small-Cell Power Amplifier Module PRELIMINARY DATA SHEET - Rev 1.1

AWB7224



power through an external 90-degree hybrid coupler, with an ACPR of -47 dBc. The device operates from a convenient +4.5 V supply and provides 29 dB of RF gain. The AWB7224 is manufactured using an advanced InGaP HBT MMIC technology offering state-of-the-art reliability, temperature stability, and ruggedness. Its 7 mm x 7 mm x 1.3 mm surface mount package incorporates RF matching networks optimized for output power, efficiency, and linearity in a 50 Ω system.



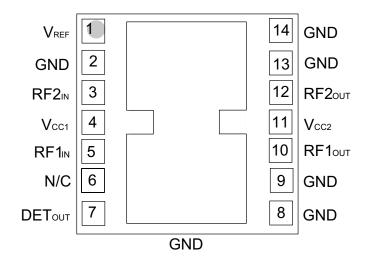


Figure 2: Pinout (X-ray Top View)

PIN	NAME	DESCRIPTION
1	VREF	Reference Voltage
2	GND	Ground
3	RF2ℕ	RF2 Input
4	V _{CC1}	Supply Voltage
5	RF1ℕ	RF1 Input
6	N/C	No Connection
7	DETout	Detector Output
8	GND	Ground
9	GND	Ground
10	RF1out	RF1 Output
11	V _{CC2}	Supply Voltage
12	RF2out	RF2 Output
13	GND	Ground
14	GND	Ground

Table 1: Pin Description

ELECTRICAL CHARACTERISTICS

PARAMETER	MIN	MAX	UNIT			
Supply Voltage (Vcc)	0	+5	V			
Reference Voltage (VREF)	0	+3.5	V			
RF Output Power (Pout) (1)	-	+30	dBm			
ESD Rating Human Body Model ⁽²⁾ Charged Device Model ⁽³⁾	Class 1C Class IV	- -				
MSL Rating (4)	4	-				
Junction Temperature (T _j)	-	+150	°C			
Storage Temperature (Tstg)	-40	+150	°C			

Table 2: Absolute Minimum and Maximum Ratings

Stresses in excess of the absolute ratings may cause permanent damage. Functional operation is not implied under these conditions. Exposure to absolute ratings for extended periods of time may adversely affect reliability. *Notes*:

(1) At output of external 90° hybrid coupler.

(2) JEDEC JS-001-2010.

(3) JEDEC JESD22-C101D.

(4) 260 °C peak reflow.

PARAMETER	MIN	ТҮР	MAX	UNIT	COMMENTS
Operating Frequency (f)	728	-	768	MHz	
Supply Voltage (Vcc)	+3.6	+4.5	+4.65	V	
Reference Voltage (V _{REF})	+2.75 0	+2.85 -	+2.95 +0.5	V	PA "on" PA "shut down"
RF Output Power (Pour)	-	+27	-	dBm	Using external 90° hybrid couplers
Case Temperature (Tc)	-40	-	+85	°C	

Table 3: Operating Ranges

The device may be operated safely over these conditions; however, parametric performance is guaranteed only over the conditions defined in the electrical specifications.

PARAMETER	MIN	TYP	MAX	UNIT	COMMENTS
Gain (2)	-	29	-	dB	728 - 768 MHz
ACPR ^{(1), (2), (3)} @ 10 MHz @ 20 MHz	-	-47 -57	-	dBc	
Power-Added Efficiency (1), (2), (3)	-	13	-	%	
Thermal Resistance	-	17	-	°C/W	Junction to Case
Quiescent Current (lcq)	-	265	-	mA	
Reference Current	-	10	-	mA	through VREF pin
Leakage Current	-	3	10	μA	Vcc = +5 V, V REF = 0 V
Harmonics ⁽²⁾ 2fo 3fo, 4fo	- -	-50 -60	- -	dBc	
Input Return Loss (2)	-	20	-	dB	
Output Return Loss (2)	-	20	-	dB	
P1dB	-	+35.5	-	dBm	CW tone
Spurious Output Level ⁽²⁾ (all spurious outputs)	-	-	-60	dBc	Pout ≤ +27 dBm In-band load VSWR < 5:1 Out-of-band load VSWR < 10:1 Applies over all voltage and temperature operating ranges
Load mismatch stress with no permanent degradation or failure ⁽²⁾	8:1	-	-	VSWR	Vcc = +4.5 V, Pout = + 27 dBm Applies over full operating temperature range

Table 4: Electrical Specifications (Tc = +25 °C, Vcc = +4.5 V, V_{REF} = +2.85 V, 50 Ω system)

Notes:

(1) ACPR and Efficiency measured at 748 MHz.

(2) $P_{OUT} = +27 \text{ dBm}$, using specified external 90° hybrid couplers.

(3) LTE E-TM1.1 (10 MHz).

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APPLICATION INFORMATION

To ensure proper performance, refer to all related Application Notes on the ANADIGICS web site: http://www.anadigics.com

Shutdown Mode

The power amplifier may be placed in a shutdown mode by applying logic low levels (see Operating Ranges table) to the VREF voltage.

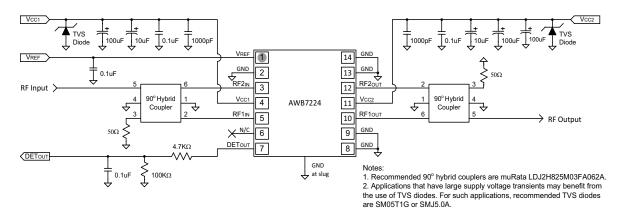
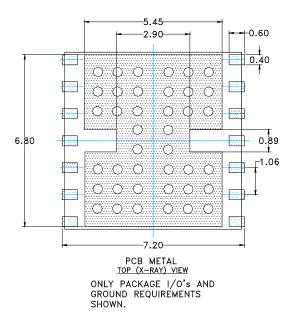
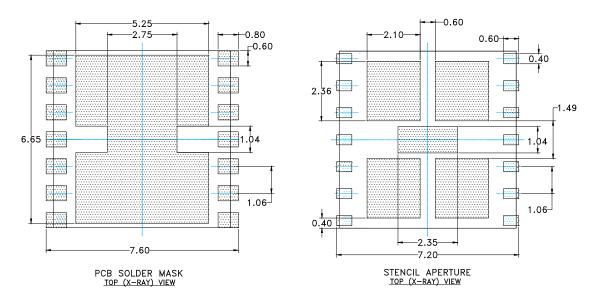


Figure 3: Application Circuit Schematic



NOTES:

- (1) UNLESS SPECIFIED DIMENSIONS ARE SYMMETRICAL ABOUT CENTER LINES SHOWN.
- (2) DIMENSIONS IN MILLIMETERS.
- (3) VIAS SHOWN IN PCB METAL VIEW ARE FOR REFERENCE ONLY. NUMBER & SIZE OF THERMAL VIAS REQUIRED DEPENDENT ON HEAT DISSIPATION REQUIREMENT AND THE PCB PROCESS CAPABILITY.





AWB7224

PACKAGE OUTLINE

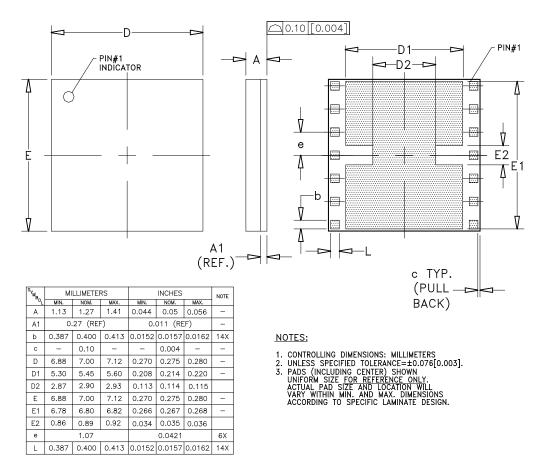
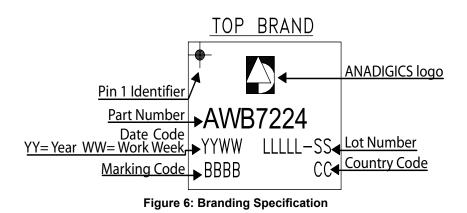


Figure 5: Package Outline - 14 Pin 7 mm x 7 mm x 1.3 mm Surface Mount Module



PRELIMINARY DATA SHEET - Rev 1.1 09/2014



COMPONENT PACKAGING

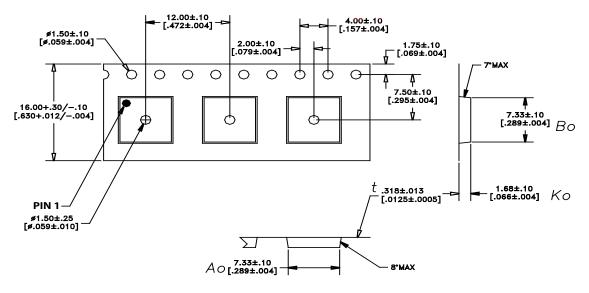


Figure 7: Tape & Reel Packaging

Table 5: Tape & Reel Dimensions

PACKAGE TYPE	TAPE WIDTH	POCKET PITCH	REEL CAPACITY	MAX REEL DIA
7 mm x 7 mm x 1.3 mm	16 mm	12 mm	2500	13"

ORDERING INFORMATION

ORDER	TEMPERATURE	PACKAGE	COMPONENT PACKAGING
NUMBER	RANGE	DESCRIPTION	
AWB7224P8	-40 °C to +85 °C	RoHS-compliant 14 Pin 7 mm x 7 mm x 1.3 mm Surface Mount Module	Tape and Reel, 2500 pieces per Reel



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URL: http://www.anadigics.com

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