P-Channel Enhancement Mode Power MOSFET

MTD080P06J3

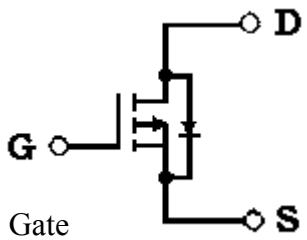
BV_{DSS}	**-60V**
I_D@V_{GS}=-10V, T_C=25°C	**-12.5A**
$R_{DS(ON)}$@V_{GS}=-10V, I_D=-10A	**84mΩ(typ)**
$R_{DS(ON)}$@V_{GS}=-5V, I_D=-8A	**113mΩ(typ)**

Features

- Low Gate Charge
- Simple Drive Requirement
- Pb-free Lead Plating & Halogen-free Package

Equivalent Circuit

MTD080P06J3



G：Gate
D：Drain
S：Source

Outline

TO-252(DPAK)

G D S

Ordering Information

Device	Package	Shipping
MTD080P06J3-0-T3-G	TO-252 (Pb-free lead plating & halogen-free package)	2500 pcs / Tape & Reel

Environment friendly grade : S for RoHS compliant products, G for RoHS compliant and green compound products

Packing spec, T3 : 2500 pcs / tape & reel, 13” reel

Product rank, zero for no rank products

Product name

Absolute Maximum Ratings (T_C=25°C, unless otherwise noted)

Parameter			Symbol	Limits	Unit
Drain-Source Voltage			V_{DS}	-60	V
Gate-Source Voltage			V_{GS}	±20	
Continuous Drain Current @T_C=25°C, V_{GS}=-10V			I_D	-12.5	A
Continuous Drain Current @T_C=100°C, V_{GS}=-10V				-7.9	
Continuous Drain Current @T_A=25°C, V_{GS}=-10V		(Note 2)	I_{DSM}	-3.5	
Continuous Drain Current @T_A=70°C, V_{GS}=-10V		(Note 2)		-2.8	
Pulsed Drain Current		(Note 3)	I_{DM}	-45	
Avalanche Current @L=0.1mH		(Note 3)	I_{AS}	-12.5	
Avalanche Energy @ L=1mH, I_D=-10A, V_{DD}=-50V		(Note 2)	E_{AS}	50	mJ
Total Power Dissipation	T_C=25°C	(Note 1)	P_D	31	W
	T_C=100°C	(Note 1)		12	
	T_A=25°C	(Note 2)	P_{DSM}	2.5	
	T_A=70°C	(Note 2)		1.6	
Operating Junction and Storage Temperature Range			Tj, Tstg	-55~+150	°C

* 100% UIS testing in condition of V_D=-15V, L=1mH, V_G=-10V, I_{AS}=-6A, Rated V_{DS}=-60V

Thermal Data

Parameter	Symbol	Typical	Maximum	Unit
Thermal Resistance, Junction-to-case	$R_{\theta JC}$	3.6	4	°C/W
Thermal Resistance, Junction-to-ambient, t≤10s (Note 2)	$R_{\theta JA}$	15	18	
Thermal Resistance, Junction-to-ambient, steady state		40	50	

Note : 1.The power dissipation P_D is based on $T_{J(MAX)}$=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

2. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with T_A=25°C. The power dissipation P_{DSM} is based on $R_{\theta JA}$ and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.

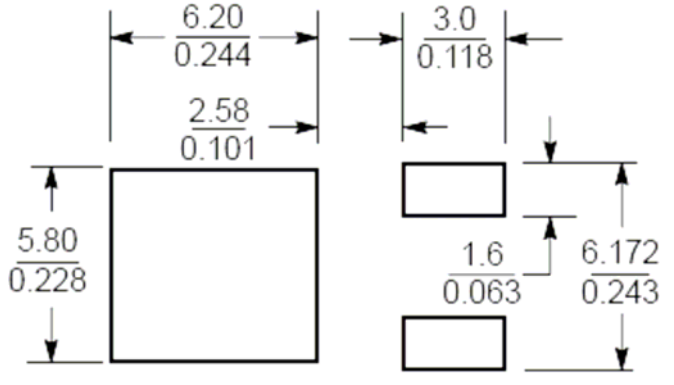
3. Pulse width limited by junction temperature $T_{J(MAX)}$=150°C. Ratings are based on low frequency and low duty cycles to keep initial T_J=25°C.

Characteristics (Tc=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV_{DSS}	-60	-	-	V	V_{GS}=0V, I_D=-250μA
$V_{GS(th)}$	-1.5	-	-3.5		V_{DS} =V_{GS}, I_D=-250μA
I_{GSS}	-	-	±100	nA	V_{GS}=±20V, V_{DS}=0V
I_{DSS}	-	-	-1	μA	V_{DS} =-48V, V_{GS} =0V
	-	-	-25		V_{DS} =-48V, V_{GS} =0V, T_J=85°C
$R_{DS(ON)}$ *1	-	84	110	mΩ	V_{GS} =-10V, I_D=-10A
	-	113	150		V_{GS} =-5V, I_D=-8A
G_{FS} *1	-	11.5	-	S	V_{DS} =-10V, I_D=-10A
Dynamic					
Qg *1, 2	-	11.9	17.9	nC	I_D=-10A, V_{DS}=-48V, V_{GS}=-10V
Qgs *1, 2	-	2.5	-		
Qgd *1, 2	-	3.2	-		
$t_{d(ON)}$ *1, 2	-	7.4	11	ns	V_{DS}=-30V, I_D=-10A, V_{GS}=-10V, R_G=3Ω
tr *1, 2	-	17.2	25.8		
$t_{d(OFF)}$ *1, 2	-	22.8	34.2		
t_f *1, 2	-	7.4	11		
Ciss	-	512	-	pF	V_{GS}=0V, V_{DS}=-25V, f=1MHz
Coss	-	53	-		
Crss	-	22	-		
Rg	-	6.5	-	Ω	f=1MHz
Source-Drain Diode Ratings and Characteristics					
I_S *1	-	-	-12.5	A	
I_{SM} *1	-	-	-45		
V_{SD} *1	-	-0.97	-1.2	V	I_S=-10A, V_{GS}=0V
trr	-	14	21	ns	I_F=-10A, dI_F/dt=100A/μs
Qrr	-	10.4	-	nC	

Note : *1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%
*2.Independent of operating temperature
*3.Pulse width limited by maximum junction temperature.

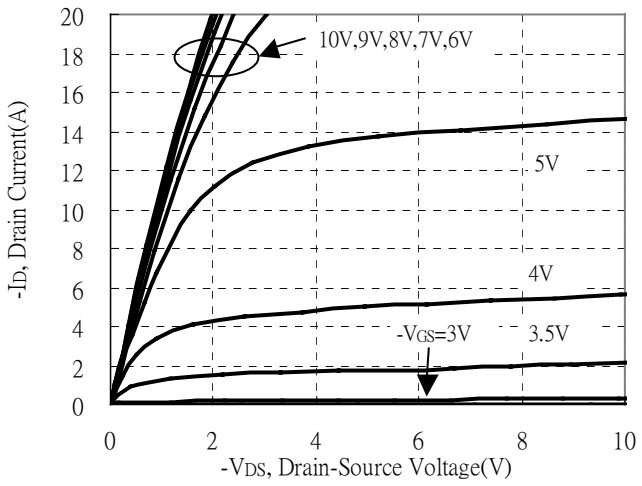
Recommended soldering footprint



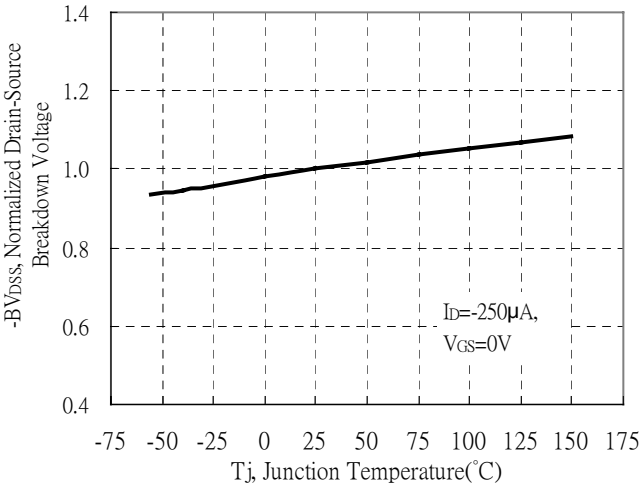
Unit (mm / inch)

Typical Characteristics

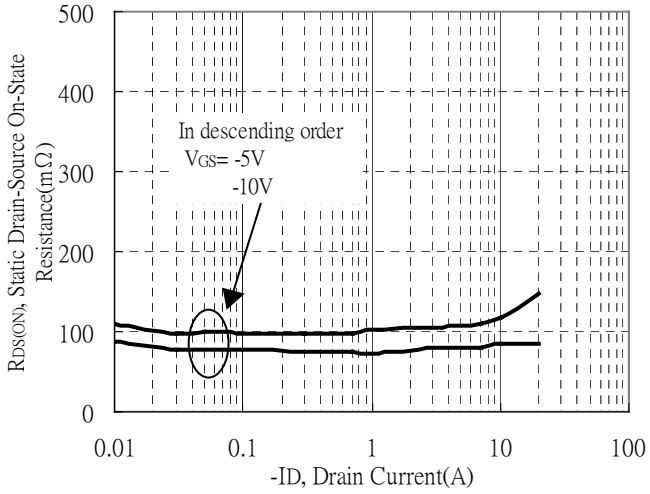
Typical Output Characteristics



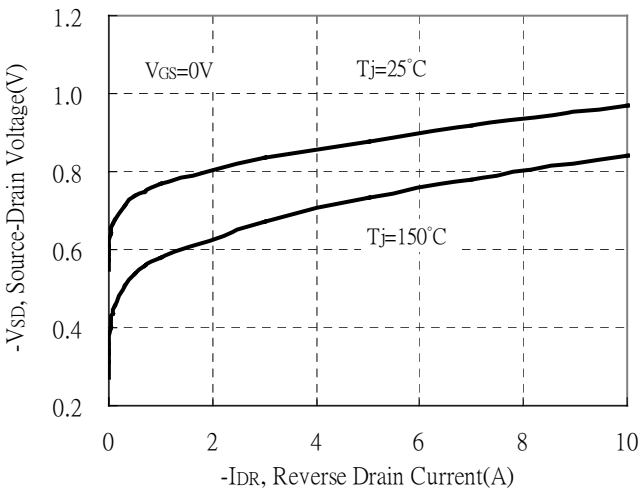
Brekdown Voltage vs Ambient Temperature



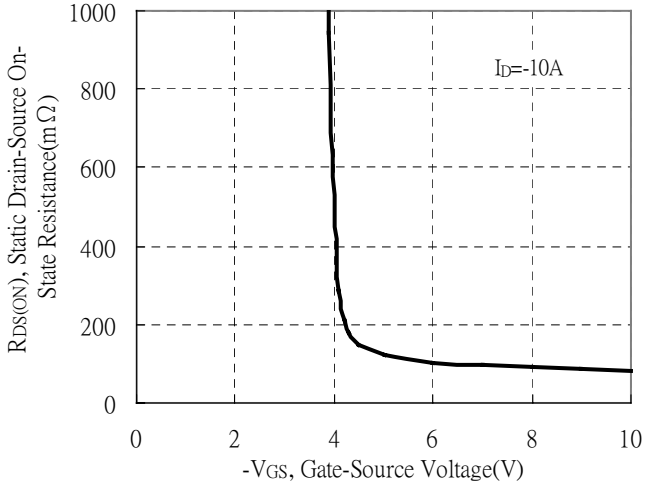
Static Drain-Source On-State resistance vs Drain Current



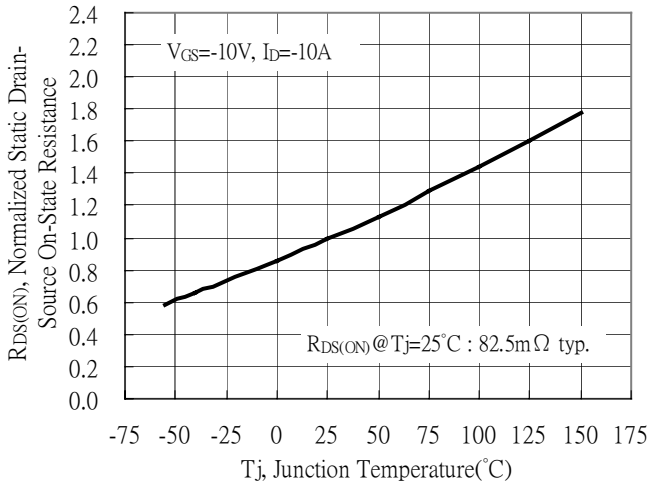
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

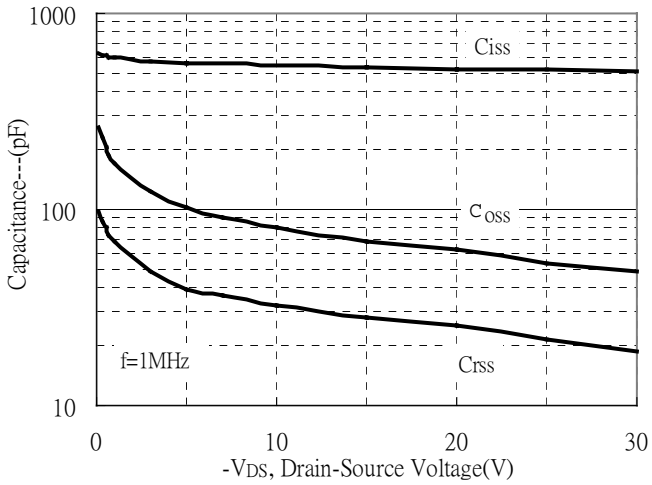


Drain-Source On-State Resistance vs Junction Tempearture

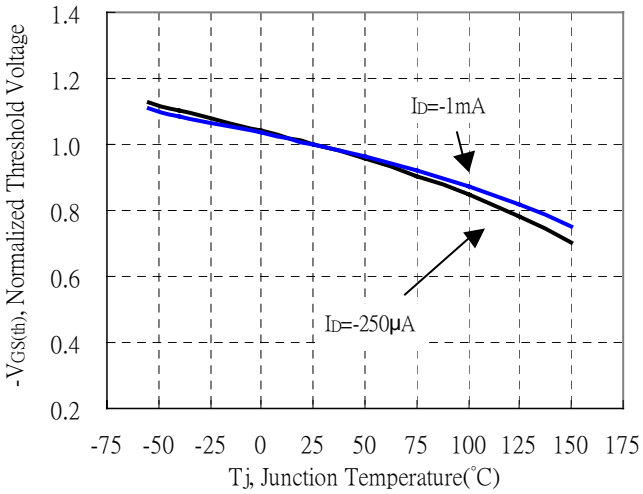


Typical Characteristics (Cont.)

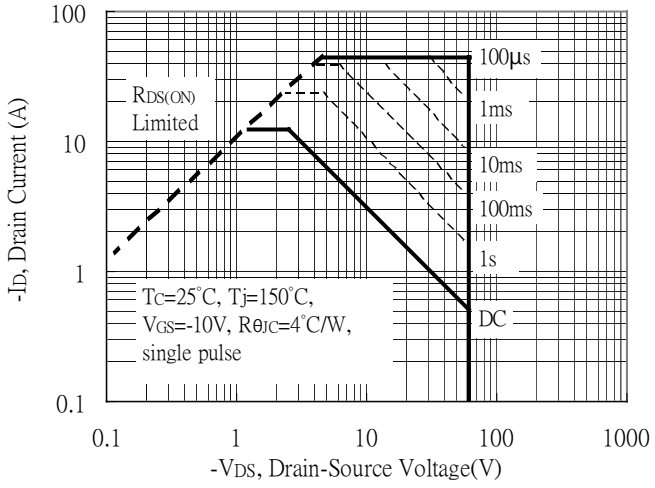
Capacitance vs Drain-to-Source Voltage



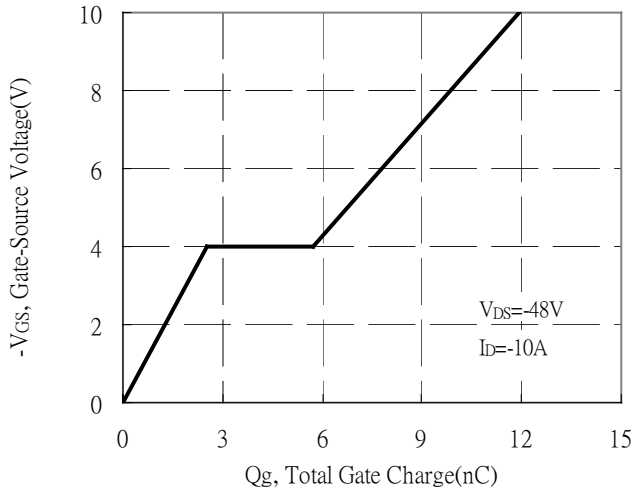
Threshold Voltage vs Junction Tempearture



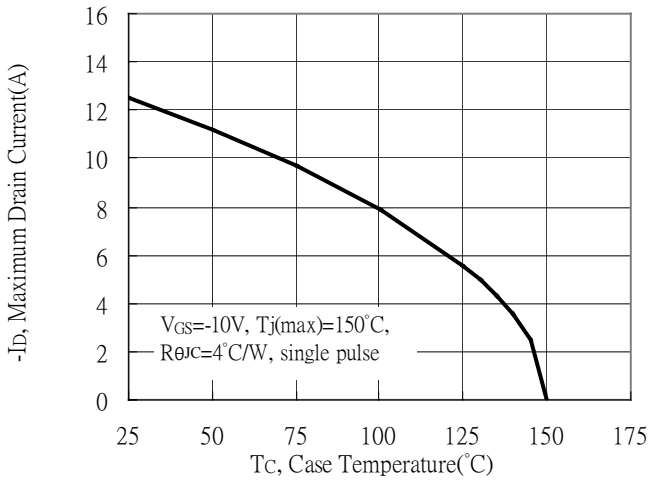
Maximum Safe Operating Area



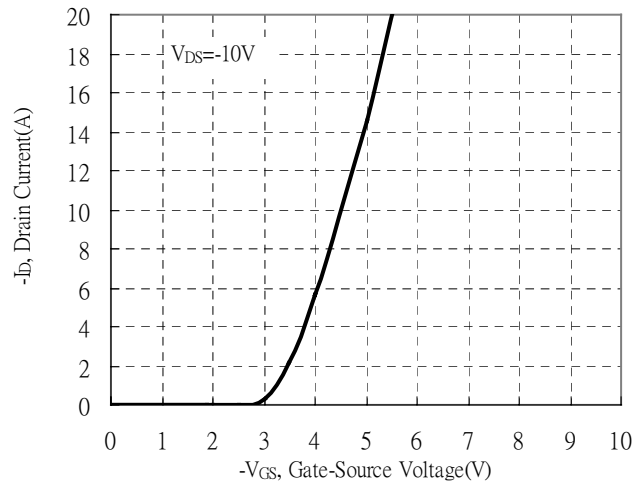
Gate Charge Characteristics



Maximum Drain Current vs Case Temperature

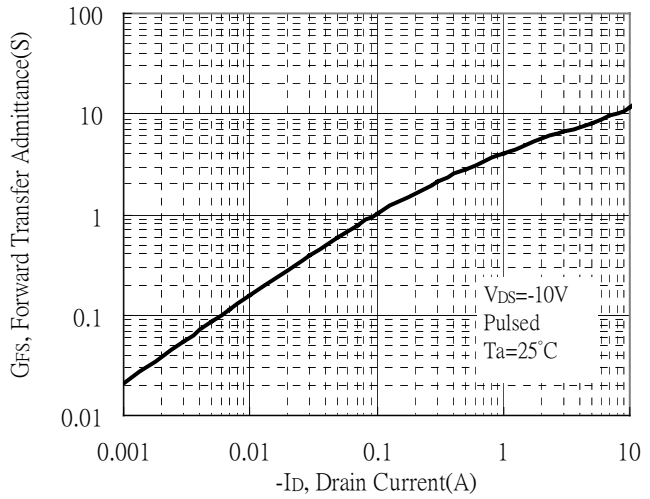


Typical Transfer Characteristics

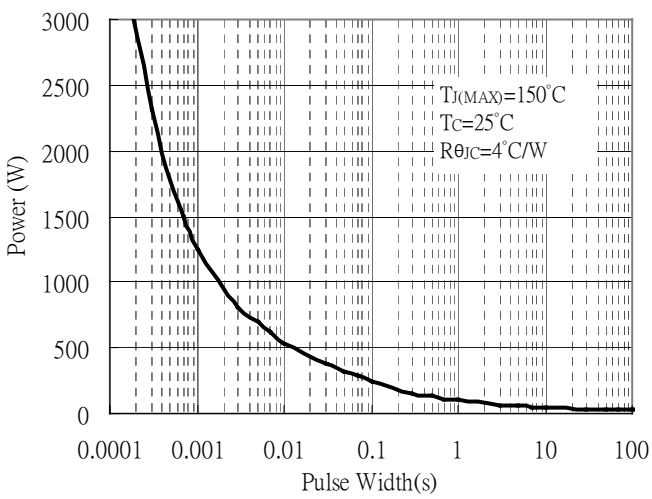


Typical Characteristics (Cont.)

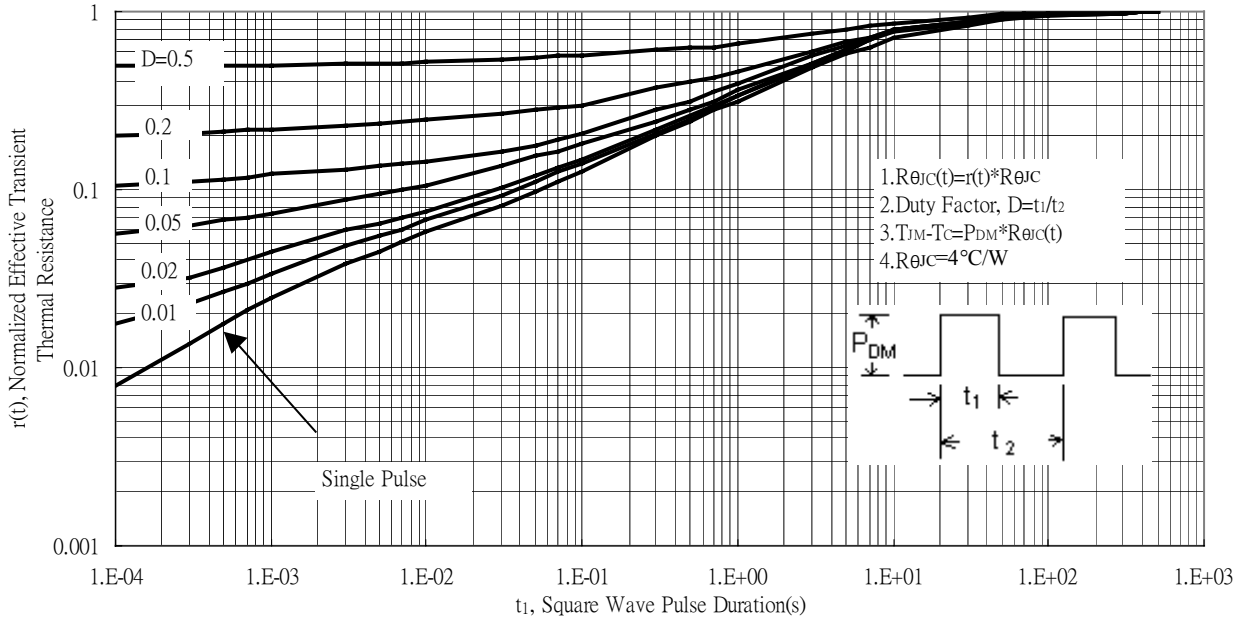
Forward Transfer Admittance vs Drain Current



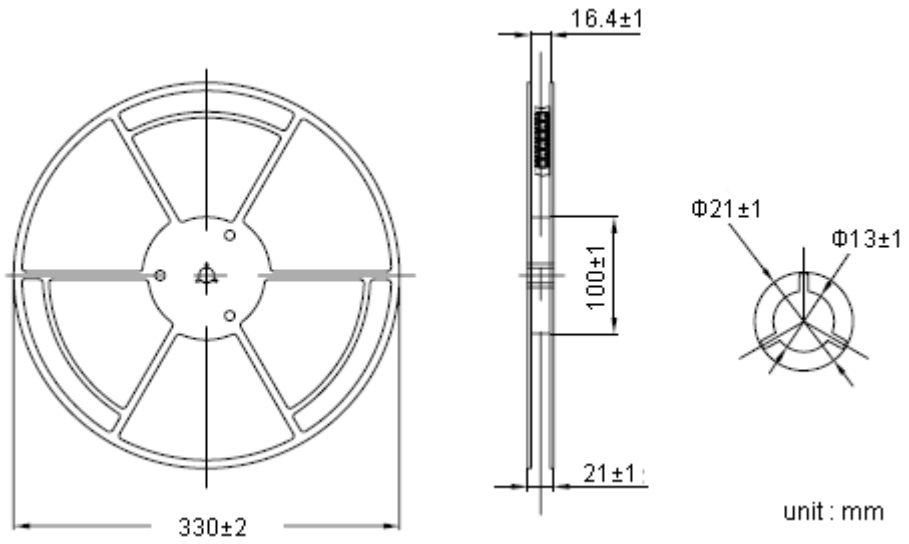
Single Pulse Power Rating, Junction to Case



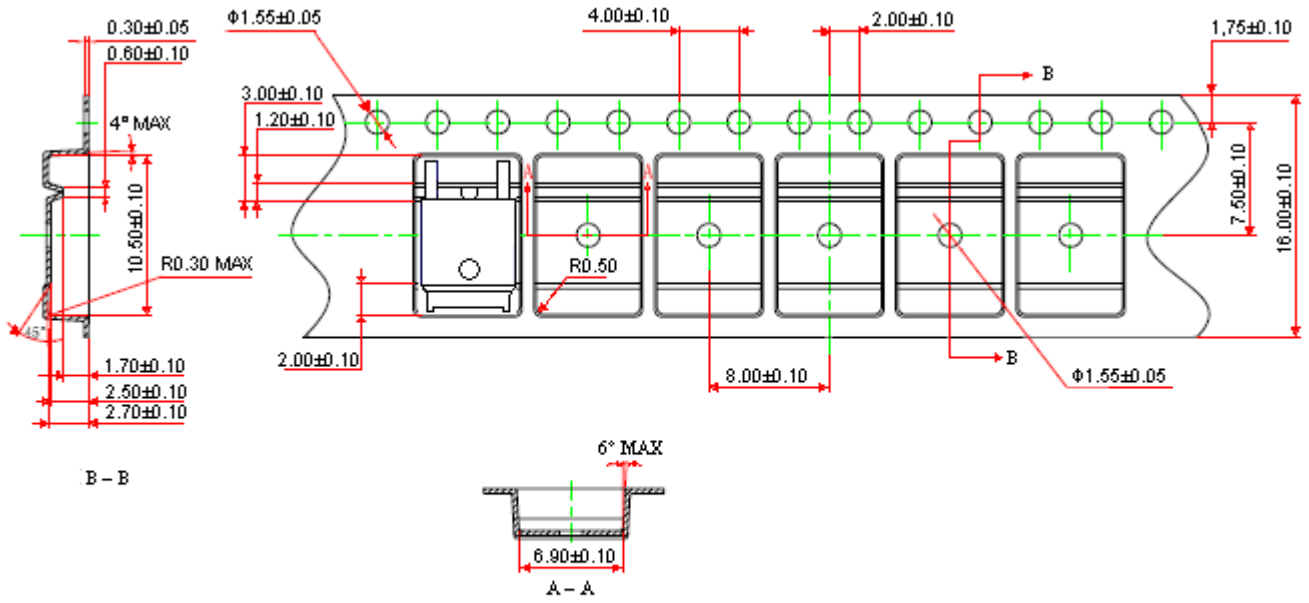
Transient Thermal Response Curves



Reel Dimension



Carrier Tape Dimension

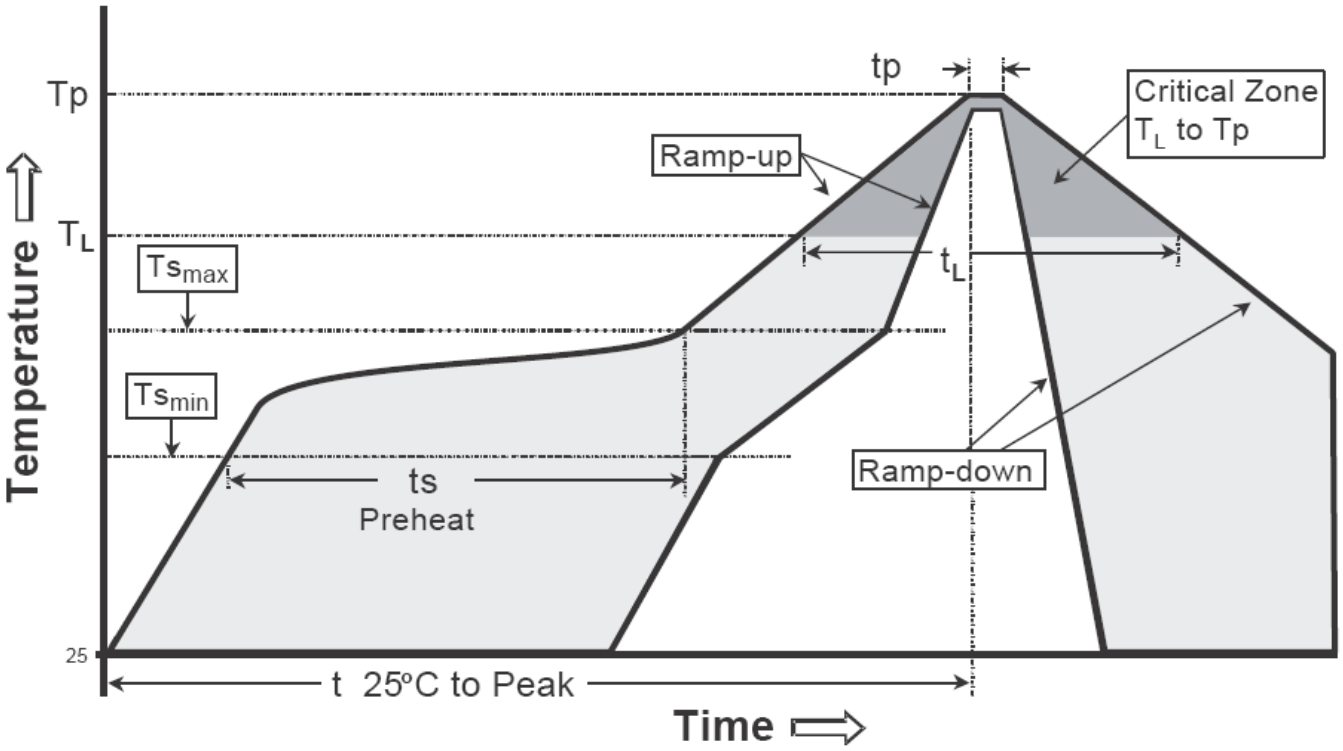


Notes:
1. 10 sprocket hole pitch cumulative tolerance ±0.2.
2. Camber not to exceed 1mm in 100mm.
3. Material: conductive black polystyrene, antistatic coated : 10^{5} Ω/□~ 10^{11} Ω/□ unit : mm

Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

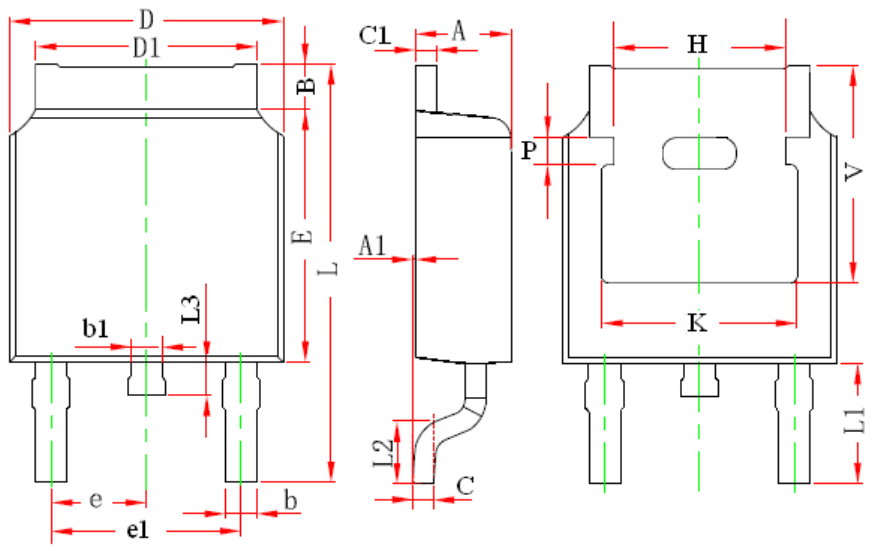
Recommended temperature profile for IR reflow

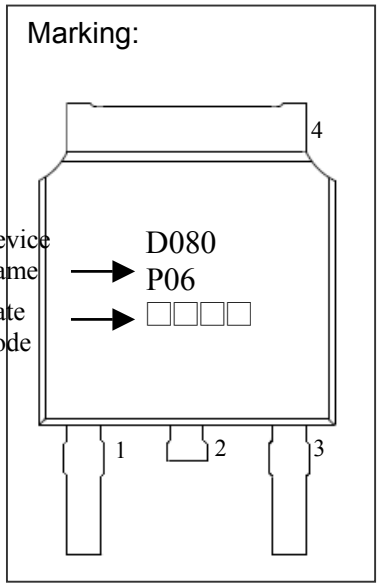


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat −Temperature Min(T_S min) −Temperature Max(T_S max) −Time($t_{s\ min}$ to $t_{s\ max}$)	 100°C 150°C 60-120 seconds	 150°C 200°C 60-180 seconds
Time maintained above: −Temperature (T_L) − Time (t_L)	 183°C 60-150 seconds	 217°C 60-150 seconds
Peak Temperature(T_P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

TO-252 Dimension





3-Lead TO-252 Plastic Surface Mount Package
CYStek Package Code: J3

Style: Pin 1.Gate 2.Drain 3.Source 4.Drain

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.087	0.094	2.200	2.400	e	0.086	0.094	2.186	2.386
A1	0.000	0.005	0.000	0.127	e1	0.172	0.188	4.372	4.772
B	0.039	0.048	0.990	1.210	H	0.163	REF	4.140	REF
b	0.026	0.034	0.660	0.860	K	0.190	REF	4.830	REF
b1	0.026	0.034	0.660	0.860	L	0.386	0.409	9.800	10.400
C	0.018	0.023	0.460	0.580	L1	0.114	REF	2.900	REF
C1	0.018	0.023	0.460	0.580	L2	0.055	0.067	1.400	1.700
D	0.256	0.264	6.500	6.700	L3	0.024	0.039	0.600	1.000
D1	0.201	0.215	5.100	5.460	P	0.026	REF	0.650	REF
E	0.236	0.244	6.000	6.200	V	0.211	REF	5.350	REF

Notes: 1.Controlling dimension: millimeters.
2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead : Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.