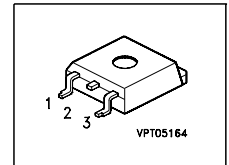


**Cool MOS™ Power Transistor**
**Feature**

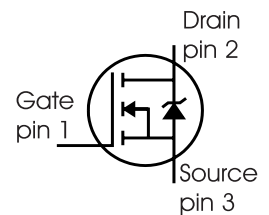
- New revolutionary high voltage technology
- Qualified according to JEDEC<sup>0)</sup> for target applications
- Ultra low gate charge
- Periodic avalanche rated
- Extreme dv/dt rated
- Ultra low effective capacitances
- Improved transconductance

$V_{DS}$	600	V
$R_{DS(on)}$	0.6	$\Omega$
$I_D$	7.3	A

PG-TO263



Type	Package	Ordering Code	Marking
SPB07N60S5	PG-TO263	Q67040-S4185	07N60S5


**Maximum Ratings**

Parameter	Symbol	Value	Unit
Continuous drain current $T_C = 25\text{ °C}$ $T_C = 100\text{ °C}$	$I_D$	7.3 4.6	A
Pulsed drain current, $t_p$ limited by $T_{jmax}$	$I_{D\text{ puls}}$	14.6	
Avalanche energy, single pulse $I_D = -\text{A}, V_{DD} = 50\text{ V}$	$E_{AS}$	230	mJ
Avalanche energy, repetitive $t_{AR}$ limited by $T_{jmax}$ <sup>1</sup> $I_D = 7.3\text{ A}, V_{DD} = 50\text{ V}$	$E_{AR}$	0.5	
Avalanche current, repetitive $t_{AR}$ limited by $T_{jmax}$	$I_{AR}$	7.3	A
Gate source voltage	$V_{GS}$	$\pm 20$	V
Gate source voltage AC ( $f > 1\text{ Hz}$ )	$V_{GS}$	$\pm 30$	
Power dissipation, $T_C = 25\text{ °C}$	$P_{tot}$	83	W
Operating and storage temperature	$T_j, T_{stg}$	-55... +150	°C

**Maximum Ratings**

Parameter	Symbol	Value	Unit
Drain Source voltage slope $V_{DS} = 480\text{ V}$ , $I_D = 7.3\text{ A}$ , $T_j = 125\text{ °C}$	$dv/dt$	20	V/ns

**Thermal Characteristics**

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Thermal resistance, junction - case	$R_{thJC}$	-	-	1.5	K/W
Thermal resistance, junction - ambient, leaded	$R_{thJA}$	-	-	62	
SMD version, device on PCB: @ min. footprint @ 6 cm <sup>2</sup> cooling area <sup>2)</sup>	$R_{thJA}$	-	-	62	
Soldering temperature, reflow soldering, MSL1 1.6 mm (0.063 in.) from case for 10s	$T_{sold}$	-	-	260	°C

**Electrical Characteristics, at  $T_j=25\text{ °C}$  unless otherwise specified**

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS}=0V$ , $I_D=0.25mA$	600	-	-	V
Drain-Source avalanche breakdown voltage	$V_{(BR)DS}$	$V_{GS}=0V$ , $I_D=7.3A$	-	700	-	
Gate threshold voltage	$V_{GS(th)}$	$I_D=350\mu A$ , $V_{GS}=V_{DS}$	3.5	4.5	5.5	
Zero gate voltage drain current	$I_{DSS}$	$V_{DS}=600V$ , $V_{GS}=0V$ , $T_j=25\text{ °C}$ , $T_j=150\text{ °C}$	-	0.5	1	$\mu A$
Gate-source leakage current	$I_{GSS}$	$V_{GS}=20V$ , $V_{DS}=0V$	-	-	100	
Drain-source on-state resistance	$R_{DS(on)}$	$V_{GS}=10V$ , $I_D=4.6A$ , $T_j=25\text{ °C}$ $T_j=150\text{ °C}$	-	0.54	0.6	$\Omega$
Gate input resistance	$R_G$	$f=1MHz$ , open Drain	-	19	-	

**Electrical Characteristics** , at  $T_j = 25\text{ }^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
<b>Characteristics</b>						
Transconductance	$g_{fs}$	$V_{DS} \geq 2 \cdot I_D \cdot R_{DS(on)max}$ , $I_D = 4.6\text{A}$	-	4	-	S
Input capacitance	$C_{iss}$	$V_{GS} = 0\text{V}$ , $V_{DS} = 25\text{V}$ , $f = 1\text{MHz}$	-	970	-	pF
Output capacitance	$C_{oss}$		-	370	-	
Reverse transfer capacitance	$C_{rss}$		-	10	-	
Effective output capacitance, <sup>3)</sup> energy related	$C_{o(er)}$	$V_{GS} = 0\text{V}$ , $V_{DS} = 0\text{V to } 480\text{V}$	-	30	-	pF
Effective output capacitance, <sup>4)</sup> time related	$C_{o(tr)}$		-	55	-	
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 350\text{V}$ , $V_{GS} = 0/10\text{V}$ , $I_D = 7.3\text{A}$ , $R_G = 12\Omega$	-	120	-	ns
Rise time	$t_r$		-	40	-	
Turn-off delay time	$t_{d(off)}$		-	170	255	
Fall time	$t_f$		-	20	30	

**Gate Charge Characteristics**

Gate to source charge	$Q_{gs}$	$V_{DD} = 350\text{V}$ , $I_D = 7.3\text{A}$	-	7.5	-	nC
Gate to drain charge	$Q_{gd}$		-	16.5	-	
Gate charge total	$Q_g$	$V_{DD} = 350\text{V}$ , $I_D = 7.3\text{A}$ , $V_{GS} = 0\text{ to } 10\text{V}$	-	27	35	
Gate plateau voltage	$V_{(plateau)}$	$V_{DD} = 350\text{V}$ , $I_D = 7.3\text{A}$	-	8	-	V

<sup>0</sup>J-STD20 and JESD22

<sup>1</sup>Repetitive avalanche causes additional power losses that can be calculated as  $P_{AV} = E_{AR} \cdot f$ .

<sup>2</sup>Device on 40mm\*40mm\*1.5mm epoxy PCB FR4 with 6cm<sup>2</sup> (one layer, 70 μm thick) copper area for drain connection. PCB is vertical without blown air.

<sup>3</sup> $C_{o(er)}$  is a fixed capacitance that gives the same stored energy as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

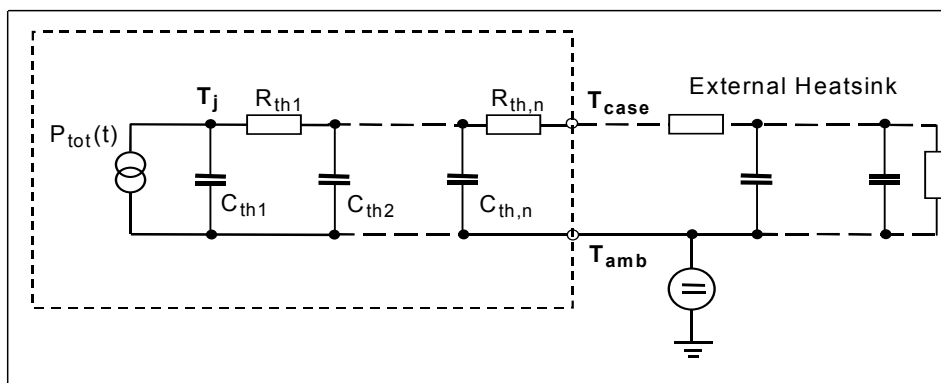
<sup>4</sup> $C_{o(tr)}$  is a fixed capacitance that gives the same charging time as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

**Electrical Characteristics**, at  $T_j = 25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Inverse diode continuous forward current	$I_S$	$T_C=25^\circ\text{C}$	-	-	7.3	A
Inverse diode direct current, pulsed	$I_{SM}$		-	-	14.6	
Inverse diode forward voltage	$V_{SD}$	$V_{GS}=0\text{V}, I_F=I_S$	-	1	1.2	V
Reverse recovery time	$t_{rr}$	$V_R=350\text{V}, I_F=I_S,$	-	750	1275	ns
Reverse recovery charge	$Q_{rr}$	$di_F/dt=100\text{A}/\mu\text{s}$	-	4.9	-	$\mu\text{C}$

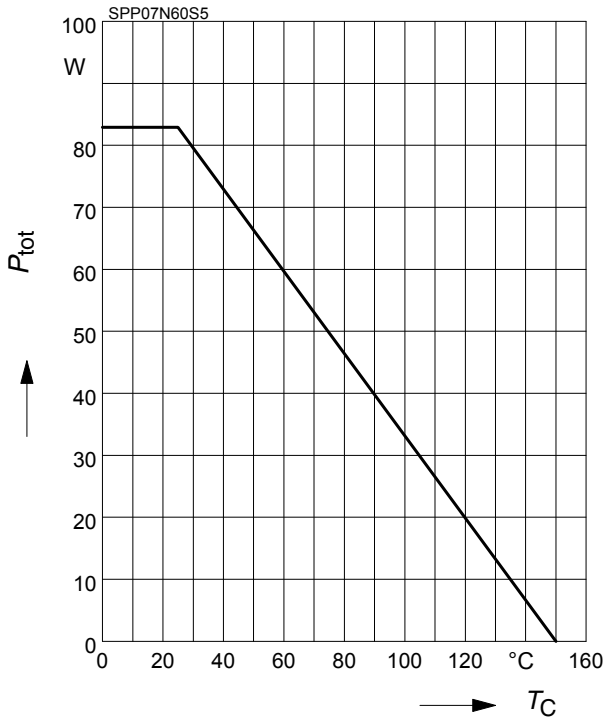
**Typical Transient Thermal Characteristics**

Symbol	Value	Unit	Symbol	Value	Unit
	typ.			typ.	
Thermal resistance			Thermal capacitance		
$R_{th1}$	0.024	K/W	$C_{th1}$	0.00012	Ws/K
$R_{th2}$	0.046		$C_{th2}$	0.0004578	
$R_{th3}$	0.085		$C_{th3}$	0.000645	
$R_{th4}$	0.308		$C_{th4}$	0.001867	
$R_{th5}$	0.317		$C_{th5}$	0.004795	
$R_{th6}$	0.112		$C_{th6}$	0.045	



**1 Power dissipation**

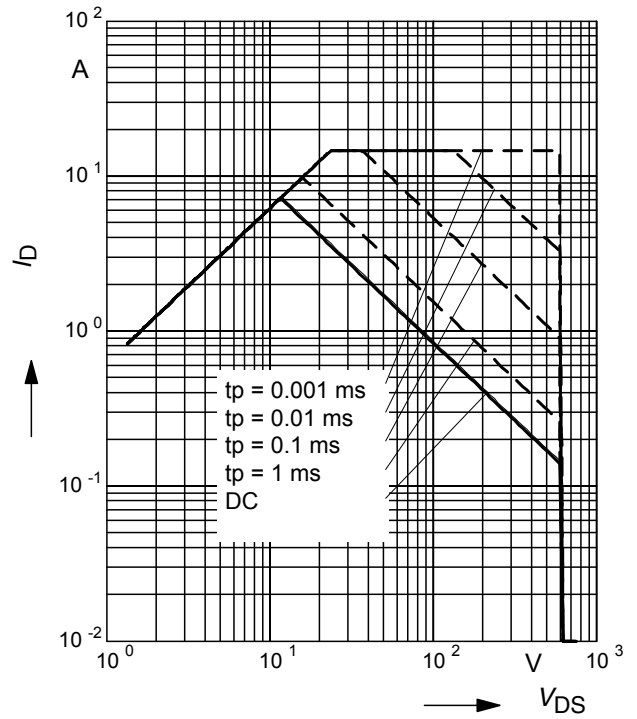
$P_{tot} = f(T_C)$



**2 Safe operating area**

$I_D = f(V_{DS})$

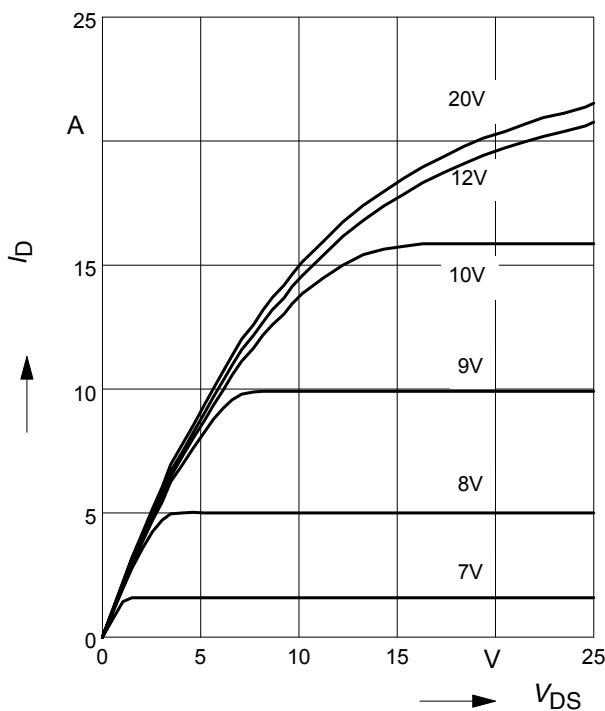
parameter :  $D = 0, T_C = 25^\circ C$



**3 Typ. output characteristic**

$I_D = f(V_{DS}); T_j = 25^\circ C$

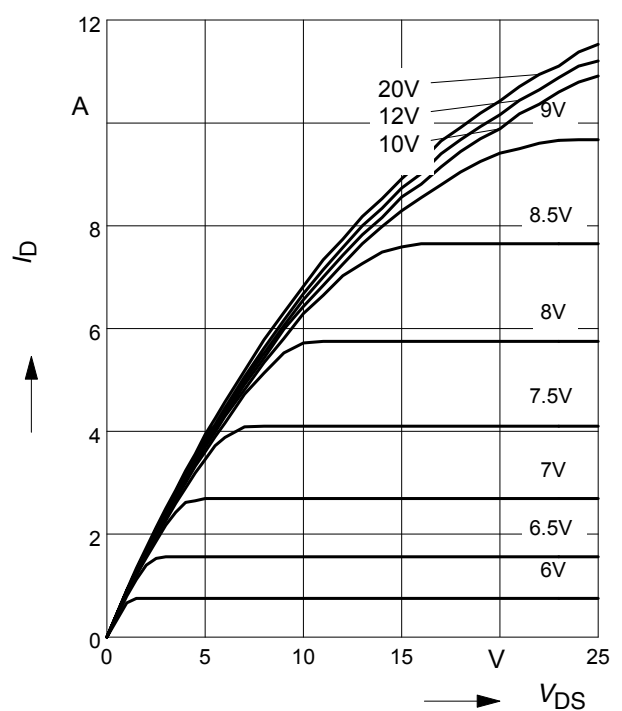
parameter:  $t_p = 10 \mu s, V_{GS}$



**4 Typ. output characteristic**

$I_D = f(V_{DS}); T_j = 150^\circ C$

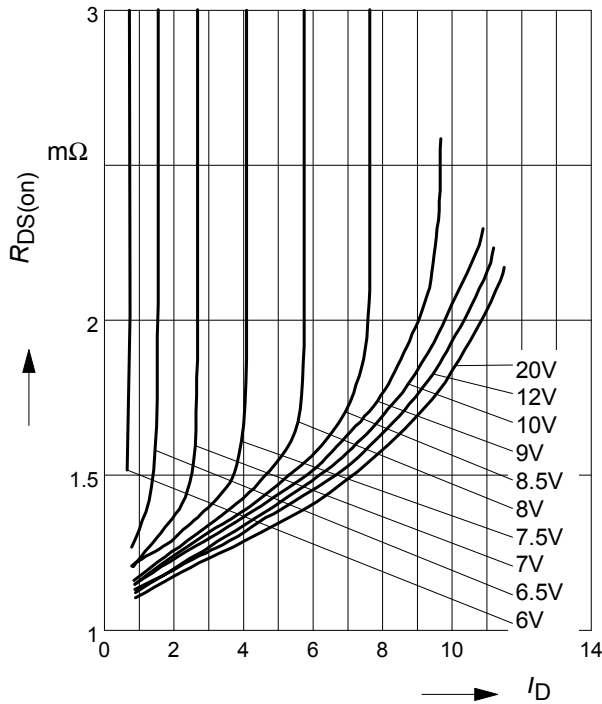
parameter:  $t_p = 10 \mu s, V_{GS}$



**5 Typ. drain-source on resistance**

$$R_{DS(on)} = f(I_D)$$

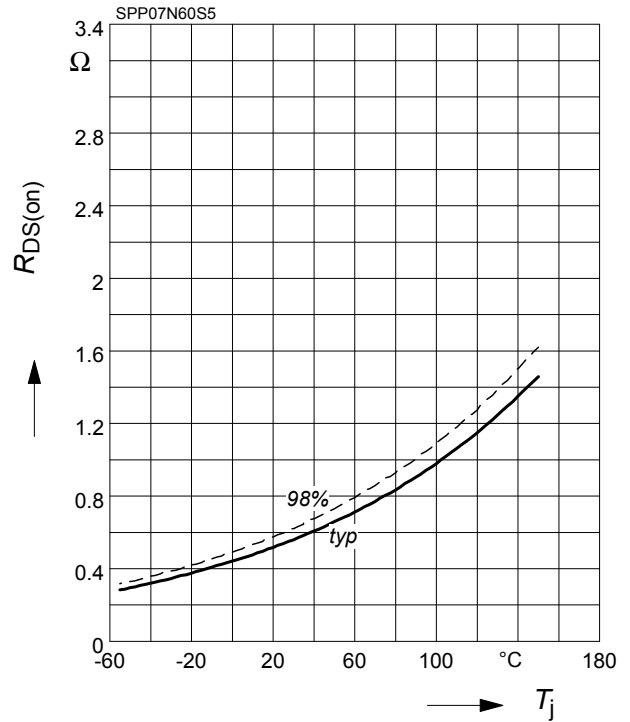
parameter:  $T_j = 150^\circ\text{C}$ ,  $V_{GS}$



**6 Drain-source on-state resistance**

$$R_{DS(on)} = f(T_j)$$

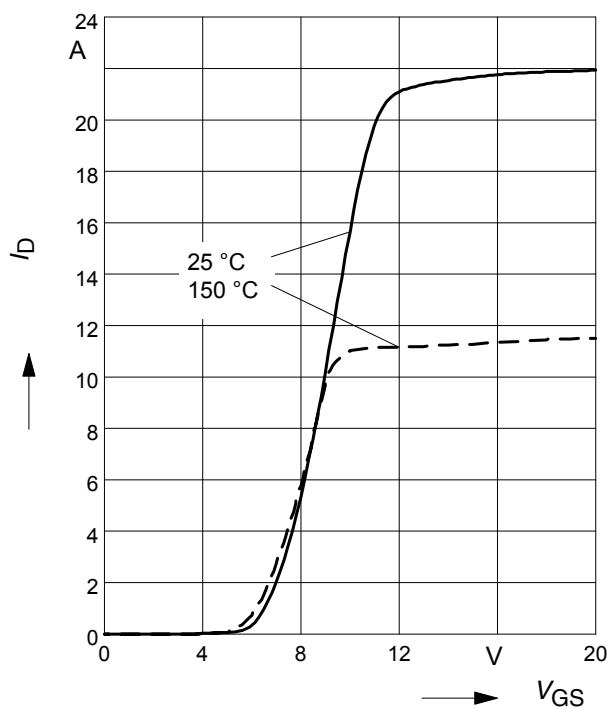
parameter:  $I_D = 4.6\text{ A}$ ,  $V_{GS} = 10\text{ V}$



**7 Typ. transfer characteristics**

$$I_D = f(V_{GS}); V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$$

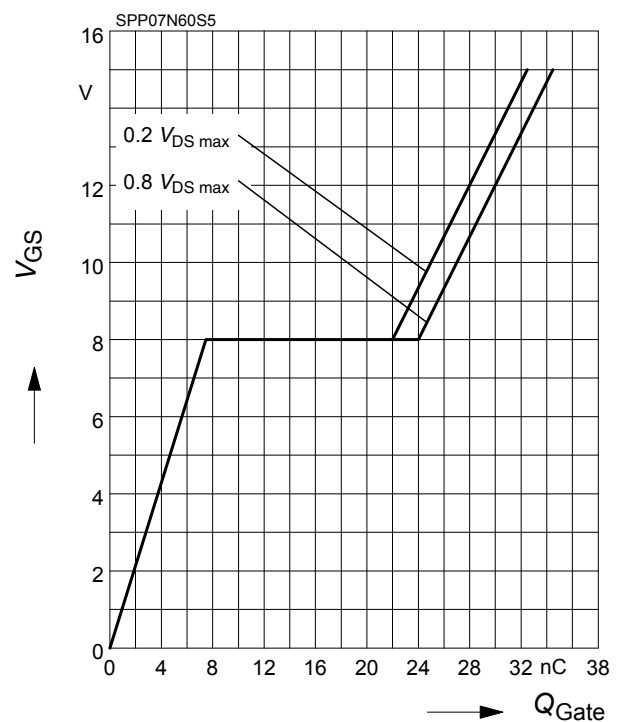
parameter:  $t_p = 10\ \mu\text{s}$



**8 Typ. gate charge**

$$V_{GS} = f(Q_{Gate})$$

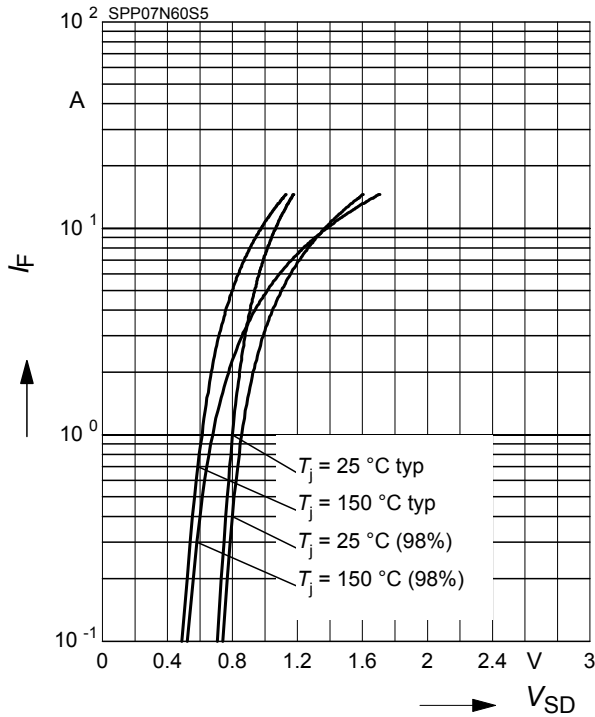
parameter:  $I_D = 7.3\text{ A}$  pulsed



**9 Forward characteristics of body diode**

$I_F = f(V_{SD})$

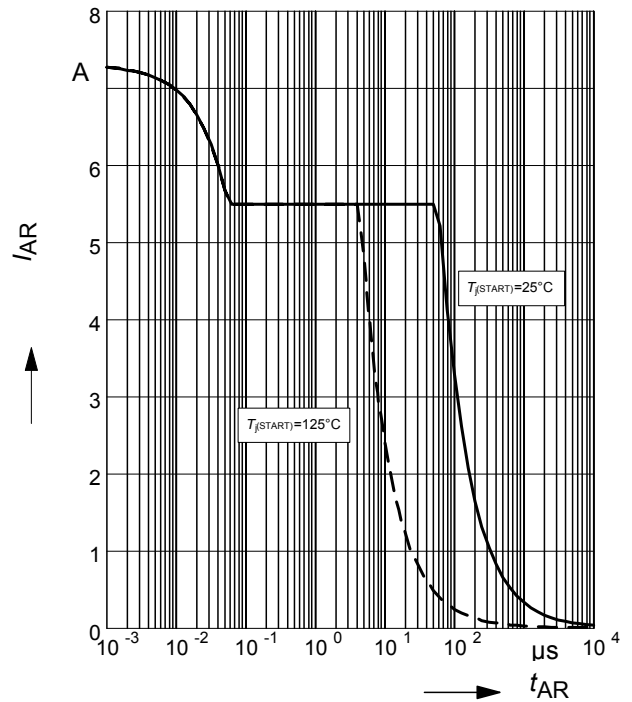
parameter:  $T_j$ ,  $t_p = 10 \mu s$



**10 Avalanche SOA**

$I_{AR} = f(t_{AR})$

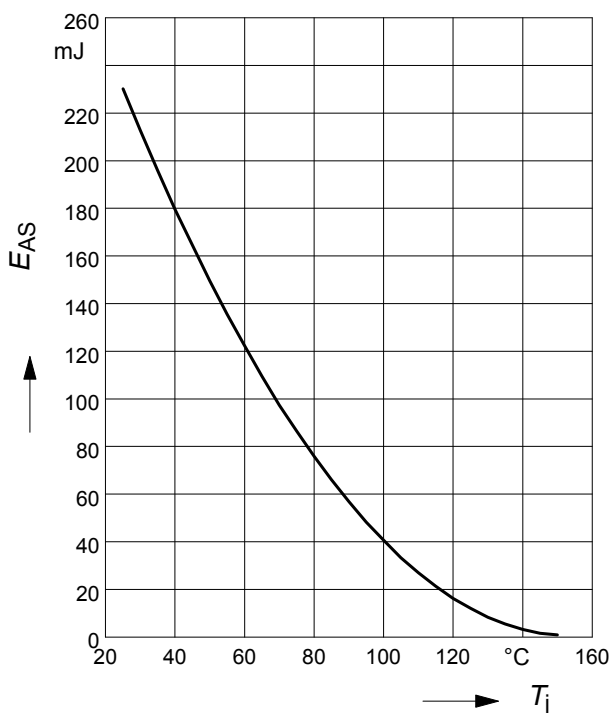
par.:  $T_j \leq 150 \text{ °C}$



**11 Avalanche energy**

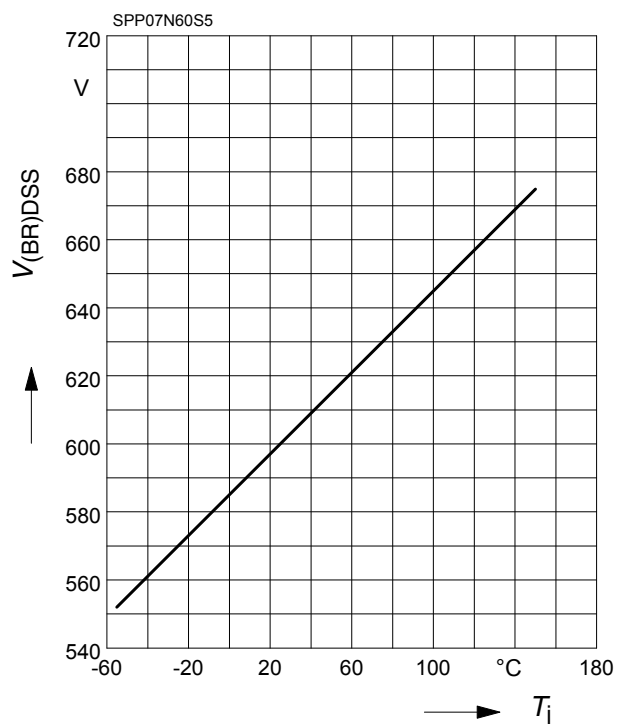
$E_{AS} = f(T_j)$

par.:  $I_D = -A$ ,  $V_{DD} = 50 V$



**12 Drain-source breakdown voltage**

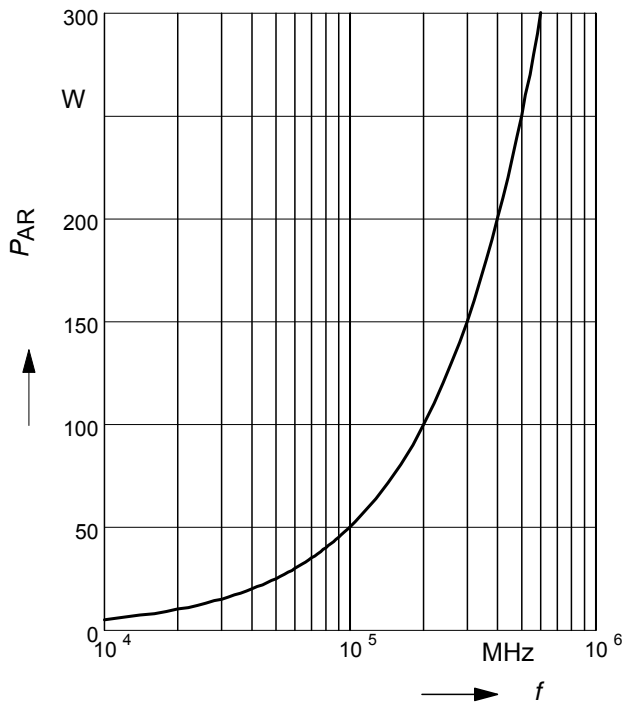
$V_{(BR)DSS} = f(T_j)$



**13 Avalanche power losses**

$$P_{AR} = f(f)$$

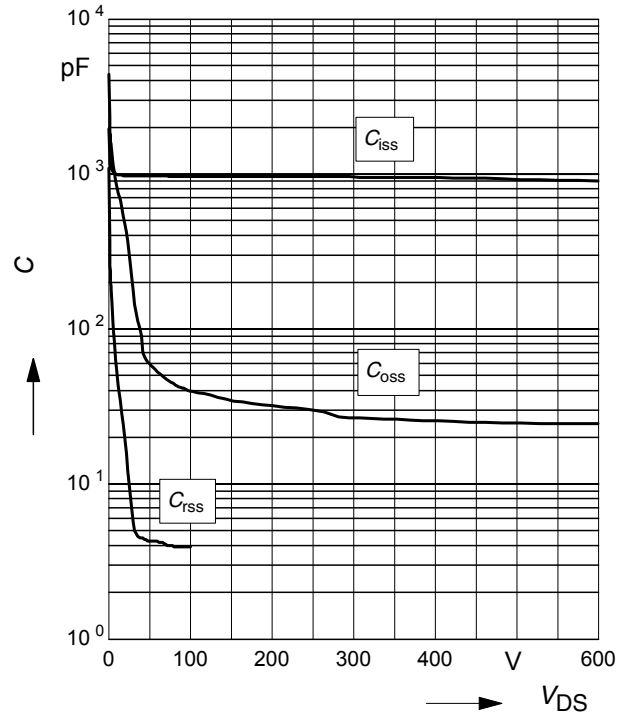
parameter:  $E_{AR}=0.5\text{mJ}$



**14 Typ. capacitances**

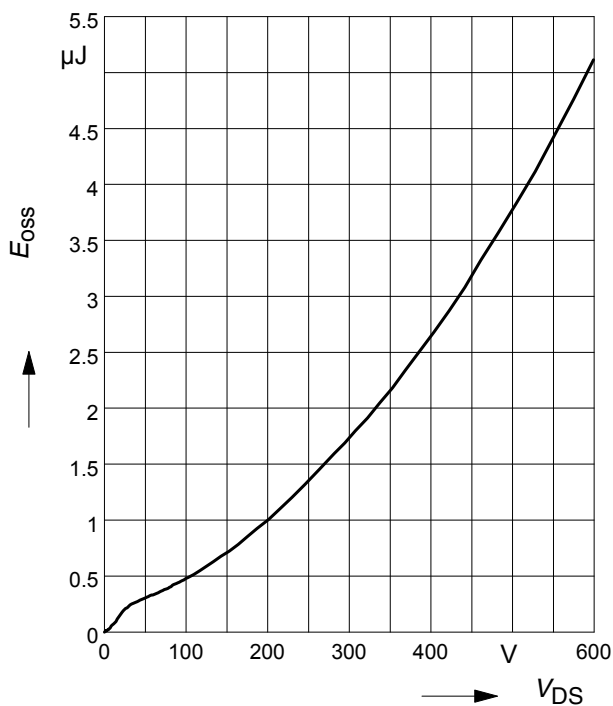
$$C = f(V_{DS})$$

parameter:  $V_{GS}=0\text{V}, f=1\text{ MHz}$



**15 Typ.  $C_{OSS}$  stored energy**

$$E_{OSS} = f(V_{DS})$$



**16 Typ. gate threshold voltage**

$$V_{GS(th)} = f(T_j)$$

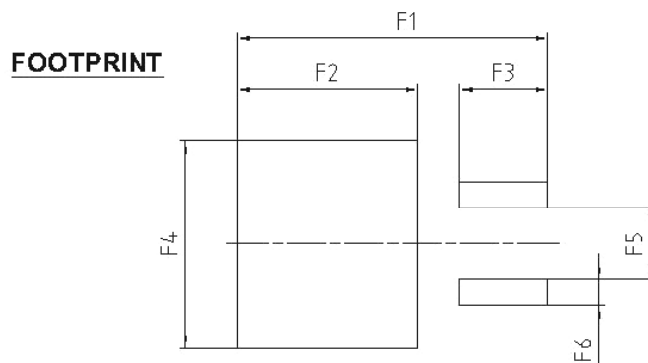
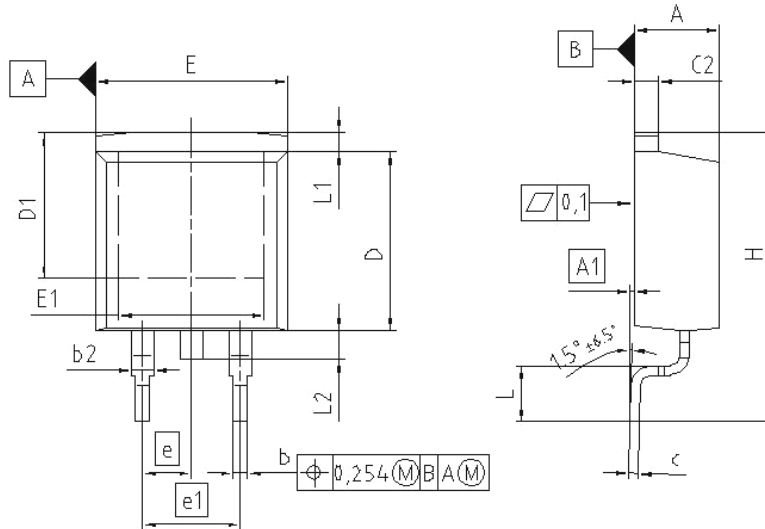
parameter:  $V_{GS} = V_{DS}$



Definition of diodes switching characteristics



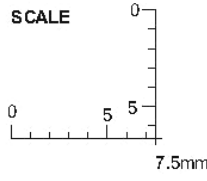
PG-TO263-3-2, PG-TO263-3-5, PG-TO263-3-22



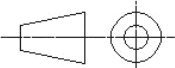
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.300	4.572	0.169	0.180
A1	0.000	0.254	0.000	0.010
b	0.650	0.850	0.026	0.033
b2	0.950	1.321	0.037	0.052
c	0.330	0.650	0.013	0.026
c2	0.170	1.400	0.046	0.055
D	8.509	9.450	0.335	0.372
D1	7.100	-	0.280	-
E	9.800	10.312	0.386	0.406
E1	6.500	-	0.256	-
e	2.540		0.100	
e1	5.080		0.200	
N	2		2	
H	14.605	15.875	0.575	0.625
L	2.200	3.000	0.087	0.118
L1	-	1.600	-	0.063
L2	1.000	1.778	0.039	0.070
F1	16.050	16.250	0.632	0.640
F2	9.300	9.500	0.366	0.374
F3	4.500	4.700	0.177	0.185
F4	10.700	10.900	0.421	0.429
F5	3.630	3.830	0.143	0.151
F6	1.100	1.300	0.043	0.051

**REFERENCE**  
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**SCALE**



**EUROPEAN PROJECTION**



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