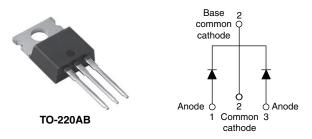


Vishay Semiconductors

Schottky Rectifier, 2 x 30 A



PRODUCT SUMMARY					
Package	TO-220AB				
I _{F(AV)}	2 x 30 A				
V _R	35 V, 40 V, 45 V				
V _F at I _F	0.53 V				
I _{RM} max.	250 mA at 125 °C				
T _J max.	150 °C				
Diode variation	Common cathode				
E _{AS}	20 mJ				

FEATURES

- 150 °C T_J operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



RoHS

- Guard ring for enhanced ruggedness and long
 term reliability
 COMPLIANT
 COMPLIANT
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition
 (-N3 only)

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	SYMBOL CHARACTERISTICS VALUES UNIT						
I _{F(AV)}	Rectangular waveform (per device)	60	A				
V _{RRM}		35 to 45	V				
I _{FRM}	T _C = 113 °C (per leg)	60	A				
I _{FSM}	t _p = 5 μs sine	1500					
V _F	30 A _{pk} , T _J = 125 °C	0.53	V				
TJ	Range	- 65 to 150	°C				

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS- 60CTQ035PbF	VS- 60CTQ035-N3	VS- 60CTQ040PbF	VS- 60CTQ040-N3	VS- 60CTQ045PbF	VS- 60CTQ045-N3	UNITS
Maximum DC reverse voltage	V _R							
Maximum working peak reverse voltage	V _{RWM}	35	35	40	40	45	45	V

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS		UNITS		
Maximum average per leg		50 % duty cycle at T _C = 113 °C	rootangular wayoform	30			
forward current per devi	e I _{F(AV)}	30% duty cycle at $T_{\rm C} = 113$ C	, rectangular wavelonn	60			
Peak repetitive forward current per leg	I _{FRM}	Rated V _R , square wave, 20 kHz, T _C = 113 °C		60	А		
Maximum peak one cycle non-repetitive	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1500			
surge current per leg	IFSM	10 ms sine or 6 ms rect. pulse	V_{RRM} applied	300			
Non-repetitive avalanche energy per leg	che energy per leg E_{AS} $T_J = 25 \text{ °C}, I_{AS} = 3 \text{ A}, L = 4.40 \text{ mH}$		20	mJ			
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to ze Frequency limited by T_J maxim		3	А		

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	NDITIONS	TYP.	MAX.	UNITS		
		30 A	T _{.1} = 25 °C	0.51	0.56			
Maximum forward voltage drop	V _{FM} ⁽¹⁾	60 A	1j=25 0	0.66	0.72	V		
Maximum forward voltage drop	VFM (*)	30 A	T _{.1} = 125 °C	0.48	0.53			
		60 A	1j = 125 C	0.68	0.75			
Maximum instantaneous reverse current	I _{RM}	T _J = 25 °C	Rated DC voltage	0.33	2	mA		
Maximum instantaneous reverse current		T _J = 125 °C	haled DC vollage	145	250	ШA		
Maximum junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		20	00	pF		
Typical series inductance	L _S	Measured from top of terminal to mounting plane			.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R		10	000	V/µs		

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,\,duty\,cycle$ < 2 $\,\%$

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction temperatur	e range	TJ		- 65 to 150	°C		
Maximum storage temperatur	e range	T _{Stg}		- 65 to 175	U		
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation	1.2	°C/W		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50			
Approvimate weight				2	g		
Approximate weight				0.07	oz.		
Mounting torque	minimum		Non-lubricated threads	6 (5)	kgf ⋅ cm		
Mounting torque maximum			Non-lubricated threads	12 (10)	(lbf ⋅ in)		
Marking device				60CT	Q035		
			Case style TO-220AB		Q040		
				60CT	Q045		

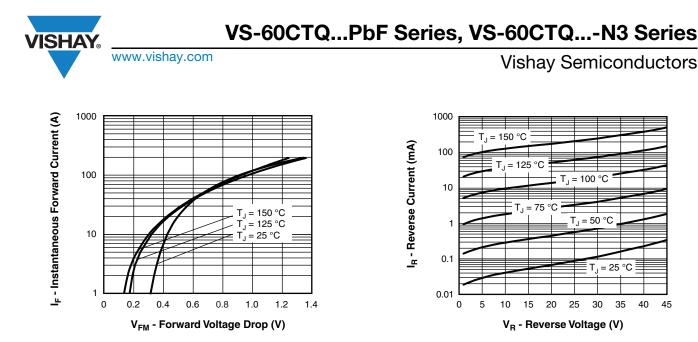


Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. **Reverse Voltage**

40

45

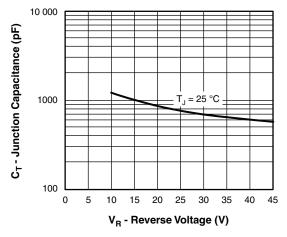
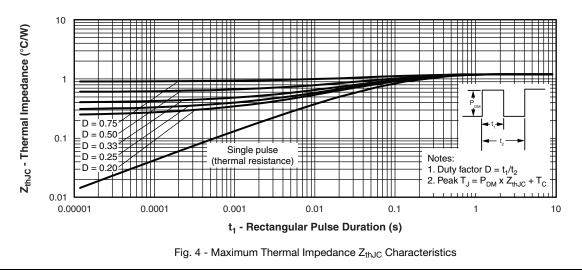


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

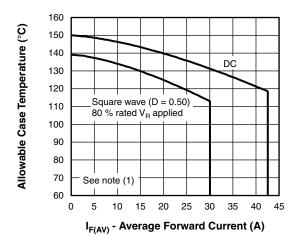


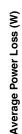
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VS-60CTQ...PbF Series, VS-60CTQ...-N3 Series

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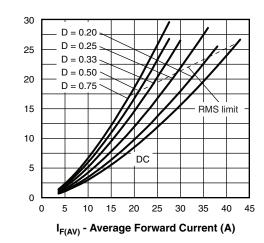
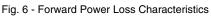


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



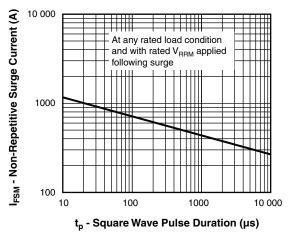


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
 - $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$



Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code	VS-	60	С	т	Q	045	PbF	
	1	2	3	4	5	6	7	
1	-	Vishay	Semico	nductor	s produc	ct		
2	-	Curren	Current rating (60 = 60 A)					
3	-	Circuit	Circuit configuration					
		C = Co	ommon d	athode				
4	-	Packag	ge					
		T = TO	-220					
5	-	Schottky "Q" series						
6	-	Voltage ratings					035 = 3 040 = 4	
7	-	Enviror	Environmental digit				045 = 4	
		• PbF	= Lead	(Pb)-fre	e and R	oHS co	mpliant	

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-60CTQ035PbF	50	1000	Antistatic plastic tube				
VS-60CTQ035-N3	50	1000	Antistatic plastic tube				
VS-60CTQ040PbF	50	1000	Antistatic plastic tube				
VS-60CTQ040-N3	50	1000	Antistatic plastic tube				
VS-60CTQ045PbF	50	1000	Antistatic plastic tube				
VS-60CTQ045-N3	50	1000	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95222					
Daut mandring information	TO-220AB PbF	www.vishay.com/doc?95225			
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028			

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Vishay Semiconductors

TO-220AB

DIMENSIONS in millimeters and inches





.ead	assignments

Diodes

1. - Anode/open 2. - Cathode 3. - Anode

SYMBOL	MILLIN	IETERS	INC	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- ⁽³⁾ Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left(4\right) }$ Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 10.51 0.414 10.11 0.398 3,6 Е E1 6.86 8.89 0.270 0.350 6 E2 0.76 0.030 7 --2.41 2.67 0.095 0.105 е 0.208 e1 4.88 5.28 0.192 H1 6.09 6.48 0.240 0.255 6,7 13.52 14.02 0.532 0.552 L L1 3.32 3.82 0.131 0.150 2 ØΡ 3.54 3.73 0.139 0.147 2.60 0.102 Q 3.00 0.118 90° to 93° 90° to 93° θ

Conforms to JEDEC outline TO-220AB

- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



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