Multilayer Ceramic Chip Capacitors

C5750C0G2J683K230KC



TDK item description C5750C0G2J683KT****

Tok item description C5750C02J665K1		<u> </u>
Applications	Commercial Grade	
Feature	Mid Mid Voltage (100 to 630V)	
Series	C5750 [EIA 2220]	1.0
Status	Production (Not Recommended for New Design)	Dmena

	Size		
Length(L)	5.70mm ±0.40mm		
Width(W)	5.00mm ±0.40mm		
Thickness(T)	2.30mm ±0.20mm		
Terminal Width(B)	0.20mm Min.		
Terminal Spacing(G)			
Recommended Land Pattern (PA)	4.10mm to 4.80mm		
Recommended Land Pattern (PB)	1.20mm to 1.40mm		
Recommended Land Pattern (PC)	4.00mm to 5.00mm		

Electrical Characteristics		
Capacitance	68nF ±10%	
Rated Voltage	630VDC	
Temperature Characteristic	C0G(0±30ppm/°C)	
Q (Min.)	1000	
Insulation Resistance (Min.)	7352ΜΩ	

Other	
Soldering Method	Reflow
AEC-Q200	No
Packing	Blister (Plastic)Taping [180mm Reel]
Package Quantity	500pcs

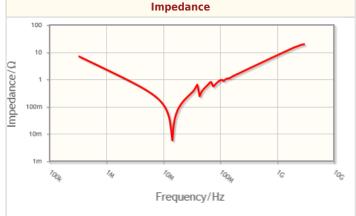
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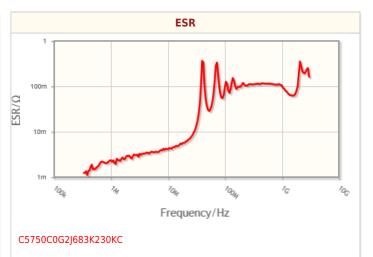
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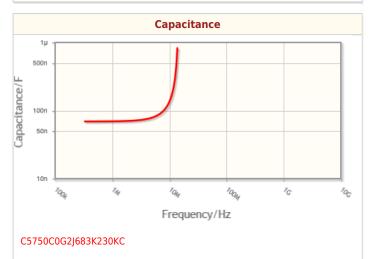


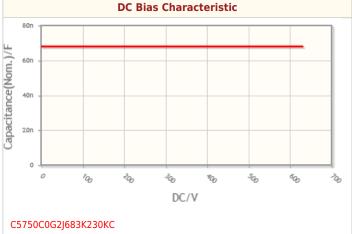


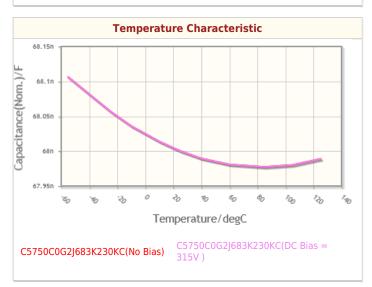
Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)



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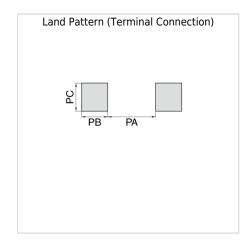
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Associated Images



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