

Maximum Ratings (@T_A = +25°C unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	T _A = +25 °C	I _D	5.8	A
		T _A = +70 °C		4.0	
Maximum Body Diode Forward Current (Note 6)			I _S	1.5	A
Pulsed Drain Current (10μs pulse, duty cycle = 1%)			I _{DM}	30	A

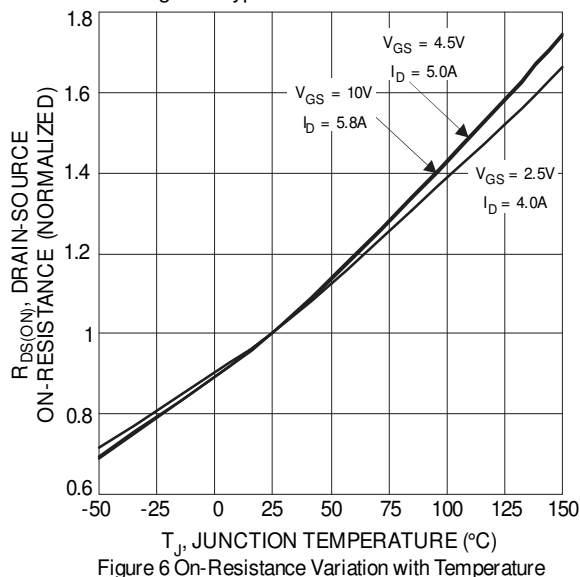
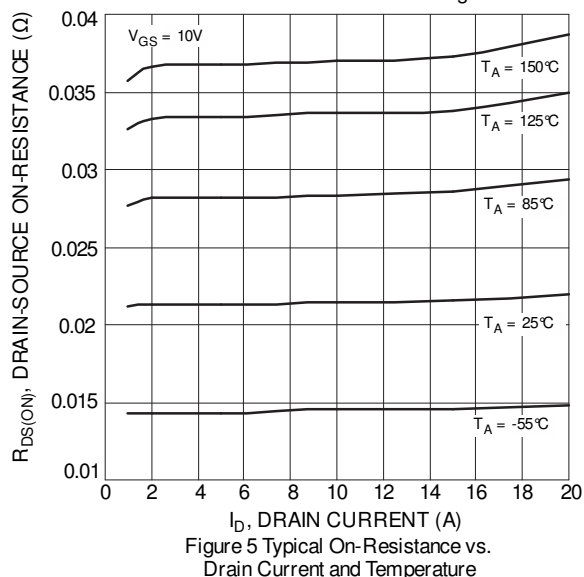
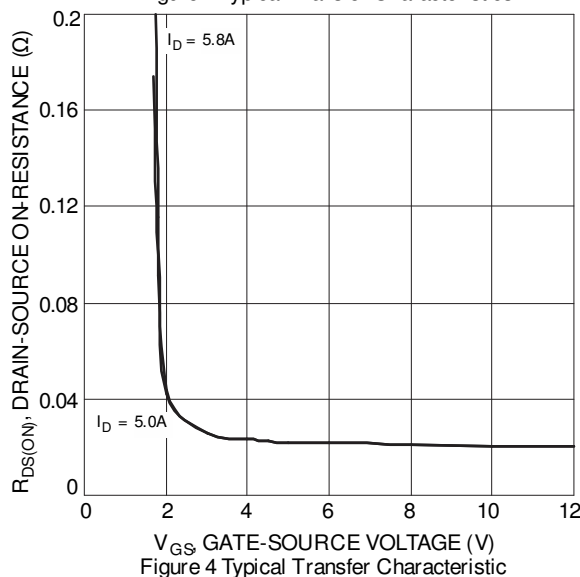
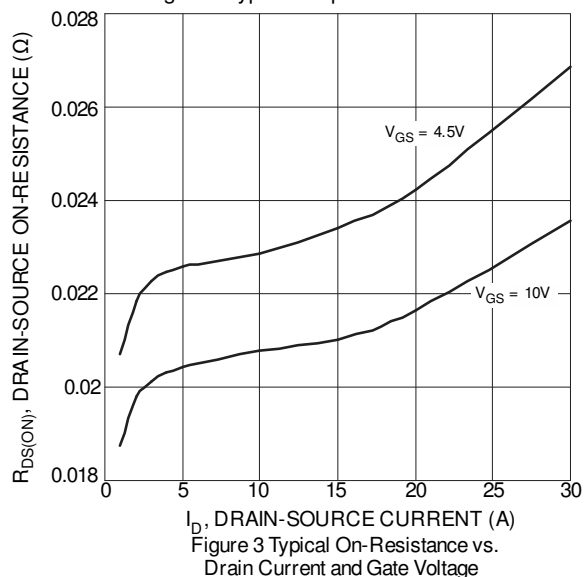
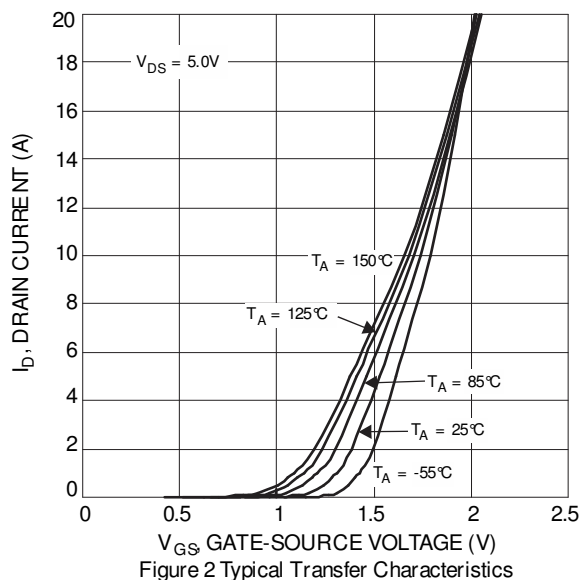
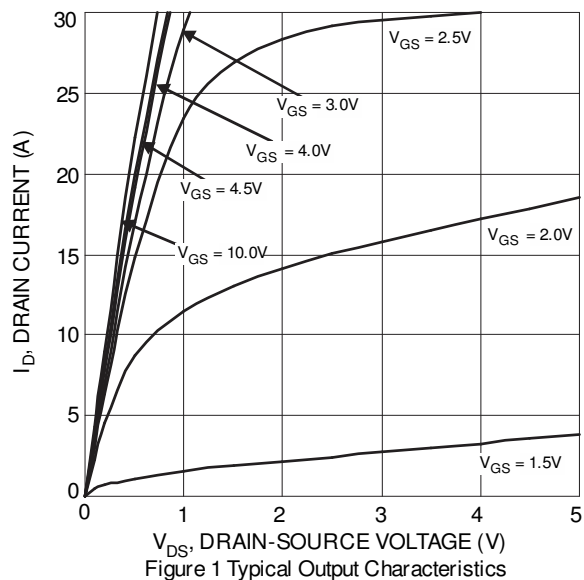
Thermal Characteristics

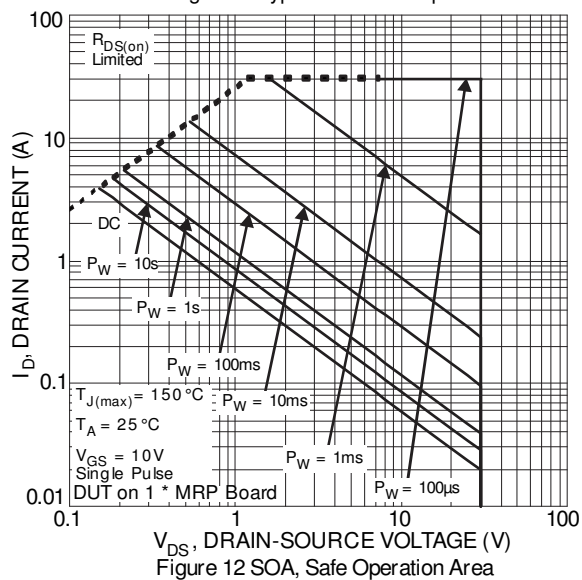
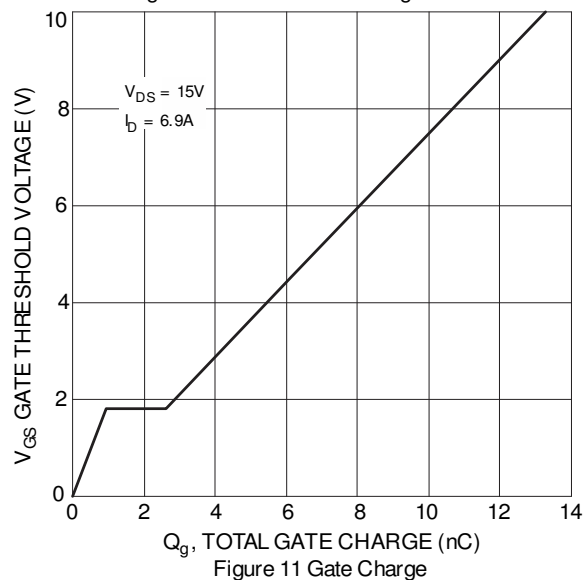
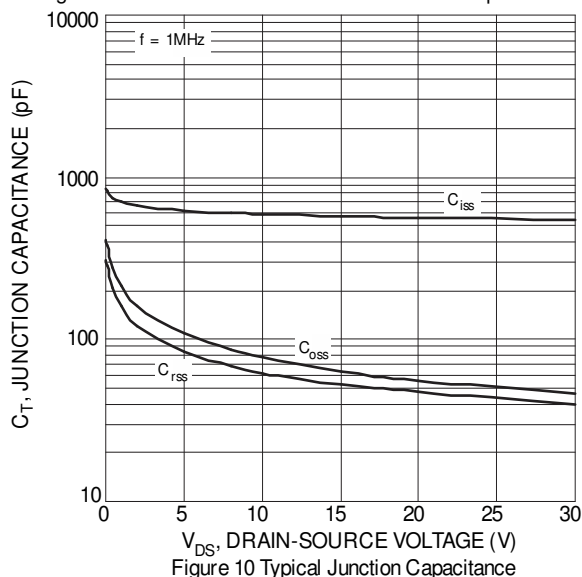
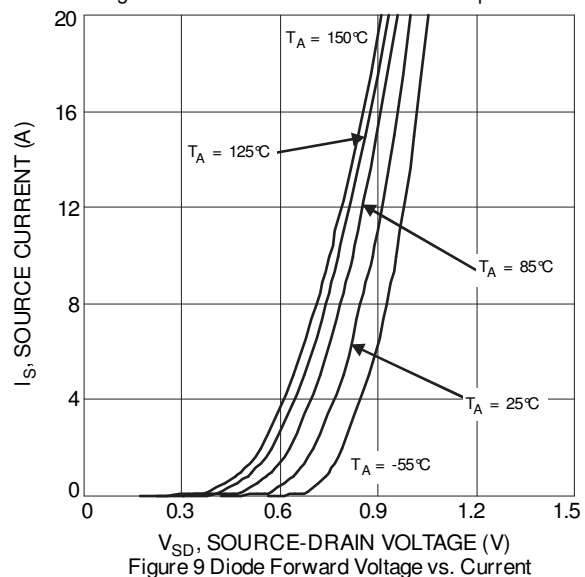
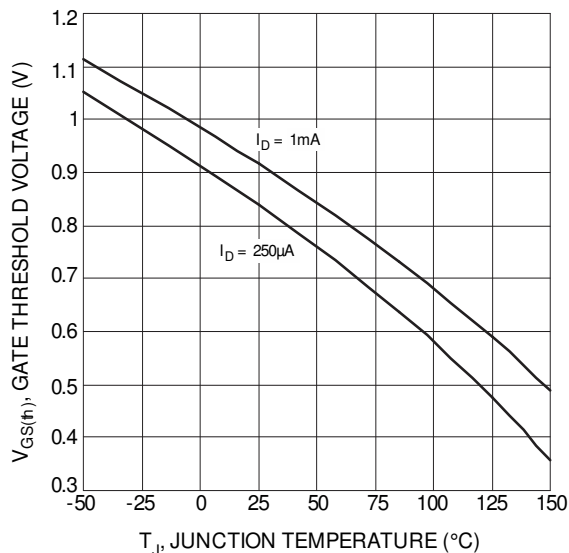
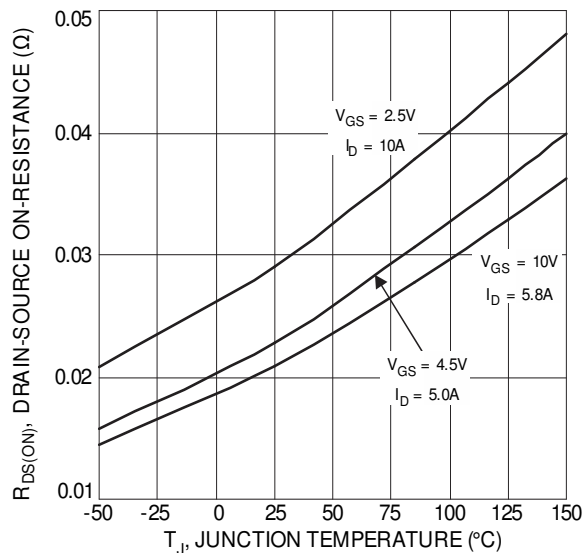
Characteristic		Symbol	Value	Unit
Power Dissipation (Note 5)		P _D	0.72	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{θJA}	171	°C/W
Power Dissipation (Note 6)		P _D	1.4	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{θJA}	93	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

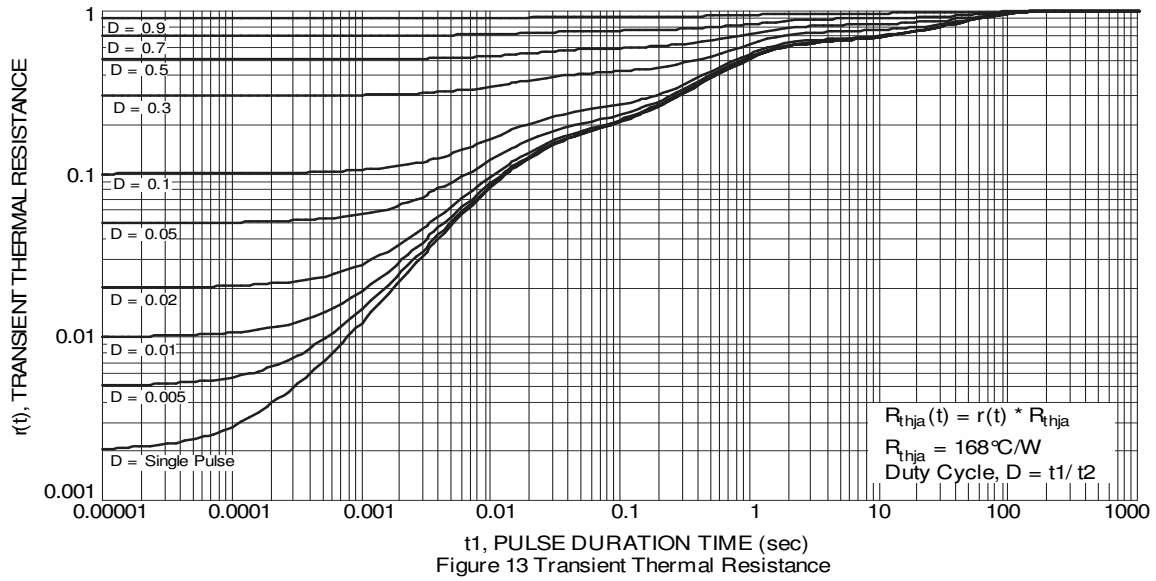
Electrical Characteristics (@T_A = +25°C unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±12V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	0.6	—	1.4	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	21	26.5	mΩ	V _{GS} = 10V, I _D = 5.8A
		—	23	32		V _{GS} = 4.5V, I _D = 5.0A
		—	29	48		V _{GS} = 2.5V, I _D = 4.0A
Diode Forward Voltage	V _{SD}	—	0.7	1.2	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	570	860	pF	V _{DS} = 15V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	63	95		
Reverse Transfer Capacitance	C _{rss}	—	53	80		
Gate Resistance	R _G	—	3.2	4.5	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz
Total Gate Charge (V _{GS} = 10V)	Q _g	—	13.3	20	nC	V _{DS} = 15V, I _D = 6.9A
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	6.1	8		
Gate-Source Charge	Q _{gs}	—	1.0	1.5		
Gate-Drain Charge	Q _{gd}	—	1.6	2.5		
Turn-On Delay Time	t _{D(on)}	—	1.5	2.4	nS	V _{GS} = 10V, V _{DD} = 15V, R _G = 3Ω, I _D = 6.9A
Turn-On Rise Time	t _r	—	3.3	5		
Turn-Off Delay Time	t _{D(off)}	—	13.9	22		
Turn-Off Fall Time	t _f	—	4.9	7		
Body Diode Reverse Recovery Time	t _{rr}	—	7.8	12	nS	I _S = 5A, dI/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q _{rr}	—	1.9	3	nC	I _S = 5A, dI/dt = 100A/μs

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

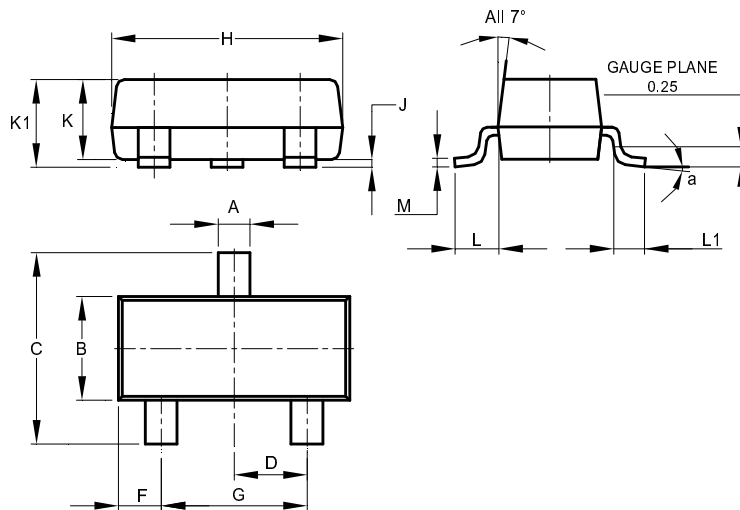






Package Outline Dimensions

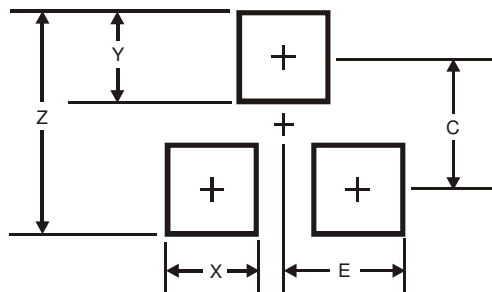
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	8°		
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35

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