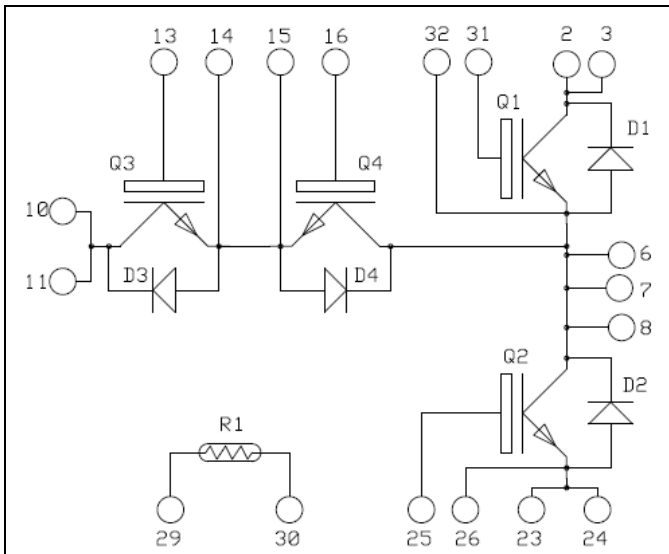


*Phase Leg & Dual Common Emitter
Power Module*

High speed Trench & Field Stop IGBT4 (Q1, Q2):
 $V_{CES} = 1200V$; $I_C = 80A$ @ $T_c = 80^\circ C$

Trench & Field Stop IGBT3 (Q3, Q4):



Application

- Uninterruptible Power Supplies

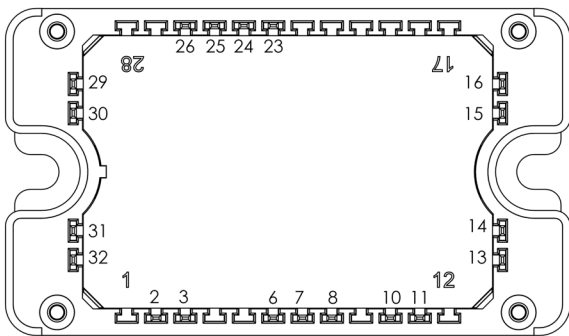
Features

- **Q1, Q2 High speed Trench + field Stop IGBT4**
 - Low voltage drop
 - Low tail current
- **Q3, Q4 Trench + field Stop IGBT3**
 - Low voltage drop
 - Low tail current
 - Switching frequency up to 20 kHz
- **SiC Schottky Diode (D3, D4)**
 - Zero reverse recovery
 - Zero forward recovery
 - Temperature Independent switching behavior
 - Positive temperature coefficient on VF

- Kelvin emitter for easy drive
- Very low stray inductance
- High level of integration
- Internal thermistor for temperature monitoring

Benefits

- Stable temperature behavior
- Very rugged
- Solderable terminals for easy PCB mounting
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Easy paralleling due to positive T_C of V_{CEsat}
- Low profile
- RoHS Compliant



All multiple inputs and outputs must be shorted together
 10/11 ; 23/24 ; 2/3 ; ...

All ratings @ $T_j = 25^\circ C$ unless otherwise specified

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.
 See application note APT0502 on www.microsemi.com

1. High speed Trench & Field Stop IGBT4 Phase Leg Q1&Q2 (per IGBT)
Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V _{CES}	Collector - Emitter Breakdown Voltage	1200	V
I _C	Continuous Collector Current	T _C = 25°C	150
		T _C = 80°C	80
I _{CM}	Pulsed Collector Current	T _C = 25°C	320
V _{GE}	Gate - Emitter Voltage	±20	V
P _D	Maximum Power Dissipation	500	W
RBSOA	Reverse Bias Safe Operating Area	T _j = 150°C	160A @ 1100V

Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit	
I _{CES}	Zero Gate Voltage Collector Current	V _{GE} = 0V, V _{CE} = 1200V			150	μA	
V _{CE(sat)}	Collector Emitter Saturation Voltage	V _{GE} = 15V I _C = 80A	T _j = 25°C	1.7	2.05	2.4	V
			T _j = 150°C		2.6		
V _{GE(th)}	Gate Threshold Voltage	V _{GE} = V _{CE} , I _C = 2 mA	5.0	5.8	6.5	V	
I _{GES}	Gate - Emitter Leakage Current	V _{GE} = 20V, V _{CE} = 0V			240	nA	

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C _{ies}	Input Capacitance	V _{GE} = 0V V _{CE} = 25V f = 1MHz		4600		pF
C _{oes}	Output Capacitance			300		
C _{res}	Reverse Transfer Capacitance			270		
Q _G	Gate charge	V _{GE} = 15V, I _C = 80A V _{CE} = 960V		370		nC
T _{d(on)}	Turn-on Delay Time	Inductive Switching (25°C) V _{GE} = ±15V V _{Bus} = 600V I _C = 80A R _G = 6Ω		30		ns
T _r	Rise Time			57		
T _{d(off)}	Turn-off Delay Time			290		
T _f	Fall Time			16		
T _{d(on)}	Turn-on Delay Time	Inductive Switching (150°C) V _{GE} = ±15V V _{Bus} = 600V I _C = 80A R _G = 6Ω		30		ns
T _r	Rise Time			49		
T _{d(off)}	Turn-off Delay Time			366		
T _f	Fall Time			48		
E _{on}	Turn on Energy	V _{GE} = ±15V V _{Bus} = 600V I _C = 80A R _G = 6Ω	T _i = 25°C	6.4		mJ
			T _i = 150°C	7.5		
E _{off}	Turn off Energy	I _C = 80A R _G = 6Ω	T _i = 25°C	2.4		
			T _i = 150°C	4.5		
I _{sc}	Short Circuit data	V _{GE} ≤ 15V ; V _{Bus} = 600V t _p ≤ 10μs ; T _i = 150°C		300		A
R _{thJC}	Junction to Case Thermal Resistance				0.3	°C/W

Diode ratings and characteristics (D1 & D2) (per diode)

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>		<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
V _{RRM}	Maximum Peak Repetitive Reverse Voltage			1200			V
I _{RM}	Maximum Reverse Leakage Current	V _R =1200V				100	μA
I _F	DC Forward Current	T _c = 80°C			30		A
V _F	Diode Forward Voltage	I _F = 30A			2.6	3.1	V
		I _F = 60A			3.2		
		I _F = 30A	T _j = 125°C		1.8		
t _{rr}	Reverse Recovery Time	I _F = 30A V _R = 800V di/dt = 200A/μs	T _j = 25°C		300		ns
			T _j = 125°C		380		
Q _{rr}	Reverse Recovery Charge	I _F = 30A V _R = 800V di/dt = 200A/μs	T _j = 25°C		360		nC
			T _j = 125°C		1700		
R _{thJC}	Junction to Case Thermal Resistance					1.2	°C/W

2. Trench & Field Stop IGBT3 Dual common emitter Q3&Q4 (per IGBT)
Absolute maximum ratings

<i>Symbol</i>	<i>Parameter</i>	<i>Max ratings</i>	<i>Unit</i>
V _{CES}	Collector - Emitter Breakdown Voltage	600	V
I _C	Continuous Collector Current	T _C = 25°C	100
		T _C = 80°C	75
I _{CM}	Pulsed Collector Current	T _C = 25°C	140
V _{GE}	Gate - Emitter Voltage	±20	V
P _D	Maximum Power Dissipation	T _C = 25°C	250
RBSOA	Reverse Bias Safe Operating Area	T _j = 150°C	150A @ 550V

Electrical Characteristics

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
I _{CES}	Zero Gate Voltage Collector Current	V _{GE} = 0V, V _{CE} = 600V			250	μA
V _{CE(sat)}	Collector Emitter Saturation Voltage	V _{GE} = 15V I _C = 75A	T _j = 25°C		1.5	1.9
			T _j = 150°C		1.7	
V _{GE(th)}	Gate Threshold Voltage	V _{GE} = V _{CE} , I _C = 600μA	5.0	5.8	6.5	V
I _{GES}	Gate - Emitter Leakage Current	V _{GE} = 20V, V _{CE} = 0V			600	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C _{ies}	Input Capacitance	V _{GE} = 0V V _{CE} = 25V f = 1MHz		4620		pF
C _{oes}	Output Capacitance			300		
C _{res}	Reverse Transfer Capacitance			140		
Q _G	Gate charge	V _{GE} = ±15V, I _C = 75A V _{CE} = 300V		0.8		μC
T _{d(on)}	Turn-on Delay Time	Inductive Switching (25°C) V _{GE} = ±15V V _{Bus} = 300V I _C = 75A R _G = 4.7Ω		110		ns
T _r	Rise Time			45		
T _{d(off)}	Turn-off Delay Time			200		
T _f	Fall Time			40		
T _{d(on)}	Turn-on Delay Time	Inductive Switching (150°C) V _{GE} = ±15V V _{Bus} = 300V I _C = 75A R _G = 4.7Ω		120		ns
T _r	Rise Time			50		
T _{d(off)}	Turn-off Delay Time			250		
T _f	Fall Time			60		
E _{on}	Turn-on Switching Energy	V _{GE} = ±15V V _{Bus} = 300V	T _i = 25°C	0.21		mJ
			T _i = 150°C	0.36		
E _{off}	Turn-off Switching Energy	I _C = 75A R _G = 4.7Ω	T _i = 25°C	2.2		mJ
			T _i = 150°C	2.6		
I _{sc}	Short Circuit data	V _{GE} ≤ 15V ; V _{Bus} = 360V t _p ≤ 6μs ; T _i = 150°C		380		A
R _{thJC}	Junction to Case Thermal Resistance				0.60	°C/W

SiC diode ratings and characteristics (D3 & D4) (per diode)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V _{RRM}	Maximum Peak Repetitive Reverse Voltage		600			V
I _{RM}	Maximum Reverse Leakage Current	V _R = 600V	T _i = 25°C	30	180	μA
			T _i = 175°C	60	900	
I _F	DC Forward Current	T _c = 100°C		30		A
V _F	Diode Forward Voltage	I _F = 30A	T _i = 25°C	1.6	1.8	V
			T _i = 175°C	2	2.4	
Q _C	Total Capacitive Charge	I _F = 30A, V _R = 600V di/dt = 1000A/μs		84		nC
C	Total Capacitance	f = 1MHz, V _R = 200V		195		pF
		f = 1MHz, V _R = 400V		150		
R _{thJC}	Junction to Case Thermal Resistance				1	°C/W

3. Thermal & Package characteristics

Temperature sensor NTC (see application note APT0406 on www.microsemi.com).

Symbol	Characteristic	Min	Typ	Max	Unit
R ₂₅	Resistance @ 25°C		22		kΩ
ΔR ₂₅ /R ₂₅				5	%
B _{25/100}	T ₂₅ = 298.15 K		3980		K
ΔB/B				3	%

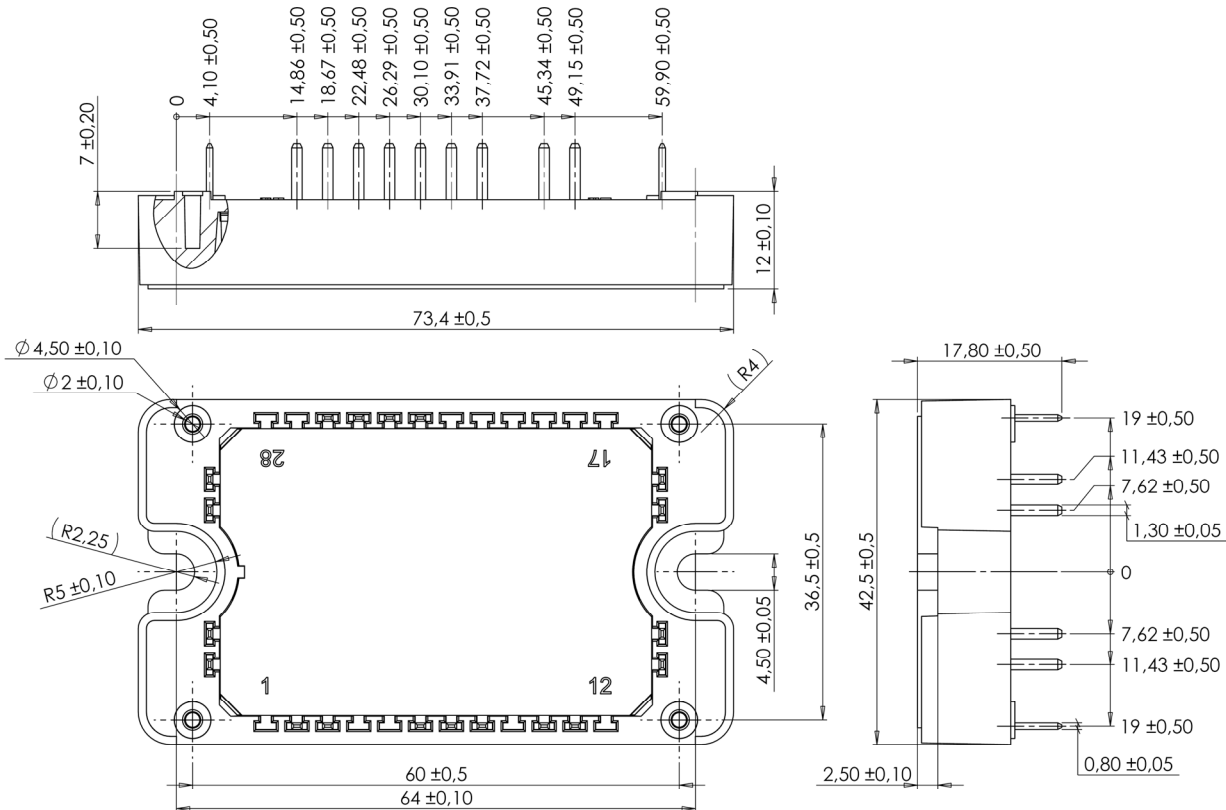
$$R_T = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$

T: Thermistor temperature
 R_T: Thermistor value at T

Thermal and package characteristics

Symbol	Characteristic	Min	Typ	Max	Unit		
V _{ISOL}	RMS Isolation Voltage, any terminal to case t=1 min, 50/60Hz	4000			V		
T _J	Operating junction temperature range	-40		175	°C		
T _{STG}	Storage Temperature Range	-40		125			
T _C	Operating Case Temperature	-40		100			
Torque	Mounting torque	To heatsink		M4	2	3	N.m
Wt	Package Weight					110	g

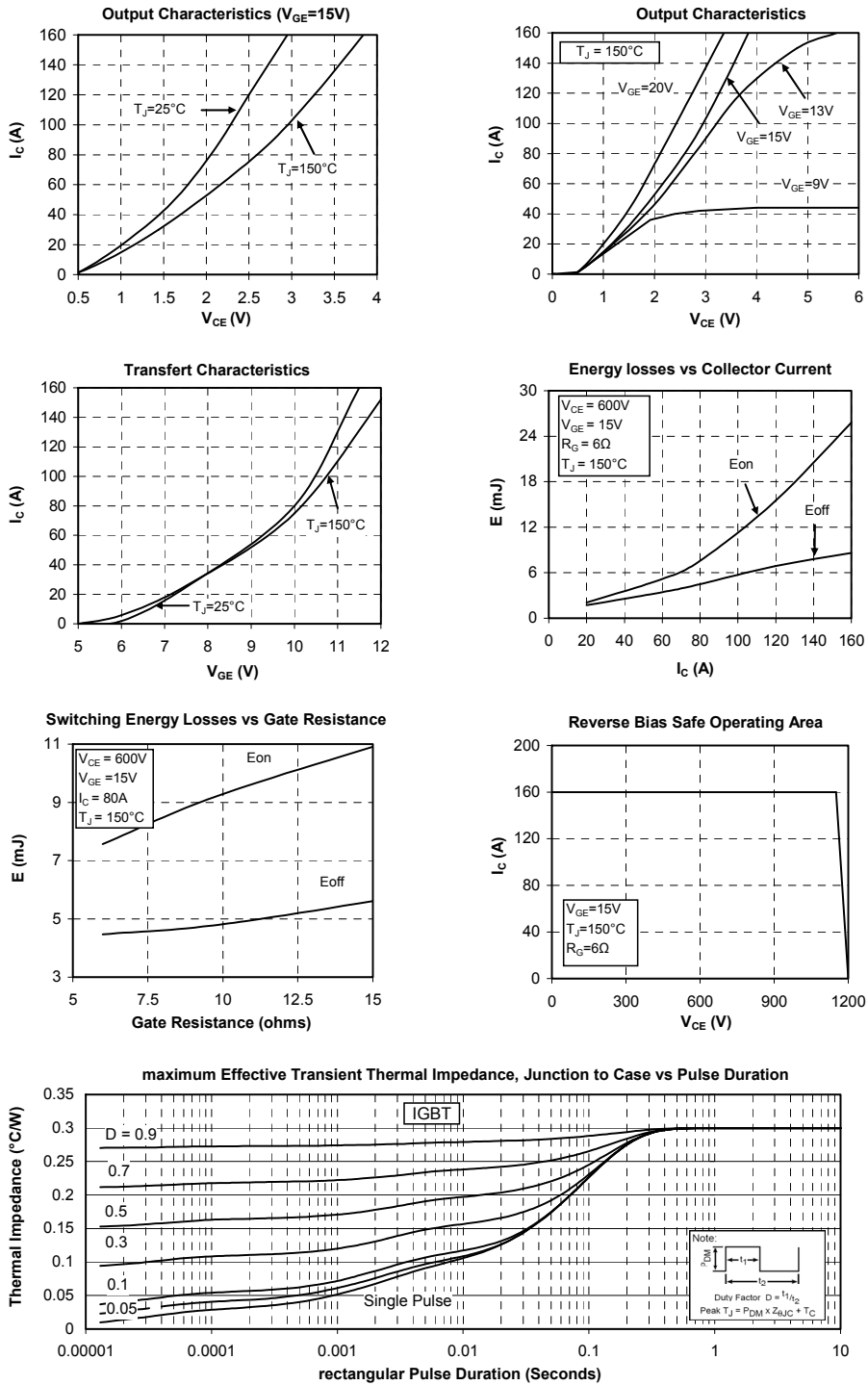
SP3F Package outline (dimensions in mm)

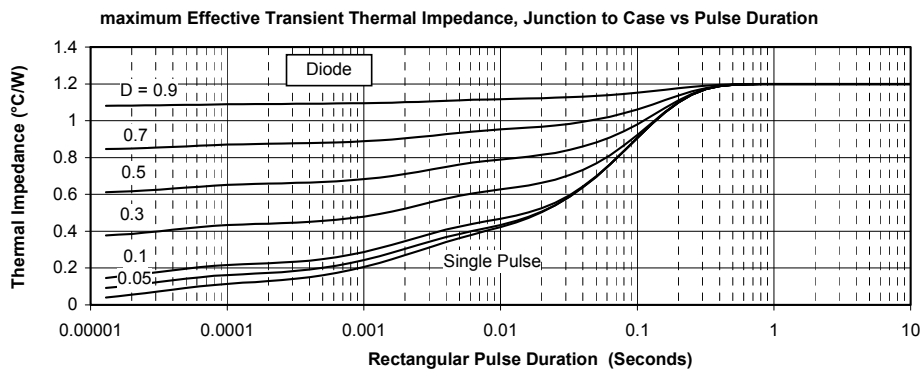
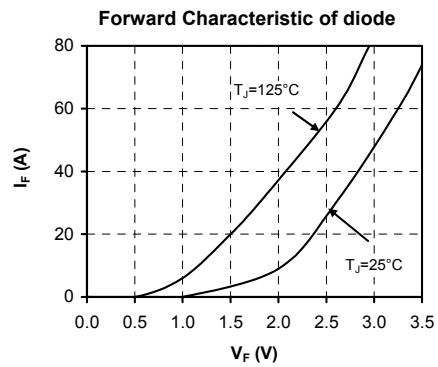
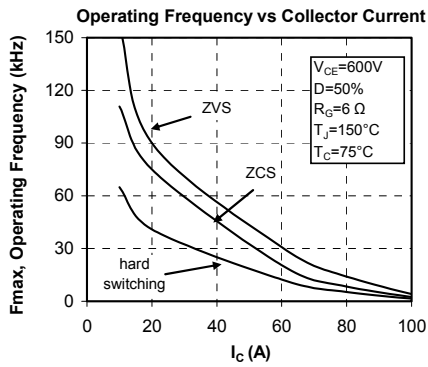


See application note 1906 - Mounting Instructions for SP3F Power Modules on www.microsemi.com

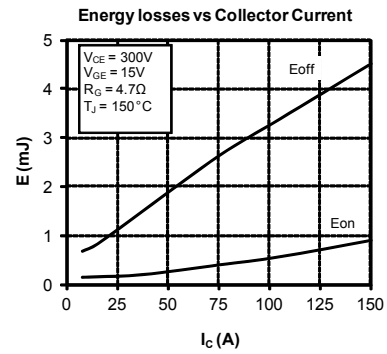
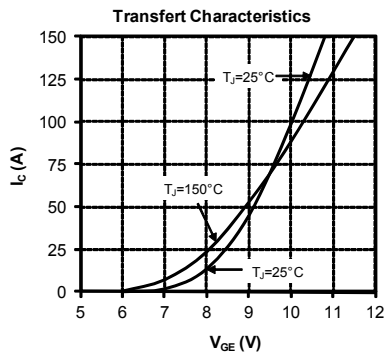
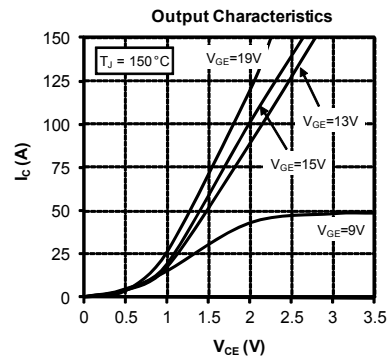
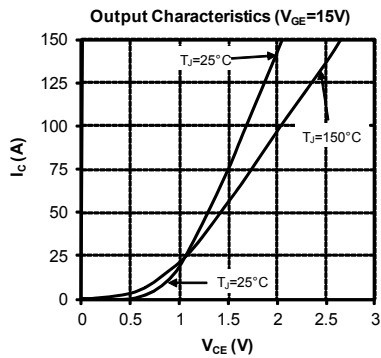
4. Typical performance curve

Q1, Q2 High speed Trench + field stop IGBT4 + CR1 & CR2 diode characteristics

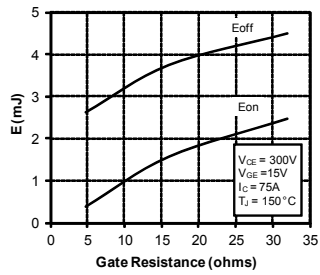




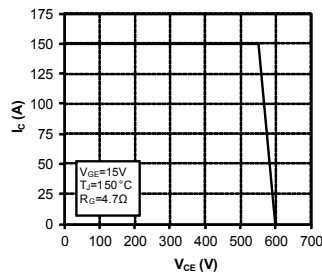
Q3, Q4 Trench + field stop IGBT3



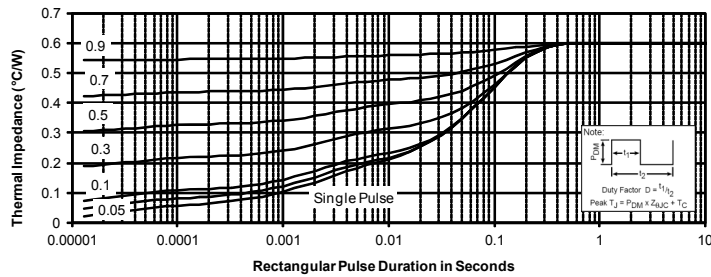
Switching Energy Losses vs Gate Resistance



Reverse Bias Safe Operating Area

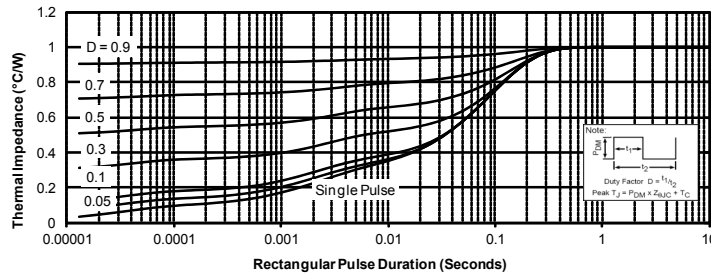


maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration

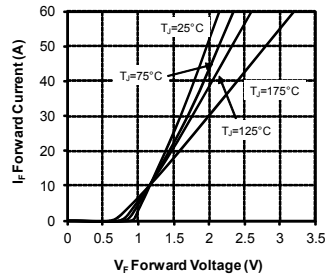


CR3 & CR4 SiC diode characteristics

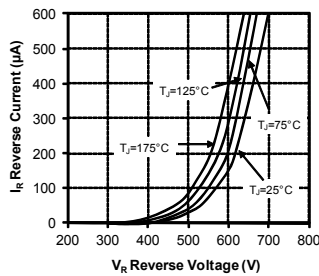
Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration



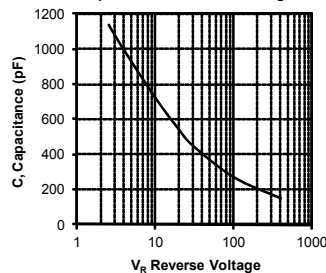
Forward Characteristics



Reverse Characteristics



Capacitance vs. Reverse Voltage



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