Diode Semiconductor Korea HER2010C---HER2060C

HIGH EFFICIENCY RECTIFIERS

VOLTAGE RANGE: 100 --- 600 V

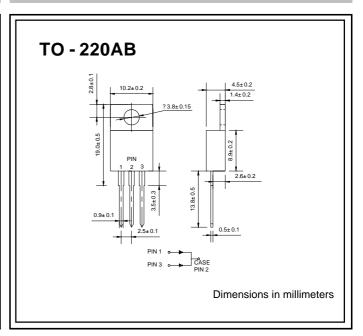
CURRENT: 20 A

FEATURES

- ♦ Low cost
- Low forward voltage drop
- High current capability
- Easily cleaned with alcohol, Isopropanol and similar solvents

MECHANICAL DATA

- MIL-STD-202, Method 208
- Polarity: As marked
- ♦ Weight: 0.071 ounces,2.006grams
- ♦ Mounting position: Any



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25℃ ambient temperature unless otherwise specified.

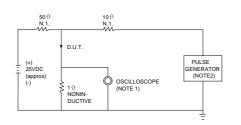
Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate by 20%.

		HER 2010C	HER 2020C	HER 2040C	HER 2060C	UNITS
Maximum recurrent peak reverse voltage	V_{RRM}	100	200	400	600	V
Maximum RMS voltage	V_{RMS}	70	140	280	420	V
Maximum DC blocking voltage	V_{DC}	100	200	400	600	V
Maximum average forw ard rectified current	J _E (A)A 20					А
@T _c =75 °C	I _{F(AV)}	20				
Peak forward surge current						
8.3ms single half-sine-wave	I_{FSM}	200				Α
superimposed on rated load @T _J =125℃						
Maximum instantaneous forward voltage @ 10A	V _F	1.0 1.3 1		1.7	V	
Maximum reverse current @T _c =25°C	10					υ. Δ
at rated DC blocking voltage @T _c =100°C	I _R	^R 150				μА
Maximum reverse recovery time (Note1)	t _{rr}	50 100			ns	
Typical junction capacitance (Note2)	CJ	40				pF
Typical thermal resistance (Note3)	$R_{\theta JC}$	2.5				.C.W.
Operating junction temperature range	TJ	- 55 + 150			${\mathbb C}$	
Storage temperature range	T _{STG}	- 55 + 150				$^{\circ}$

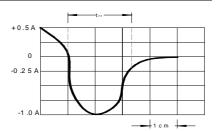
NOTE: 1. Measured with I_F =0.5A, I_R =1A, I_{rr} =0.25A.

- 2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.
- 3. Thermal resistance junction to case.

FIG.1 -- TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC



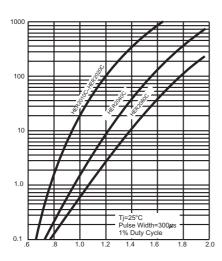
NOTES:1.RISE TIME = 7ns MAX.INPUT IMPEDANCE = 1M Ω .22pF. 2.RISE TIME =10ns MAX.SOURCE IMPEDANCE=50 Ω .



SET TIME BASE FOR 25 ns/cm

FIG.2 -- TYPICAL FORWARD CHARACTERISTIC





INSTANTANEOUS FORWARD VOLTAGE, VOLTS

FIG.3 -- FORWARD DERATING CURVE



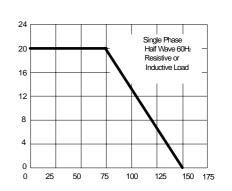
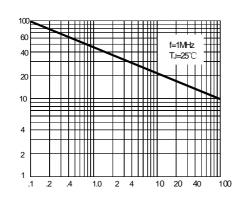


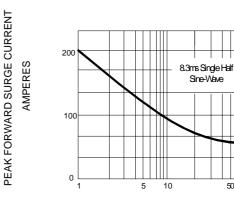
FIG.4 -- TYPICAL JUNCTION CAPACITANCE

JUNCTION CAPACITANCE, pF



REVERSE VOLTAGE, VOLTS

FIG.5 -- PEAK FORWARD SURGE CURRENT



NUMBER OF CYCLES AT 60Hz