



Features

- μPower Operation (15 μW typical at 25°C)
- Omnipolar (switches with N or S pole)
- 2.5V to 5.5V Operation
- CMOS Push-Pull Output
- Ultra Low Offset Canceling Amplifiers Provide Sensitive, Accurate, Stable Switching Points and Immunity to Mechanical Stress
- Solid State Reliability
- Operating Temperature Range: -40°C to +85°C
- RoHS Compliant TSOT-23 3-Lead Package

Applications

- Handheld Portable Devices
- White Goods
- Automotive - Body Systems
- Security Systems
- High Reliability Reed Switch Replacement

Description

The MX887P integrated Hall-Effect switch targets the requirements of low-power portable devices with battery operating voltages from 2.5V to 5.5V. On-chip power management circuitry reduces the effective average current to just 5μA at $V_{SUPPLY} = 3V_{DC}$.

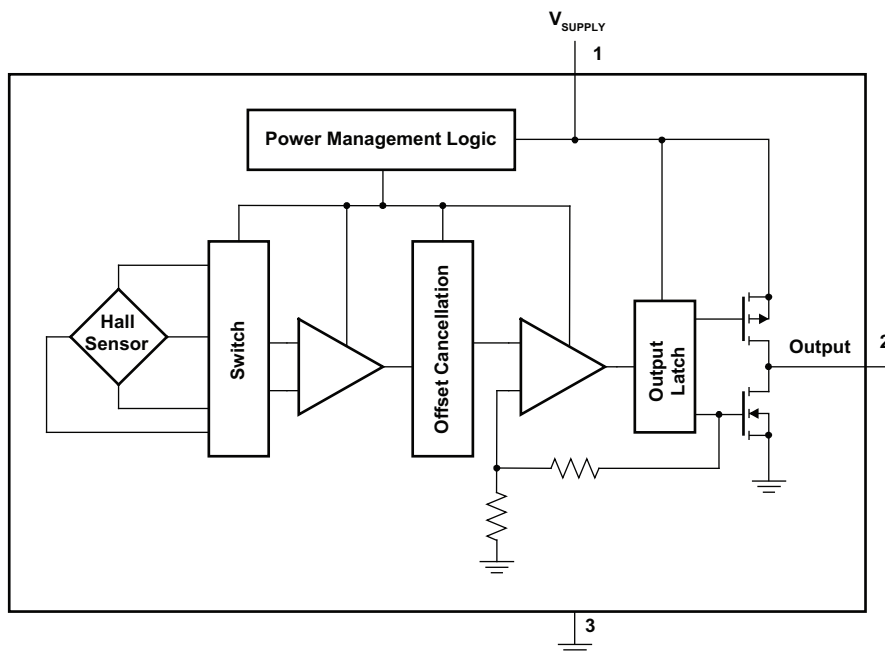
The switch output will transition to the Ground potential when either a north or south magnetic pole is applied. The removal of a magnetic field will transition the switch to the V_{SUPPLY} potential. Emulating the behavior of a traditional reed switch, together with the advantages of high integration and solid state reliability, makes the MX887P an ideal replacement in low-power portable device applications.



Ordering Information

Part	Description
MX887PHTTR	TSOT-23 3L Tape & Reel (3000/Reel)

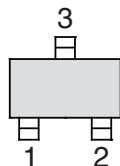
Figure 1. Functional Block Diagram



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1 Specifications

1.1 Package Pinout



1.2 Pin Description

Pin#	Name	Description
1	V _{SUPPLY}	2.5V to 5.5V
2	OUT	Push-Pull CMOS Output
3	GROUND	Ground

1.3 Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Units
Supply Voltage	V _{SUPPLY}	-0.5	6	V _{DC}
Output Voltage	V _{OUT}	-0.5	6	V
Magnetic Flux Density	B	-	Unlimited	G
Output Current	-	-	5	mA
Junction Temperature	T _J	-	150	°C
Operating Ambient Temperature	T _A	-40	85	
Storage Temperature	T _{STG}	-40	150	

Absolute maximum electrical ratings are at 25°C.

Absolute maximum ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional

operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

1.4 Electrical Characteristics

Over operating voltage and temperature range unless otherwise specified.

Parameter	Conditions	Symbol	Minimum	Typical	Maximum	Units
Supply Voltage Range	Operating	V _{SUPPLY}	2.5	-	5.5	V _{DC}
Output Leakage Current	V _{OUT} =5.5V, B _{RPN} < B < B _{RPS}	I _{LKG}	-	-	1	μA
Output On Voltage	I _{OUT} =1mA, V _{DD} =3.0V	V _{OUT}	-	100	300	mV
Awake Time	-	-	-	-	90	μs
Period	-	-	-	-	90	ms
Duty Cycle	-	-	-	0.1	-	%
Supply Current	Awake (enabled)	I _{SUPPLY}	-	-	2	mA
	Asleep (disabled)		-	-	8	μA
	Average (Calculated)		-	5	15	μA

Notes: 1. B_{OPX} = operating point (output turns ON); B_{RPX} = release point (output turns OFF).
2. Typical data is at 25°C and V_{SUPPLY} = 3V.

1.5 ESD Rating

ESD Rating (Human Body Model)
2000V

1.6 Magnetic Characteristics

Over operating voltage and temperature range unless otherwise specified.

Parameter	Conditions	Symbol	Minimum	Typical	Maximum	Units
Operating Points						
Due to North Pole Applied	North Pole to Branded Side	B _{OPN}	-60	-	-	G
Due to South Pole Applied	South Pole to Branded Side	B _{OPS}	-	-	60	
Release Points						
Due to North Pole Applied	North Pole to Branded Side	B _{RPN}	-	-	-6	G
Due to South Pole Applied	South Pole to Branded Side	B _{RPS}	6	-	-	
Hysteresis	B _{OPX} - B _{RPX}	B _{HYS}	-	5	-	G

- Notes:
1. As used here, negative flux densities are defined as less than zero (algebraic convention); -50G<+10G.
 2. B_{OPX} = operating point (output turns ON); B_{RPX} = release point (output turns OFF).
 3. Typical data is at 25°C and V_{SUPPLY} = 3V.

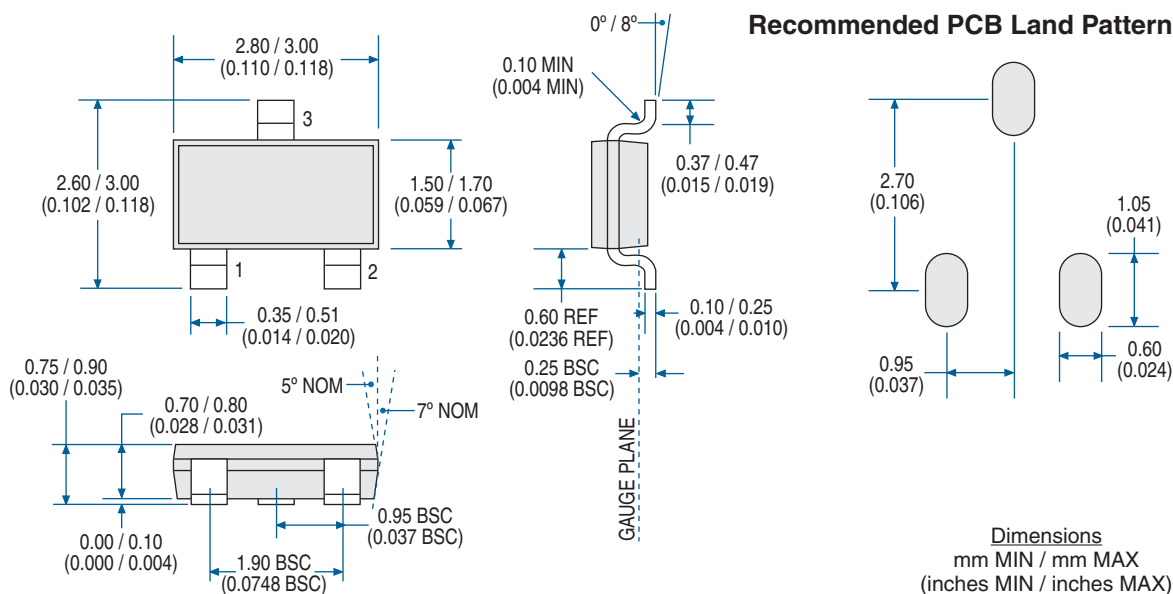
2 Circuit Description

The MX887P μ Power Hall-Effect Switch consists of a Hall element, small-signal amplifier, latch, and CMOS push-pull output. Offset cancellation circuitry rejects errors in signal stages and the influence of mechanical stress on the Hall element. This technique, together with a precision threshold generator and comparator, produces highly accurate magnetic switch points. The Hall element is activated for a small fraction of an operating cycle, then latched in that sample state for the remainder of the period. By using this technique, very low power consumption is achieved.

3 Manufacturing Information

3.1 Mechanical Dimensions

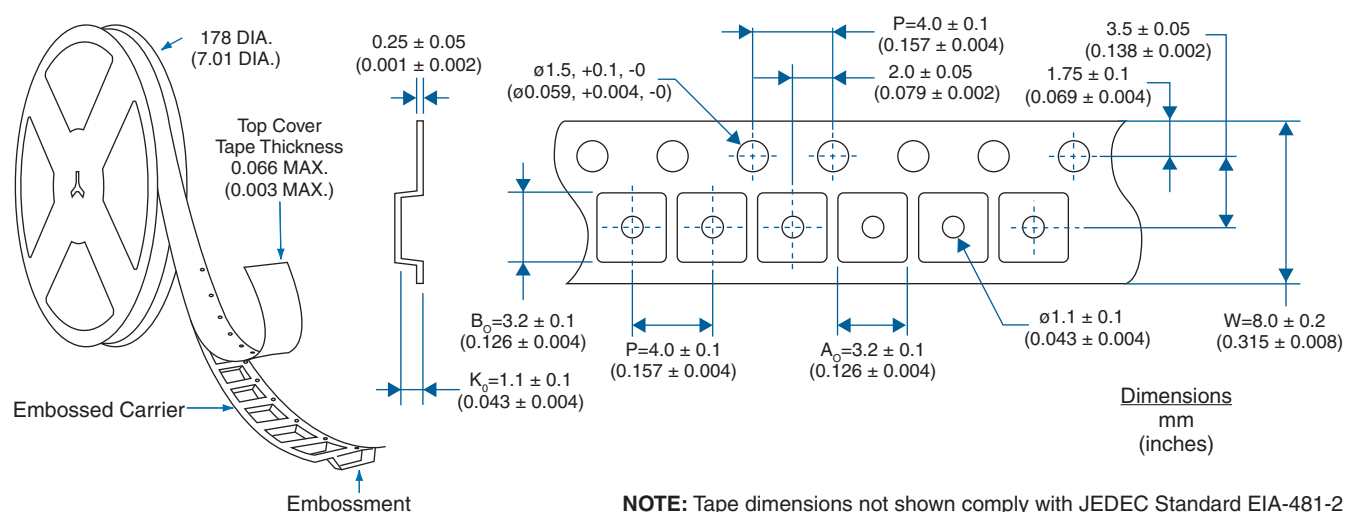
3.1.1 TSOT23 3-Lead Package



Notes: (Unless otherwise specified)

1. Dimension "D" does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, and gate burrs shall not exceed 0.10mm (0.004 inches) per side.
2. Dimension "E" does not include inter-lead flash or protrusions. Inter-lead flash and protrusions shall not exceed 0.15mm (0.006 inches) per side.
3. Package top may be smaller than package bottom. Dimensions "D" and "E1" are determined at the outermost extreme of the plastic body excluding mold flash, tie bar burrs, gate burrs, and interlead flash, but including any mismatch between top and bottom of the plastic body.

3.1.2 Tape & Reel Packaging



NOTE: Tape dimensions not shown comply with JEDEC Standard EIA-481-2

3.2 Soldering

For proper assembly, the component must be processed in accordance with the current revision of IPC/JEDEC standard J-STD-020. Failure to follow the recommended guidelines may cause permanent damage to the device resulting in impaired performance and/or a reduced lifetime expectancy.

3.3 Washing

Clare does not recommend ultrasonic cleaning or the use of chlorinated hydrocarbons.



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