

## STM320518-SK/IAR

# IAR™ starter kit for STM32F0 microcontrollers

Data brief

#### **Features**

- Microcontroller
  - STM32F051R8 (Cortex-M0), frequency up to 48 MHz, 8 Kbytes of SRAM, 64 Kbytes of Flash memory
- User interface
  - 4 status LEDs
  - Audio (headphone jack)
  - 2 user buttons, 1 reset button
  - 6 capacitive buttons
  - Small color LCD
  - Light and distance sensors
- Debug interface
  - JTAG/SWD 20-pin connector

- 20-pin trace connector
- Communication interface
  - 1 UART connector
  - HDMI-CEC connectors
- Power functions
  - Multiple power supply options: JTAG/SWD, USB, external power jack and trace connector
  - Power supply LED
  - Comparator
- Additional features
  - Potentiometer
  - 3-axis accelerometer
  - RoHS compliant





## **Description**

The IAR KickStart Kit<sup>™</sup> is a complete, cost-effective solution to start an application development and evaluate the STMicroelectronics STM32 F0 series ARM Cortex-M0 corebased microcontrollers.

The IAR KickStart Kit for STM32 F0 series contains all the necessary hardware and software and allows you to design, start developing, integrate and test your applications out of the box: the evaluation board with STM32F051R8 MCU, the IAR Embedded Workbench for ARM, 16\_Kbyte Kickstart and 30-day evaluation editions, the IAR visualSTATE 20-state evaluation edition, the Embedded IAR J-Link Lite debug probe for STMicroelectronics Cortex-M devices, and example applications.

The IAR starter kits are available for a full range of ST ARM core-based microcontrollers.

### **Ordering information**

The IAR KickStart Kit can be ordered from IAR or from your nearest ST distributor or sales office.

ST order code: STM320518-SK/IAR

For more information and complete documentation, please refer to the IAR web site, www.iar.com or the STMicroelectronics microcontroller support site on www.st.com.

#### **Revision history**

Table 1. Document revision history

Date	Revision	Changes
11-Sep-2012	1	Initial release.
24-Sep-2012	2	Updated <i>Description</i> .

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