TOSHIBA INSULATED GATE BIPOLAR TRANSISTOR SILICON N CHANNEL IGBT

GT15J103(SM)

HIGH POWER SWITCHING APPLICATIONS

MOTOR CONTROL APPLICATIONS

• High Input Impedance

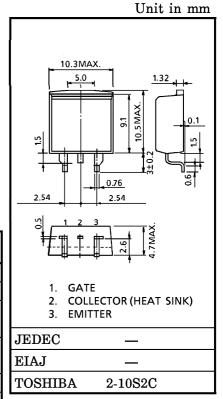
• High Speed : $t_f = 0.35 \mu s$ (Max.)

• Low Saturation Voltage : VCE(sat)=4.0V (Max.)

• Enhancement-Mode

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERIST	SYMBOL	RATING	UNIT		
Collector-Emitter Voltage		v_{CES}	600	V	
Gate-Emitter Voltage	v_{GES}	±20	V		
Collector Current	DC	$I_{\mathbf{C}}$	15	A	
	1ms	I_{CP}	30		
Collector Power Dissipation (Tc=25°C)		PC	70	W	
Junction Temperature		T_{j}	150	°C	
Storage Temperature Range		$\mathrm{T_{stg}}$	-55~150	°C	

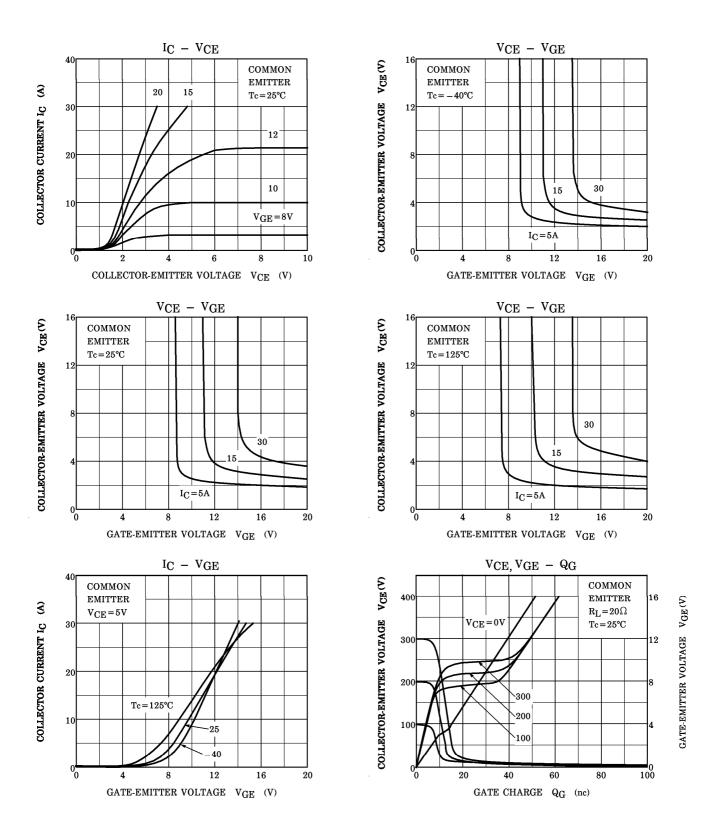


Weight: 1.4g

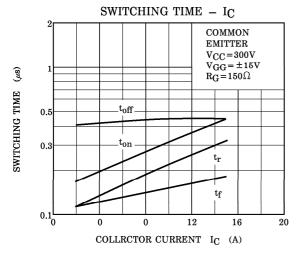
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

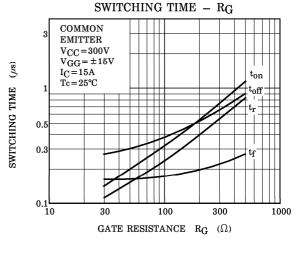
CHARA	CTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP	MAX.	UNIT
Gate Leakage Current		$I_{ ext{GES}}$	$V_{GE} = \pm 20V, V_{CE} = 0$	_	_	±500	nA
Collector Cut-off Current		ICES	$V_{CE} = 600V, V_{GE} = 0$	_	_	1.0	mA
Gate-Emitter	Cut-off Voltage	V _{GE} (OFF)	$I_C=5V, I_C=15mA$	3.0	_	6.0	V
Collector-Emitter Saturation Voltage		V _{CE} (sat)	$I_{C} = 15A, V_{GE} = 15V$	_	3.0	4.0	V
Input Capacitance		$\mathrm{c}_{\mathrm{ies}}$	$V_{CE} = 10V, V_{GE} = 0, f = 1MHz$	_	1100		pF
Switching Time	Rise Time	t_r	$\begin{array}{c c} V_{OUT} \\ V_{IN} \\ 0 \\ 150\Omega \end{array}$	_	0.3	0.6	
	Turn-on Time	ton		_	0.4	0.8	
	Fall Time	t_f		_	0.15	0.35	μ s
	Turn-off Time	$t_{ m off}$		_	0.5	1.0	

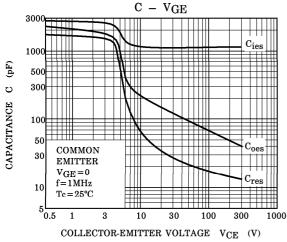
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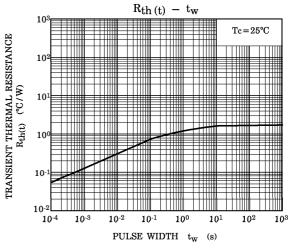


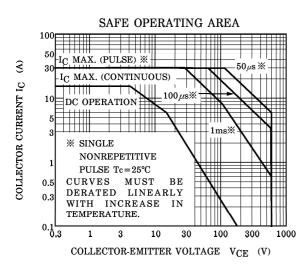
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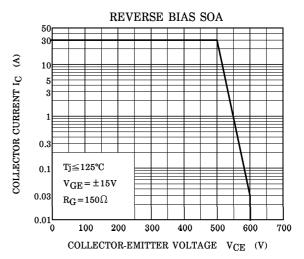












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